# SPEC® CINT2006 Result



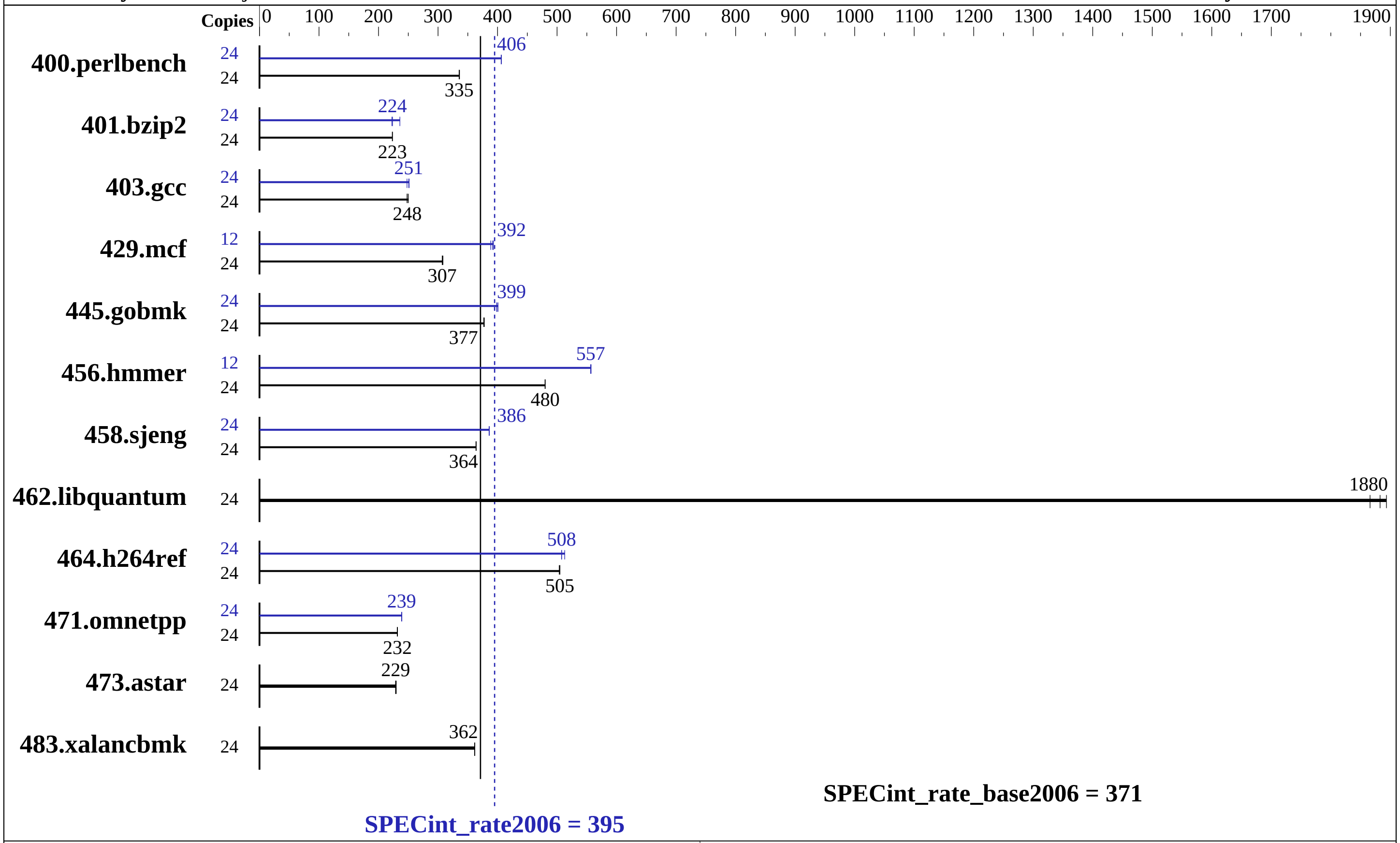
## Fujitsu

PRIMERGY RX300 S6, Intel Xeon X5675, 3.07 GHz

SPECint®_rate2006 = 395

SPECint_rate_base2006 = 371

| | | | |
|---|---|---|---|
| **CPU2006 license:** | 19 | **Test date:** | Dec-2010 |
| **Test sponsor:** | Fujitsu | **Hardware Availability:** | Feb-2011 |
| **Tested by:** | Fujitsu | **Software Availability:** | Nov-2010 |




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5675 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.47 GHz |
| CPU MHz: | 3067 |
| FPU: | Integrated |
| CPU(s) enabled: | 12 cores, 2 chips, 6 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 12 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC) |
| Disk Subsystem: | 1 x SAS, 300 GB, 10000 RPM |
| Other Hardware: | -- |

### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default |
| Compiler: | Intel C++ Compiler XE for applications running on IA-32 Version 12.0.0.082 Build 20101006 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V9.01 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 24 | 698 | 336 | 699 | 335 | **699** | **335** | 24 | 577 | 406 | 578 | 406 | **577** | **406** |
| 401.bzip2 | 24 | 1037 | 223 | 1038 | 223 | **1037** | **223** | 24 | 982 | 236 | 1041 | 223 | **1036** | **224** |
| 403.gcc | 24 | **778** | **248** | 772 | 250 | 779 | 248 | 24 | 779 | 248 | **770** | **251** | 769 | 251 |
| 429.mcf | 24 | 713 | 307 | 710 | 308 | **713** | **307** | 12 | 282 | 389 | **279** | **392** | 278 | 393 |
| 445.gobmk | 24 | 666 | 378 | 668 | 377 | **668** | **377** | 24 | **631** | **399** | 628 | 401 | 632 | 399 |
| 456.hmmer | 24 | **467** | **480** | 467 | 480 | 466 | 480 | 12 | 201 | 557 | 201 | 556 | **201** | **557** |
| 458.sjeng | 24 | **798** | **364** | 798 | 364 | 799 | 364 | 24 | 752 | 386 | 753 | 386 | **752** | **386** |
| 462.libquantum | 24 | 263 | 1890 | 267 | 1870 | **264** | **1880** | 24 | 263 | 1890 | 267 | 1870 | **264** | **1880** |
| 464.h264ref | 24 | 1055 | 504 | 1052 | 505 | **1052** | **505** | 24 | 1047 | 507 | 1036 | 513 | **1046** | **508** |
| 471.omnetpp | 24 | **647** | **232** | 648 | 231 | 647 | 232 | 24 | 628 | 239 | 627 | 239 | **628** | **239** |
| 473.astar | 24 | 733 | 230 | 737 | 229 | **736** | **229** | 24 | 733 | 230 | 737 | 229 | **736** | **229** |
| 483.xalancbmk | 24 | 458 | 361 | 458 | 362 | **458** | **362** | 24 | 458 | 361 | 458 | 362 | **458** | **362** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
Large pages were not enabled for this run
```

## Platform Notes

```
BIOS configuration:
Data Reuse Optimization = Disable
```

## General Notes

```
This result was measured on the PRIMERGY RX300 S6. The PRIMERGY TX300 S6
and the PRIMERGY RX300 S6 are electronically equivalent.

For information about Fujitsu please visit: http://www.fujitsu.com
Binaries were compiled on SLES 10 SP1 with Binutils 2.18.50.0.7.20080502
```

## Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

Continued on next page

## Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

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## Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-ansi-alias` `-opt-ra-region-strategy=block` `-Wl,-z,muldefs` `-L/smartheap` `-lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.xml




Tested with SPEC CPU2006 v1.1.
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