SPEC® CINT2006 Result

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Fujitsu

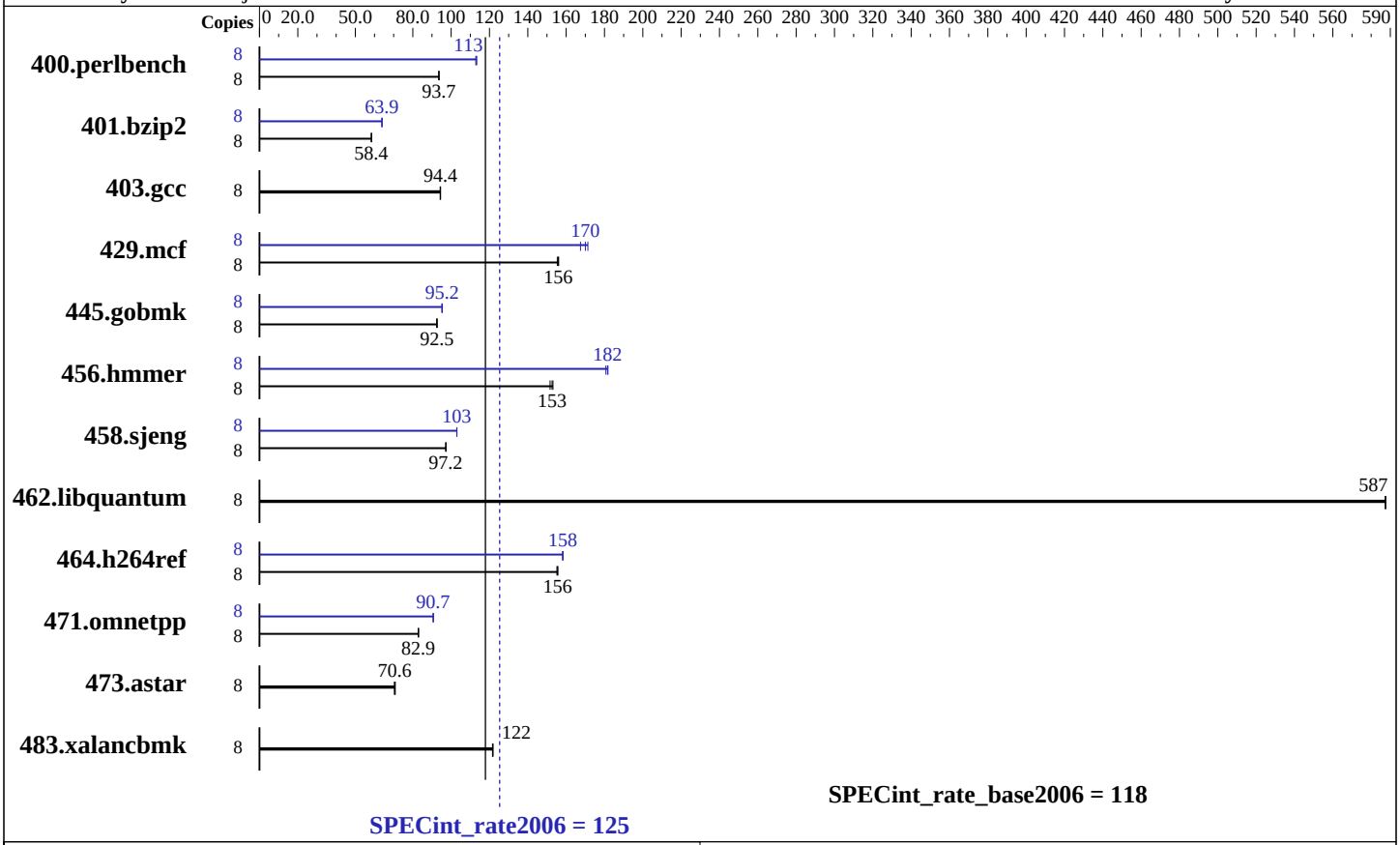
PRIMERGY BX920 S2, Intel Xeon E5603, 1.60 GHz

SPECint®_rate2006 = 125

SPECint_rate_base2006 = 118

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jan-2011
Hardware Availability: Feb-2011
Software Availability: Jan-2011



Hardware

CPU Name:	Intel Xeon E5603
CPU Characteristics:	
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (6 x 8 GB 2Rx4 PC3-10600R-9, ECC, running at 1067 MHz and CL7)
Disk Subsystem:	1 x SAS, 300 GB, 10000 RPM
Other Hardware:	--

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	836	93.5	834	93.7	**834**	**93.7**	8	693	113	690	113	**690**	**113**
401.bzip2	8	**1322**	**58.4**	1323	58.4	1321	58.5	8	**1208**	**63.9**	1208	63.9	1208	63.9
403.gcc	8	682	94.5	**682**	**94.4**	683	94.3	8	682	94.5	**682**	**94.4**	683	94.3
429.mcf	8	470	155	**468**	**156**	467	156	8	436	167	426	171	**429**	**170**
445.gobmk	8	908	92.5	**907**	**92.5**	905	92.7	8	882	95.2	**881**	**95.2**	879	95.4
456.hmmer	8	492	152	**489**	**153**	487	153	8	**411**	**182**	411	182	413	181
458.sjeng	8	**995**	**97.2**	996	97.2	995	97.3	8	**940**	**103**	940	103	940	103
462.libquantum	8	**282**	**587**	282	588	282	587	8	**282**	**587**	282	588	282	587
464.h264ref	8	1141	155	**1138**	**156**	1138	156	8	1118	158	1119	158	**1118**	**158**
471.omnetpp	8	603	82.9	603	83.0	**603**	**82.9**	8	551	90.7	552	90.7	**551**	**90.7**
473.astar	8	793	70.9	**796**	**70.6**	798	70.4	8	793	70.9	**796**	**70.6**	798	70.4
483.xalancbmk	8	453	122	454	122	**454**	**122**	8	453	122	454	122	**454**	**122**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
Hugepages were not configured on the system
```

Platform Notes

```
BIOS configuration:
Data Reuse Optimization = Disable
Performance/Power Setting = Traditional
```

General Notes

```
For information about Fujitsu please visit: http://www.fujitsu.com
Binaries were compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5
```

Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

Continued on next page

Peak Optimization Flags (Continued)

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.xml`

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For other inquiries, please contact webmaster@spec.org.

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