SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability of the system.

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name:</td>
<td>Operating System:</td>
</tr>
<tr>
<td>Intel Xeon E5607</td>
<td>SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default</td>
</tr>
<tr>
<td>CPU Characteristics:</td>
<td>Compiler:</td>
</tr>
<tr>
<td>8 cores, 2 chips, 4 cores/chip</td>
<td>Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64</td>
</tr>
<tr>
<td>CPU MHz:</td>
<td>Version:</td>
</tr>
<tr>
<td>2267</td>
<td>12.0 Update 3</td>
</tr>
<tr>
<td>FPU:</td>
<td>Auto Parallel:</td>
</tr>
<tr>
<td>Integrated</td>
<td>Yes</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>File System:</td>
</tr>
<tr>
<td>8 x 2 + 32 KB D on chip per core</td>
<td>ext3</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>System State:</td>
</tr>
<tr>
<td>1,2 chips</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>Base Pointers:</td>
</tr>
<tr>
<td>32 KB I+D on chip per core</td>
<td>64-bit</td>
</tr>
<tr>
<td>Secondary Cache:</td>
<td>Peak Pointers:</td>
</tr>
<tr>
<td>8 MB I+D on chip per chip</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>L3 Cache:</td>
<td>Other Software:</td>
</tr>
<tr>
<td>None</td>
<td>None</td>
</tr>
<tr>
<td>Other Cache:</td>
<td></td>
</tr>
<tr>
<td>Memory:</td>
<td></td>
</tr>
<tr>
<td>96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC, running at 1067 MHz)</td>
<td></td>
</tr>
<tr>
<td>Disk Subsystem:</td>
<td></td>
</tr>
<tr>
<td>1 x 300 GB 10 K SAS</td>
<td></td>
</tr>
<tr>
<td>Other Hardware:</td>
<td></td>
</tr>
<tr>
<td>None</td>
<td></td>
</tr>
</tbody>
</table>
SPEC CFP2006 Result

Hewlett-Packard Company

ProLiant DL380 G7
(2.27 GHz, Intel Xeon E5607)

SPECfp2006 = NC
SPECfp_base2006 = NC

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability of the system.

Results Table

<table>
<thead>
<tr>
<th>Benchmarks</th>
<th>Base Seconds</th>
<th>Ratio</th>
<th>Base Seconds</th>
<th>Ratio</th>
<th>Base Seconds</th>
<th>Ratio</th>
<th>Peak Seconds</th>
<th>Ratio</th>
<th>Peak Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>416.gamess</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>433.milc</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>444.namd</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>447.dealII</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>450.soplex</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>453.povray</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>454.calculix</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>459.GemsFDTS</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>465.tonto</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>470.libm</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>481.wrf</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>482幕墙</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'nodev /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
Hewlett-Packard Company
ProLiant DL380 G7
(2.27 GHz, Intel Xeon E5607)

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

SPECfp2006 = NC
SPECfp_base2006 = NC

SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability of the system.

**Platform Notes**

- BIOS configuration:
  - HP Power Profile set to Maximum Performance
  - Thermal Configuration set to Increased Cooling
  - Data Reuse set to Disabled

**General Notes**

Binaries were compiled on RHEL5.5

**Base Compiler Invocation**

- C benchmarks:
  - icc -m64

- C++ benchmarks:
  - icpc -m64

- Fortran benchmarks:
  - ifort -m64

- Benchmarks using both Fortran and C:
  - icc -m64 -ifort -m64

**Base Portability Flags**

- 410.bwaves: -DSPEC_CPU_LP64
- 416.gamess: -DSPEC_CPU_LP64
- 433.milc: -DSPEC_CPU_LP64
- 434.zeusmp: -DSPEC_CPU_LP64 -nofor_main
- 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
- 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
- 437.leslie3d: -DSPEC_CPU_LP64
- 444. namd: -DSPEC_CPU_LP64

Continued on next page
### SPEC CFP2006 Result

**Hewlett-Packard Company**  
ProLiant DL380 G7  
(2.27 GHz, Intel Xeon E5607)  

**SPECfp2006 = NC**  
**SPECfp_base2006 = NC**

<table>
<thead>
<tr>
<th>CPU2006 license: 3</th>
<th>Test date: Feb-2011</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test sponsor:</td>
<td>Hardware Availability: Feb-2011</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Software Availability: Apr-2011</td>
</tr>
</tbody>
</table>

**SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability of the system.**

#### Base Portability Flags (Continued)

447.dealII: -DSPEC_CPU_LP64  
450.soplex: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64  
465.tonto: -DSPEC_CPU_LP64  
470.lbm: -DSPEC_CPU_LP64  
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX  
482.sphinx3: -DSPEC_CPU_LP64

#### Base Optimization Flags

**C benchmarks:**  
-xSSE4.2 -ipo -03 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

**C++ benchmarks:**  
-xSSE4.2 -ipo -03 -no-prec-div -static -opt-prefetch -ansi-alias

**Fortran benchmarks:**  
-xSSE4.2 -ipo -03 -no-prec-div -static -parallel -opt-prefetch

**Benchmarks using both Fortran and C:**  
-ipo -03 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

#### Peak Compiler Invocation

**C benchmarks:**  
icc -m64

**C++ benchmarks:**  
icpc -m64

---

Continued on next page
SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability of the system.

Peak Compiler Invocation (Continued)

Fortran benchmarks:
  ifort -m64

Benchmarks using both Fortran and C:
  icc   -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
  433.milc: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
    -ansi-alias

  470.lbm: basepeak = yes

  482.sphinx: -xsse4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias
    -parallel

C++ benchmarks:
  444.namd: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
    -auto-ilp32

  447.dealII: basepeak = yes

  450.soplex: basepeak = yes

Non-Compliant
SPEC CFP2006 Result

Hewlett-Packard Company
ProLiant DL380 G7
(2.27 GHz, Intel Xeon E5607)

SPECfp2006 = NC
SPECfp_base2006 = NC

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability of the system.

Peak Optimization Flags (Continued)

453.povray: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -o3(pass 2)
-prec-div(pass 2) -prof-use(pass 2) -roll14 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xsSE4.2 -ipo -o3 -no-prec-div -opt-prefetch -parallel
-std.static

416.gameiss: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -o3(pass 2)
-prec-div(pass 2) -prof-use(pass 2) -unroll2
:inline-level=0 -scalar-rep- static

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes

459.GemsFDTD: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -o3(pass 2)
-prec-div(pass 2) -prof-use(pass 2) -unroll2
:inline-level=0 -opt-prefetch -parallel
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -o3(pass 2)
-prec-div(pass 2) -prof-use(pass 2) -inline-call
-opt-malloc-options=3 -auto -unroll4
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -o3(pass 2)
-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

436.cactusADM: basepeak = yes

454.calcui: -xsSE4.2 -ipo -o3 -no-prec-div -auto-ilp32 -ansi-alias

continued on next page
SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability of the system.

Peak Optimization Flags (Continued)
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20110316.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20110316.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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