Hewlett-Packard Company
ProLiant DL380 G7
(2.27 GHz, Intel Xeon E5607)

SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability of the system.
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Results Table

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used. numactl was used to bind copies to the cores

Non-Compliant
Hewlett-Packard Company
ProLiant DL380 G7
(2.27 GHz, Intel Xeon E5607)

**SPECint_rate2006 = NC**
**SPECint_rate_base2006 = NC**

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**SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability of the system.**

**Operating System Notes**

```
'nodev   /mnt/hugepages   hugetlbfs   defaults   0 0' added to /etc/fstab
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
echo 3600 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

**Platform Notes**

- **BIOS configuration:**
  - HP Power Profile set to Maximum Performance
  - Thermal Configuration set to Increased Cooling
  - Data Reuse set to Disabled

**General Notes**

- Binaries were compiled on RHEL5.5

**Base Compiler Invocation**

- C benchmarks:
  - `icc -m32`

- C++ benchmarks:

**Base Portability Flags**

- 400.perlbench: -DSPEC_CPU_LINUX_IA32
- 462.libquantum: -DSPEC_CPU_LINUX
- 483.xalancbmk: -DSPEC_CPU_LINUX
SPEC CINT2006 Result

Hewlett-Packard Company

ProLiant DL380 G7
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SPECint_rate_base2006 = NC

Hewlett-Packard Company

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Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32
400.perlbench: icc -m64
401.bzip2: icc -m64
56.hmmer: icc -m64
55.sjeng: icc -m64

C++ benchmarks:
icpc -m32
SPEC CINT2006 Result

Hewlett-Packard Company

ProLiant DL380 G7
(2.27 GHz, Intel Xeon E5607)

SPECint_rate2006 = NC
SPECint_rate_base2006 = NC

Hewlett-Packard Company

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Hardware Availability: Feb-2011
Software Availability: Dec-2010

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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc = yes
429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -auto-ilp32

45.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll4 -auto-ilp32
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Continued on next page
Hewlett-Packard Company
ProLiant DL380 G7
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SPECint_rate_base2006 = NC

CPU2006 license: 3
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Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes
464.h264ref:
-xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
-o3 (pass 2) -no-rec-div (pass 2) -prof-use (pass 2)
-unroll2 -ansi-alias

C++ benchmarks:
471.omnetpp:
-xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
-o3 (pass 2) -no-rec-div (pass 2) -prof-use (pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap
473.astar: basepeak = yes
483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:
-Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20110316.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20110316.xml
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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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