



SPEC® CINT2006 Result

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Intel Corporation

SPECint®2006 = 36.7

Intel DH67BLB3 Motherboard (Intel Core i3-2120)

SPECint_base2006 = 35.2

CPU2006 license: 13

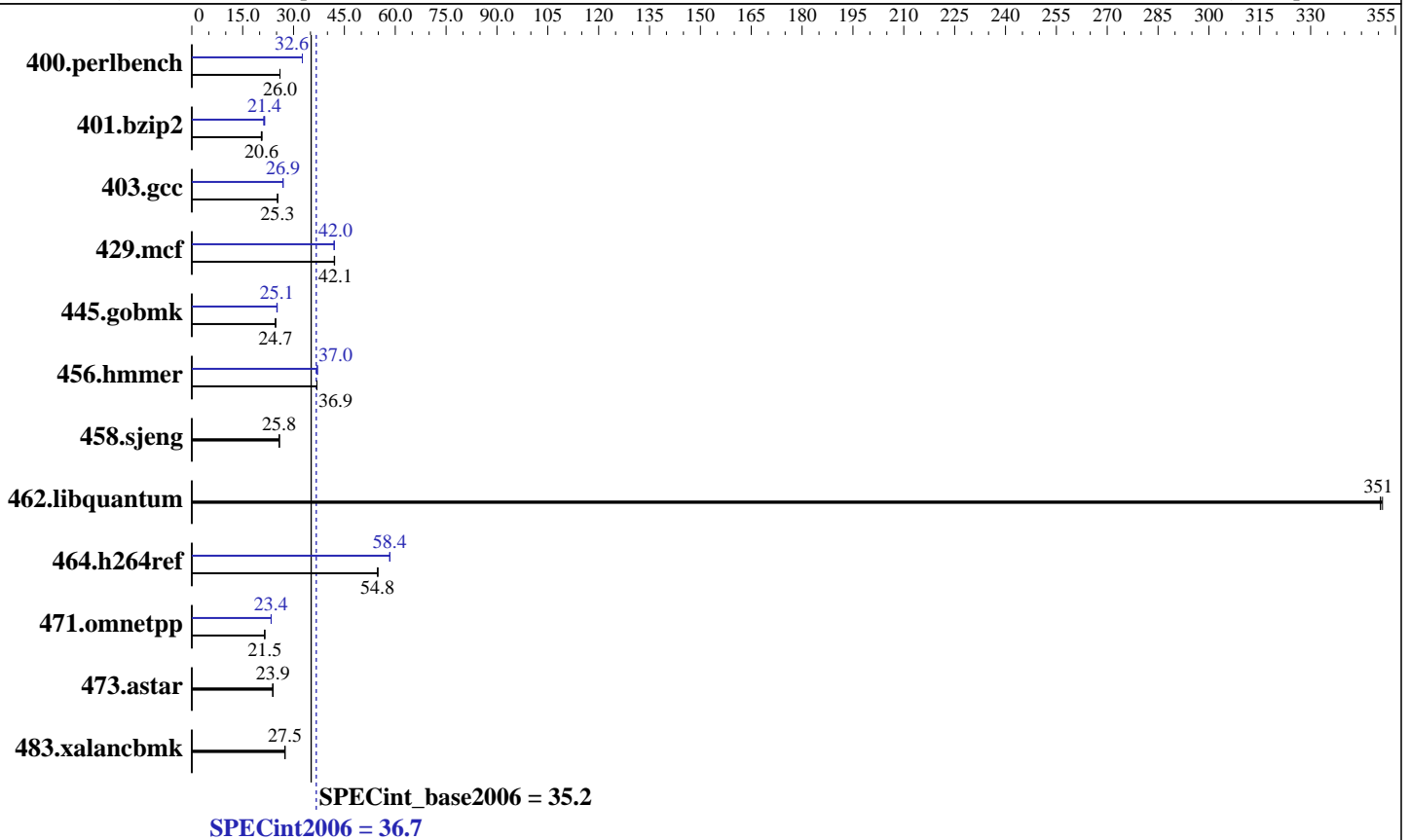
Test date: Apr-2011

Test sponsor: Intel Corporation

Hardware Availability: Mar-2011

Tested by: Intel Corporation

Software Availability: Apr-2011



Hardware

CPU Name: Intel Core i3-2120
 CPU Characteristics:
 CPU MHz: 3300
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 3 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600U-9)
 Disk Subsystem: Seagate 1 TB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Windows 7 Ultimate (64-bit)
 Compiler: Intel C++ Compiler XE for Intel64
 Version 12.0.3.163 Build 20110217
 Microsoft Visual Studio 2008 Professional SP1
 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap Library Version 9.01 from
<http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	376	26.0	375	26.0	<u>375</u>	<u>26.0</u>	300	32.6	<u>300</u>	<u>32.6</u>	300	32.6
401.bzip2	467	20.7	<u>468</u>	<u>20.6</u>	471	20.5	<u>450</u>	<u>21.4</u>	455	21.2	450	21.5
403.gcc	318	25.3	<u>318</u>	<u>25.3</u>	319	25.3	<u>300</u>	<u>26.9</u>	299	26.9	300	26.9
429.mcf	<u>217</u>	<u>42.1</u>	217	42.1	217	42.0	<u>217</u>	<u>42.0</u>	217	42.0	217	42.0
445.gobmk	425	24.7	<u>425</u>	<u>24.7</u>	425	24.7	418	25.1	416	25.2	<u>417</u>	<u>25.1</u>
456.hmmer	253	36.9	<u>253</u>	<u>36.9</u>	253	36.9	252	37.0	252	37.0	<u>252</u>	<u>37.0</u>
458.sjeng	<u>469</u>	<u>25.8</u>	469	25.8	469	25.8	<u>469</u>	<u>25.8</u>	469	25.8	469	25.8
462.libquantum	59.0	351	<u>59.1</u>	<u>351</u>	59.1	351	59.0	351	<u>59.1</u>	<u>351</u>	59.1	351
464.h264ref	403	54.9	<u>404</u>	<u>54.8</u>	404	54.8	379	58.4	<u>379</u>	<u>58.4</u>	379	58.4
471.omnetpp	291	21.5	<u>291</u>	<u>21.5</u>	291	21.5	267	23.4	<u>267</u>	<u>23.4</u>	268	23.4
473.astar	293	23.9	<u>294</u>	<u>23.9</u>	294	23.9	293	23.9	<u>294</u>	<u>23.9</u>	294	23.9
483.xalancbmk	251	27.5	251	27.4	<u>251</u>	<u>27.5</u>	251	27.5	251	27.4	<u>251</u>	<u>27.5</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

```

400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64
               -DSPEC_CPU_NO_NEED_VA_COPY
401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
429.mcf: -DSPEC_CPU_P64
445.gobmk: -DSPEC_CPU_P64
456.hmmer: -DSPEC_CPU_P64
458.sjeng: -DSPEC_CPU_P64

```

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Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption, cpp, --no_wchar_t_keyword

Base Optimization Flags

C benchmarks:
-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qauto-ilp32 /F512000000
C++ benchmarks:
-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features -Qauto-ilp32 /F512000000 shlW64M.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99
C++ benchmarks:
icl -Qvc9

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
400.perlbench: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch -Qauto-ilp32 /F512000000 shlW64M.lib -link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

401.bzip2: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qopt-prefetch -Qansi-alias -Qauto-ilp32
/F512000000

403.gcc: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qauto-ilp32 /F512000000

429.mcf: -QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F512000000

445.gobmk: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O2 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F512000000

456.hmmer: -QxAVX -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qauto-ilp32 /F512000000

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qauto-ilp32
/F512000000

C++ benchmarks:

471.omnetpp: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block -Qauto-ilp32 /F512000000
shlW64M.lib -link /FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.xml>

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For other inquiries, please contact webmaster@spec.org.

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