SPEC® CINT2006 Result

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Intel Corporation

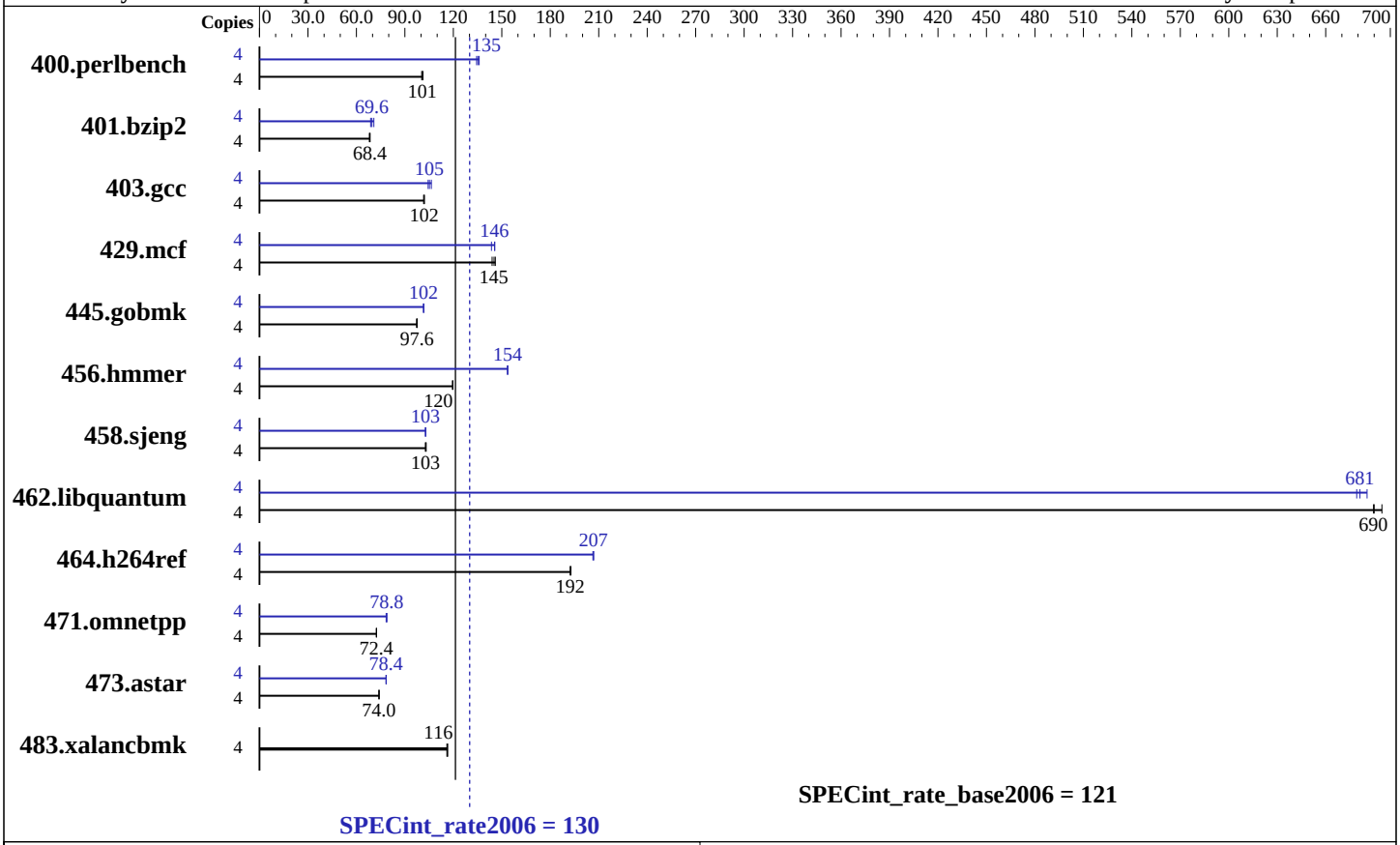
Intel DH67BLB3 Motherboard (Intel Core i5-2500)

SPECint®_rate2006 = 130

SPECint_rate_base2006 = 121

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Apr-2011
Hardware Availability: Mar-2011
Software Availability: Apr-2011



Hardware

CPU Name:	Intel Core i5-2500
CPU Characteristics:	Intel Turbo Boost Technology up to 3.7 GHz
CPU MHz:	3300
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	6 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (2 x 4 GB 2Rx8 PC3-10600U-9)
Disk Subsystem:	Seagate 1 TB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows 7 Ultimate (64-bit)
Compiler:	Intel C++ Compiler XE for IA32 and Intel 64 Version 12.0.3.163 Build 20110217 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap Library Version 9.01 from http://www.microquill.com/

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**388**	**101**	387	101	389	100	4	291	134	288	136	**289**	**135**
401.bzip2	4	564	68.4	**564**	**68.4**	569	68.0	4	561	68.8	**553**	**69.6**	547	70.8
403.gcc	4	315	102	**316**	**102**	317	102	4	**306**	**105**	308	104	303	106
429.mcf	4	250	146	**251**	**145**	253	144	4	254	144	250	146	**251**	**146**
445.gobmk	4	431	97.2	**431**	**97.6**	431	97.6	4	413	102	**413**	**102**	412	102
456.hmmer	4	312	120	**312**	**120**	312	120	4	**243**	**154**	243	154	243	154
458.sjeng	4	471	103	**470**	**103**	469	103	4	471	103	**471**	**103**	470	103
462.libquantum	4	**120**	**690**	119	695	120	690	4	**122**	**681**	121	686	122	679
464.h264ref	4	**460**	**192**	460	192	460	193	4	429	206	428	207	**428**	**207**
471.omnetpp	4	**345**	**72.4**	345	72.4	345	72.4	4	**318**	**78.8**	318	78.8	318	78.4
473.astar	4	379	74.0	379	74.0	**379**	**74.0**	4	358	78.4	359	78.4	**359**	**78.4**
483.xalancbmk	4	**238**	**116**	237	116	238	116	4	**238**	**116**	237	116	238	116

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
The start command with the /affinity switch was used to bind processes to cores
```

General Notes

```
Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DWIN32 -DSPEC_CPU_NO_INTTYPES`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
```
-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000
```

C++ benchmarks:
```
-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlW32M.lib            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icl -Qvc9 -Qstd=c99
```

456.hmmer:
```
C:/Program Files (x86)/Intel/ComposerXE-2011/bin/intel64/icl.exe
        -link -LIBPATH:C:/Program Files (x86)/Intel/ComposerXE-2011/compiler/lib/intel
        -link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib/AMD64
        -link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib
        -link -LIBPATH:C:/Program Files (x86)/Microsoft SDKs/Windows/v7.0A/lib/x64
```

458.sjeng:
```
C:/Program Files (x86)/Intel/ComposerXE-2011/bin/intel64/icl.exe
        -link -LIBPATH:C:/Program Files (x86)/Intel/ComposerXE-2011/compiler/lib/intel
        -link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib/AMD64
        -link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib
        -link -LIBPATH:C:/Program Files (x86)/Microsoft SDKs/Windows/v7.0A/lib/x64
```

462.libquantum:
```
C:/Program Files (x86)/Intel/ComposerXE-2011/bin/intel64/icl.exe
        -Qstd=c99
        -link -LIBPATH:C:/Program Files (x86)/Intel/ComposerXE-2011/compiler/lib/intel
        -link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib/AMD64
        -link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib
        -link -LIBPATH:C:/Program Files (x86)/Microsoft SDKs/Windows/v7.0A/lib/x64
```

C++ benchmarks (except as noted below):
```
icl -Qvc9
```

473.astar:
```
C:/Program Files (x86)/Intel/ComposerXE-2011/bin/intel64/icl.exe
        -link -LIBPATH:C:/Program Files (x86)/Intel/ComposerXE-2011/compiler/lib/intel
        -link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib/AMD64
        -link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib
        -link -LIBPATH:C:/Program Files (x86)/Microsoft SDKs/Windows/v7.0A/lib/x64
```

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
456.hmmer: -DSPEC_CPU_P64
458.sjeng: -DSPEC_CPU_P64
462.libquantum: -DSPEC_CPU_P64
464.h264ref: -DWIN32 -DSPEC_CPU_NO_INTTYPES
473.astar: -DSPEC_CPU_P64
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch /F512000000 shlW32M.lib -link /FORCE:MULTIPLE

401.bzip2: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias /F512000000

403.gcc: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

429.mcf: -QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

445.gobmk: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qauto-ilp32 -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

458.sjeng: -Qauto-ilp32 -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: -Qauto-ilp32 -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

464.h264ref: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block /F512000000 shlW32M.lib -link /FORCE:MULTIPLE

Continued on next page

Peak Optimization Flags (Continued)

473.astar: `-Qauto-ilp32 -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000 shlW64M.lib -link /FORCE:MULTIPLE`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12-win32-revB.html`
`http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12-win32-revB.xml`
`http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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