# SPEC® CINT2006 Result

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## Intel Corporation

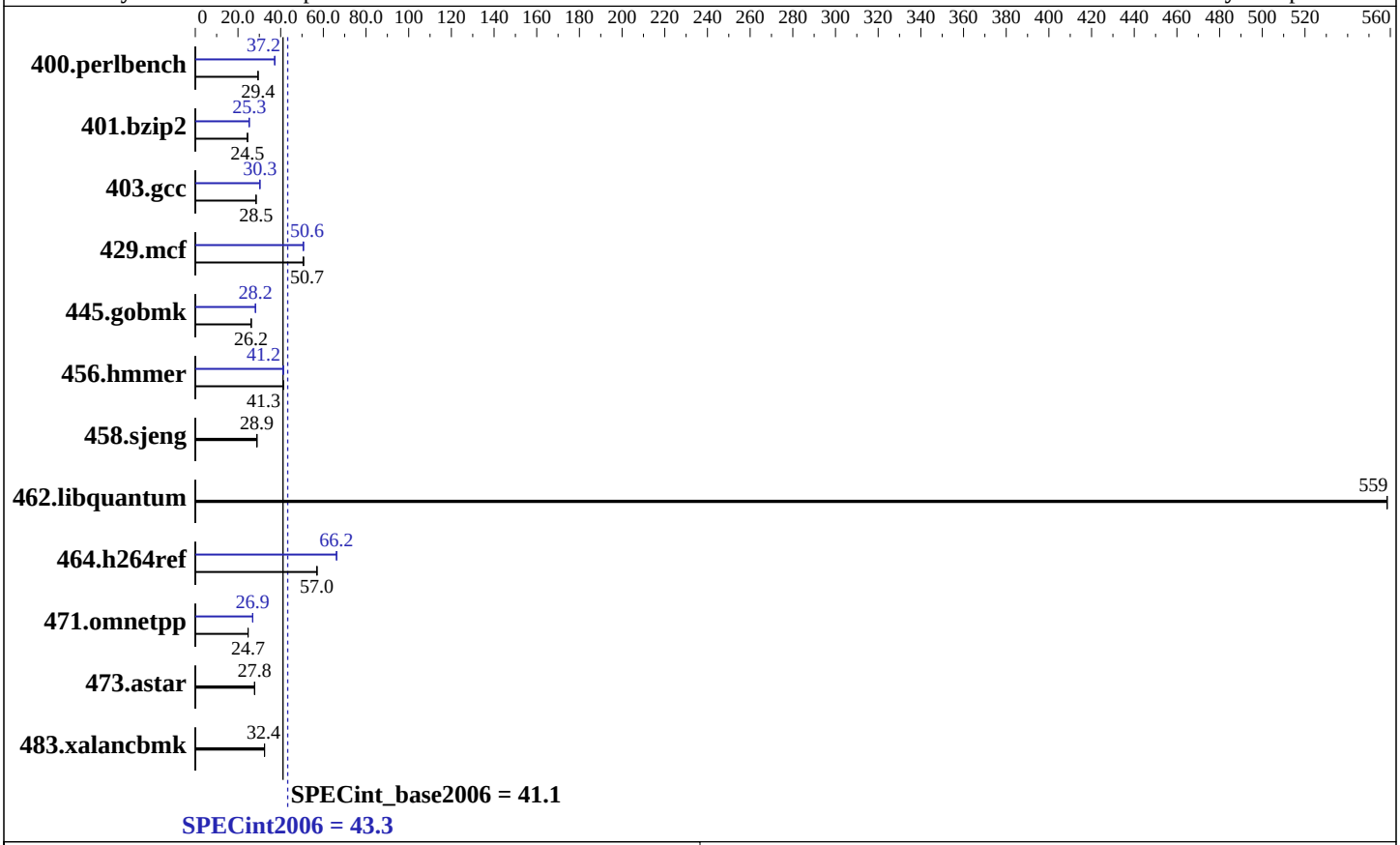
Intel DH67BLB3 Motherboard (Intel Core i5-2500)

SPECint®2006 = 43.3

SPECint_base2006 = 41.1

**CPU2006 license:** 13
**Test sponsor:** Intel Corporation
**Tested by:** Intel Corporation

**Test date:** Apr-2011
**Hardware Availability:** Mar-2011
**Software Availability:** Apr-2011




### Hardware

| | |
|---|---|
| CPU Name: | Intel Core i5-2500 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.7 GHz |
| CPU MHz: | 3300 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 1 chip, 4 cores/chip |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 6 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 8 GB (2 x 4 GB 2Rx8 PC3-10600U-9) |
| Disk Subsystem: | Seagate 1 TB SATA, 7200 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Windows 7 Ultimate (64-bit) |
| Compiler: | Intel C++ Compiler XE for Intel64<br>Version 12.0.3.163 Build 20110217<br>Microsoft Visual Studio 2008 Professional SP1 (for libraries) |
| Auto Parallel: | Yes |
| File System: | NTFS |
| System State: | Default |
| Base Pointers: | 32/64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | SmartHeap Library Version 9.01 from http://www.microquill.com/ |

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## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | **332** | **29.4** | 332 | 29.4 | 332 | 29.4 | 262 | 37.2 | **262** | **37.2** | 262 | 37.3 |
| 401.bzip2 | 394 | 24.5 | **395** | **24.5** | 396 | 24.4 | **381** | **25.3** | 383 | 25.2 | 381 | 25.3 |
| 403.gcc | **283** | **28.5** | 283 | 28.5 | 283 | 28.4 | 266 | 30.3 | 266 | 30.2 | **266** | **30.3** |
| 429.mcf | 180 | 50.8 | **180** | **50.7** | 181 | 50.5 | 180 | 50.8 | **180** | **50.6** | 181 | 50.5 |
| 445.gobmk | **401** | **26.2** | 400 | 26.2 | 401 | 26.2 | 372 | 28.2 | **372** | **28.2** | 373 | 28.2 |
| 456.hmmer | 226 | 41.3 | **226** | **41.3** | 226 | 41.2 | **226** | **41.2** | 226 | 41.2 | 226 | 41.3 |
| 458.sjeng | **419** | **28.9** | 419 | 28.9 | 420 | 28.8 | **419** | **28.9** | 419 | 28.9 | 420 | 28.8 |
| 462.libquantum | 37.1 | 559 | **37.1** | **559** | 37.1 | 559 | 37.1 | 559 | **37.1** | **559** | 37.1 | 559 |
| 464.h264ref | 388 | 57.1 | **388** | **57.0** | 389 | 56.9 | 334 | 66.2 | **334** | **66.2** | 334 | 66.2 |
| 471.omnetpp | 252 | 24.8 | **253** | **24.7** | 253 | 24.7 | 233 | 26.9 | **233** | **26.9** | 233 | 26.9 |
| 473.astar | 252 | 27.8 | 253 | 27.8 | **252** | **27.8** | 252 | 27.8 | 253 | 27.8 | **252** | **27.8** |
| 483.xalancbmk | 212 | 32.5 | **213** | **32.4** | 213 | 32.4 | 212 | 32.5 | **213** | **32.4** | 213 | 32.4 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

```
Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter
```

## Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64 -DSPEC_CPU_NO_NEED_VA_COPY`
401.bzip2: `-DSPEC_CPU_P64`
403.gcc: `-DSPEC_CPU_P64 -DSPEC_CPU_WIN64`
429.mcf: `-DSPEC_CPU_P64`
445.gobmk: `-DSPEC_CPU_P64`
456.hmmer: `-DSPEC_CPU_P64`
458.sjeng: `-DSPEC_CPU_P64`

Continued on next page

## Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption,cpp,--no_wchar_t_keyword

## Base Optimization Flags

C benchmarks:
-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qauto-ilp32 /F512000000

C++ benchmarks:
-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features -Qauto-ilp32 /F512000000 shlW64M.lib -link /FORCE:MULTIPLE

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

400.perlbench: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch -Qauto-ilp32 /F512000000 shlW64M.lib -link /FORCE:MULTIPLE

Continued on next page

# Peak Optimization Flags (Continued)

401.bzip2: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qopt-prefetch -Qansi-alias -Qauto-ilp32
/F512000000

403.gcc: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qauto-ilp32 /F512000000

429.mcf: -QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F512000000

445.gobmk: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O2 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F512000000

456.hmmer: -QxAVX -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qauto-ilp32 /F512000000

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qauto-ilp32
/F512000000

C++ benchmarks:

471.omnetpp: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block -Qauto-ilp32 /F512000000
shlW64M.lib -link /FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

# Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.html
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.html

You can also download the XML flags sources by saving the following links:

`http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.xml`
`http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.xml`


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For other inquiries, please contact webmaster@spec.org.

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