SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

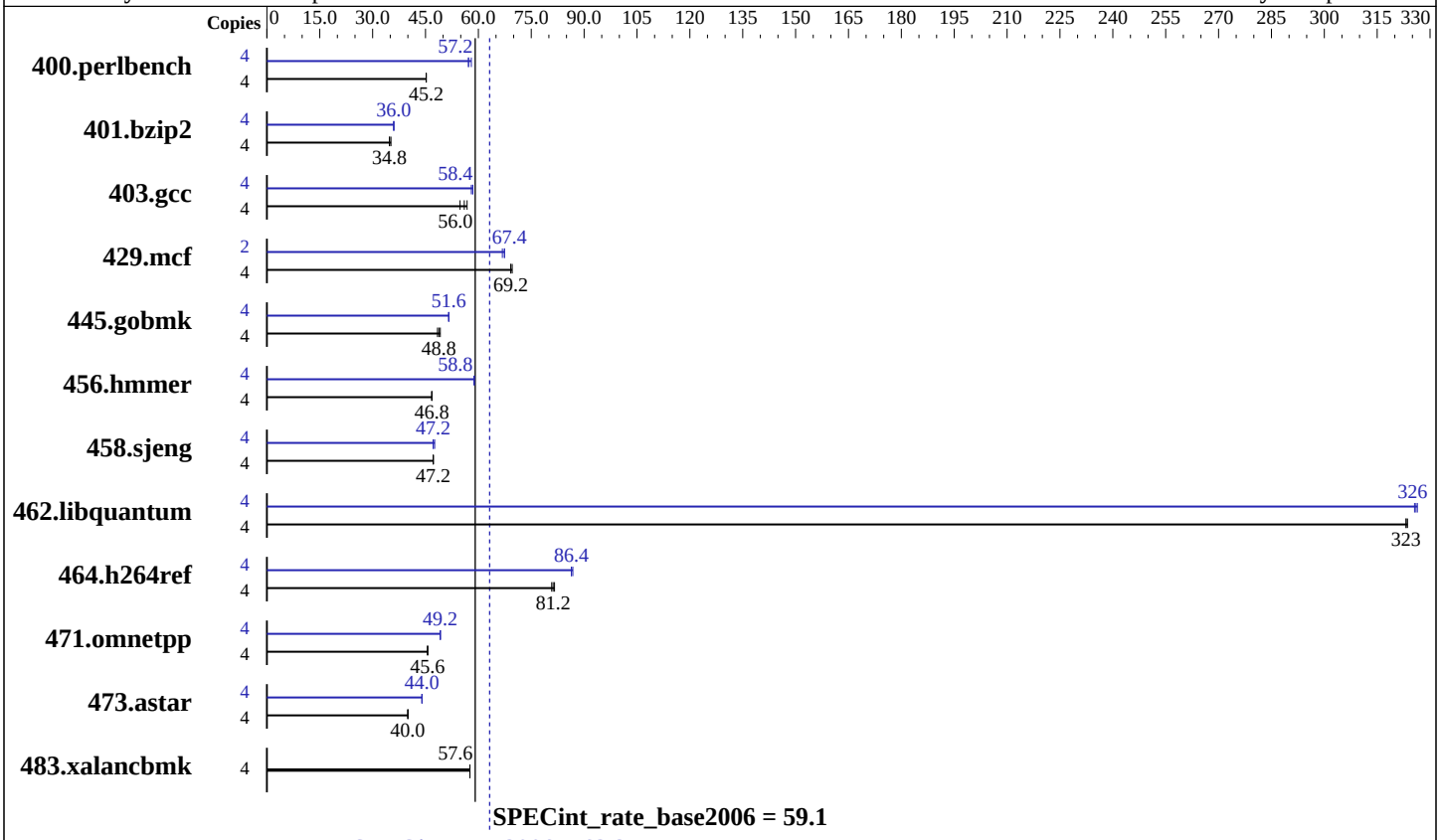
Intel DH67BLB3 Motherboard (Intel Core i3-2100T)

SPECint®_rate2006 = 63.2

SPECint_rate_base2006 = 59.1

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Apr-2011
Hardware Availability: Mar-2011
Software Availability: Apr-2011



Hardware

CPU Name:	Intel Core i3-2100T
CPU Characteristics:	
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	3 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (2 x 4 GB 2Rx8 PC3-10600U-9)
Disk Subsystem:	Seagate 1 TB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows 7 Ultimate (64-bit)
Compiler:	Intel C++ Compiler XE for IA32 and Intel 64 Version 12.0.3.163 Build 20110217 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap Library Version 9.01 from http://www.microquill.com/

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

Intel DH67BLB3 Motherboard (Intel Core i3-2100T)

SPECint_rate2006 = 63.2

SPECint_rate_base2006 = 59.1

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Apr-2011
Hardware Availability: Mar-2011
Software Availability: Apr-2011

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	864	45.2	862	45.2	**862**	**45.2**	4	**684**	**57.2**	685	57.2	676	58.0
401.bzip2	4	1102	35.2	**1104**	**34.8**	1106	34.8	4	**1068**	**36.0**	1069	36.0	1067	36.0
403.gcc	4	**575**	**56.0**	568	56.8	588	54.8	4	**553**	**58.4**	557	58.0	553	58.4
429.mcf	4	**527**	**69.2**	528	69.2	526	69.6	2	271	67.4	273	66.8	**271**	**67.4**
445.gobmk	4	**861**	**48.8**	865	48.4	854	49.2	4	811	51.6	814	51.6	**814**	**51.6**
456.hmmer	4	797	46.8	**797**	**46.8**	796	46.8	4	635	58.8	**636**	**58.8**	637	58.8
458.sjeng	4	1027	47.2	**1025**	**47.2**	1024	47.2	4	1025	47.2	1018	47.6	**1022**	**47.2**
462.libquantum	4	**256**	**323**	256	323	256	324	4	254	326	**254**	**326**	254	326
464.h264ref	4	**1090**	**81.2**	1094	80.8	1087	81.6	4	1021	86.8	1023	86.4	**1022**	**86.4**
471.omnetpp	4	**549**	**45.6**	549	45.6	547	45.6	4	507	49.2	508	49.2	**508**	**49.2**
473.astar	4	701	40.0	**701**	**40.0**	700	40.0	4	639	44.0	640	44.0	**640**	**44.0**
483.xalancbmk	4	480	57.6	**480**	**57.6**	479	57.6	4	480	57.6	**480**	**57.6**	479	57.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
The start command with the /affinity switch was used to bind processes to cores
```

General Notes

```
Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DWIN32 -DSPEC_CPU_NO_INTTYPES`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
```
-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000
```

C++ benchmarks:
```
-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlW32M.lib            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icl -Qvc9 -Qstd=c99
```

456.hmmer: `C:/Program Files (x86)/Intel/ComposerXE-2011/bin/intel64/icl.exe`

458.sjeng: `C:/Program Files (x86)/Intel/ComposerXE-2011/bin/intel64/icl.exe`

462.libquantum: `C:/Program Files (x86)/Intel/ComposerXE-2011/bin/intel64/icl.exe -Qstd=c99`

C++ benchmarks (except as noted below):
```
icl -Qvc9
```

473.astar: `C:/Program Files (x86)/Intel/ComposerXE-2011/bin/intel64/icl.exe`

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
456.hmmer: `-DSPEC_CPU_P64`
458.sjeng: `-DSPEC_CPU_P64`
462.libquantum: `-DSPEC_CPU_P64`
464.h264ref: `-DWIN32 -DSPEC_CPU_NO_INTTYPES`
473.astar: `-DSPEC_CPU_P64`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Peak Optimization Flags

C benchmarks:

400.perlbench: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch /F512000000 shlW32M.lib -link /FORCE:MULTIPLE

401.bzip2: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias /F512000000

403.gcc: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

429.mcf: -QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

445.gobmk: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qauto-ilp32 -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

458.sjeng: -Qauto-ilp32 -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: -Qauto-ilp32 -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

464.h264ref: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block /F512000000 shlW32M.lib -link /FORCE:MULTIPLE

473.astar: -Qauto-ilp32 -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000 shlW64M.lib -link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page

Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

456.hmmer: -link -LIBPATH:C:/Program Files (x86)/Intel/ComposerXE-2011/compiler/lib/intel64
-link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib/AMD64
-link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib
-link -LIBPATH:C:/Program Files (x86)/Microsoft SDKs/Windows/v7.0A/lib/x64

458.sjeng: -link -LIBPATH:C:/Program Files (x86)/Intel/ComposerXE-2011/compiler/lib/intel64
-link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib/AMD64
-link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib
-link -LIBPATH:C:/Program Files (x86)/Microsoft SDKs/Windows/v7.0A/lib/x64

462.libquantum: -link -LIBPATH:C:/Program Files (x86)/Intel/ComposerXE-2011/compiler/lib/intel64
-link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib/AMD64
-link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib
-link -LIBPATH:C:/Program Files (x86)/Microsoft SDKs/Windows/v7.0A/lib/x64

C++ benchmarks:

473.astar: -link -LIBPATH:C:/Program Files (x86)/Intel/ComposerXE-2011/compiler/lib/intel64
-link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib/AMD64
-link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib
-link -LIBPATH:C:/Program Files (x86)/Microsoft SDKs/Windows/v7.0A/lib/x64

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12-win32-revB.20110511.html
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12-win32-revB.20110511.xml
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.xml

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 21:13:06 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 10 May 2011.