SPEC® CINT2006 Result

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Dell Inc.

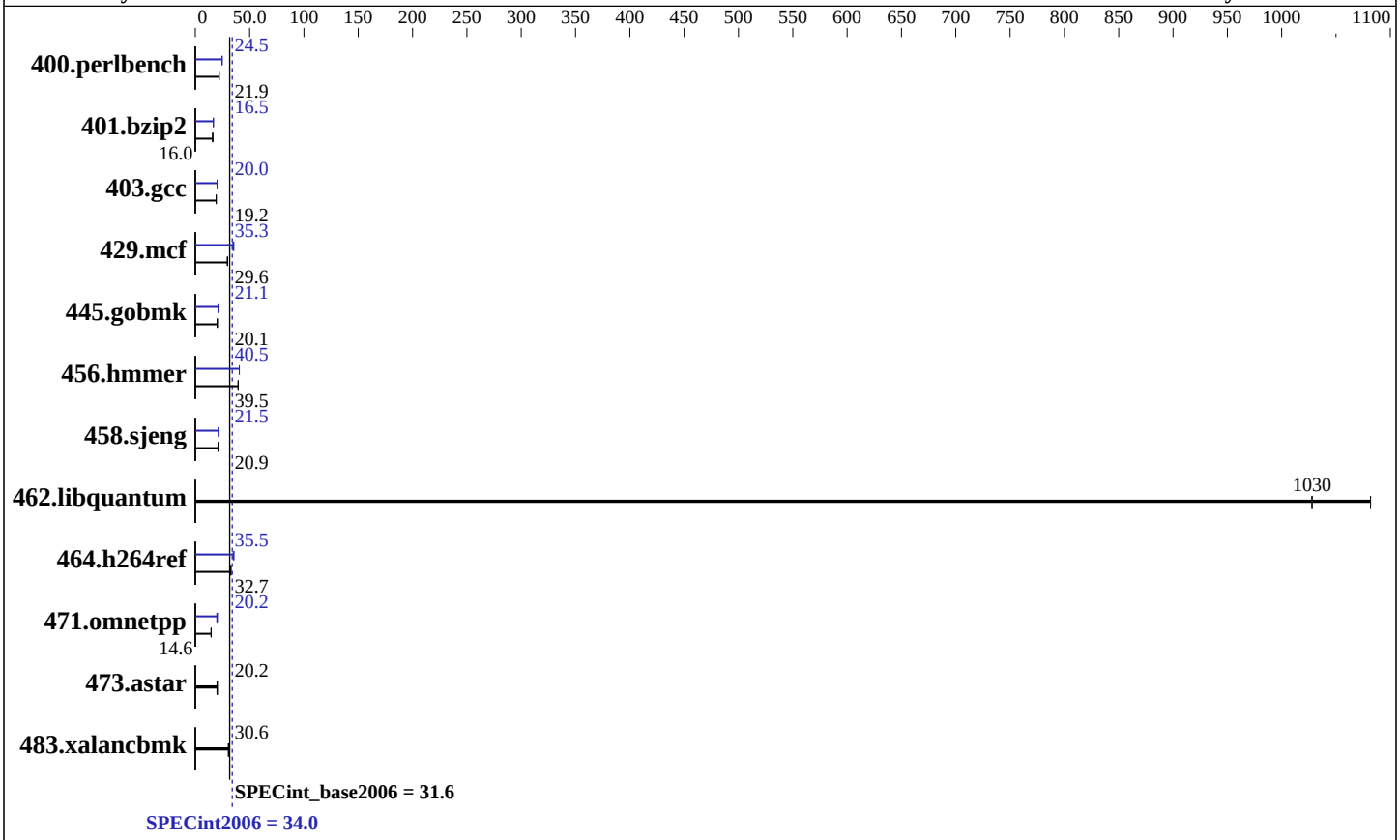
PowerEdge R910 (Intel Xeon X7542, 2.67 GHz)

SPECint®2006 = 34.0

SPECint_base2006 = 31.6

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Apr-2011
Hardware Availability: Jul-2011
Software Availability: Jan-2011



Hardware

CPU Name:	Intel Xeon X7542
CPU Characteristics:	Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	24 cores, 4 chips, 6 cores/chip
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	18 MB I+D on chip per chip
Other Cache:	None
Memory:	512 GB (64 x 8 GB 4Rx8 PC3-8500R-7, ECC, running at 978 MHz)
Disk Subsystem:	1 x 500 GB 7200 RPM SAS 6Gb
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	446	21.9	**446**	**21.9**	444	22.0	392	24.9	**399**	**24.5**	400	24.5
401.bzip2	603	16.0	**602**	**16.0**	602	16.0	575	16.8	**584**	**16.5**	584	16.5
403.gcc	**419**	**19.2**	419	19.2	419	19.2	**402**	**20.0**	402	20.0	402	20.0
429.mcf	307	29.7	**309**	**29.6**	309	29.6	**258**	**35.3**	258	35.4	264	34.6
445.gobmk	522	20.1	**522**	**20.1**	514	20.4	497	21.1	497	21.1	**497**	**21.1**
456.hmmer	**236**	**39.5**	236	39.5	236	39.5	230	40.5	231	40.5	**230**	**40.5**
458.sjeng	579	20.9	**579**	**20.9**	578	20.9	563	21.5	573	21.1	**564**	**21.5**
462.libquantum	**20.2**	**1030**	20.2	1030	19.2	1080	**20.2**	**1030**	20.2	1030	19.2	1080
464.h264ref	675	32.8	679	32.6	**676**	**32.7**	**624**	**35.5**	635	34.9	624	35.5
471.omnetpp	430	14.5	**428**	**14.6**	427	14.6	**309**	**20.2**	309	20.2	316	19.8
473.astar	**347**	**20.2**	352	19.9	346	20.3	**347**	**20.2**	352	19.9	346	20.3
483.xalancbmk	222	31.1	**225**	**30.6**	227	30.4	222	31.1	**225**	**30.6**	227	30.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

```
BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)
```

General Notes

```
The Dell PowerEdge R910 and
the Bull NovaScale R480 F2 models are electronically equivalent.
The results have been measured on a Dell PowerEdge R910 model.
 OMP_NUM_THREADS set to number of cores
 Binaries were compiled on RHEL5.5
```

Base Compiler Invocation

C benchmarks:

```
icc  -m64
```

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap64
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32

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Peak Compiler Invocation (Continued)

429.mcf: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

471.omnetpp: `icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-opt-prefetch -ansi-alias`
`-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

401.bzip2: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div -prof-use`(pass 2) `-auto-ilp32`
`-opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc`
`-opt-malloc-options=3 -auto-ilp32`
`-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

429.mcf: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-auto-ilp32 -ansi-alias`
`-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

445.gobmk: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-prof-use`(pass 2)
`-auto-ilp32 -ansi-alias`
`-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

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Peak Optimization Flags (Continued)

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml

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