SPEC® CINT2006 Result

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Acer Incorporated

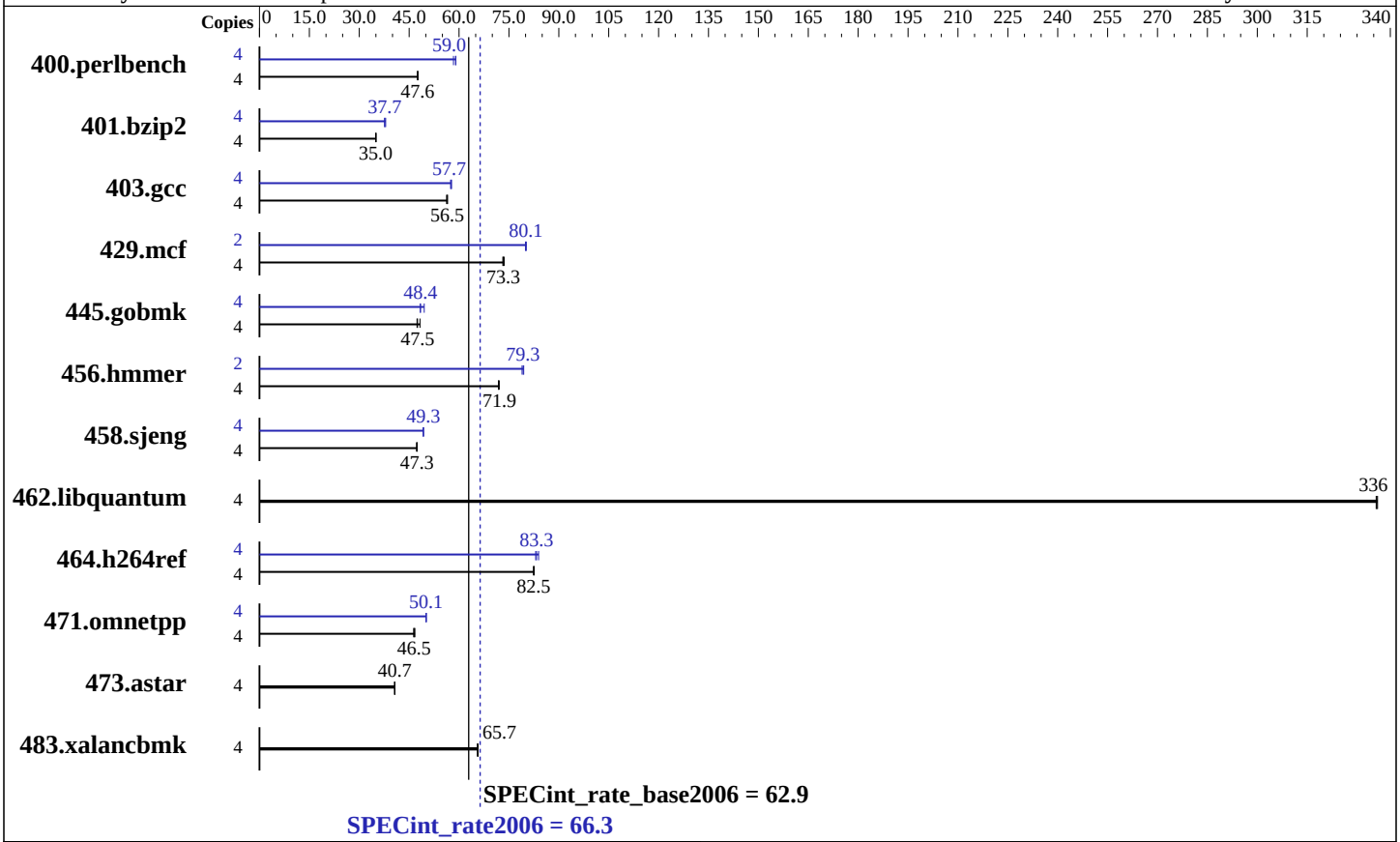
Acer AT110 F2 (Xeon E3-1220L)

SPECint®_rate2006 = 66.3

SPECint_rate_base2006 = 62.9

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Mar-2011
Hardware Availability: Apr-2011
Software Availability: Jan-2011



Hardware

CPU Name:	Intel Xeon E3-1220L
CPU Characteristics:	Intel Turbo Boost Technology up to 3.40 GHz
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	3 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (4 x 4 GB 2Rx8 PC3-10600E-9,ECC)
Disk Subsystem:	1 x 640 GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**822**	**47.6**	820	47.7	823	47.5	4	662	59.0	670	58.3	**663**	**59.0**
401.bzip2	4	**1103**	**35.0**	1104	34.9	1101	35.1	4	**1024**	**37.7**	1027	37.6	1017	37.9
403.gcc	4	572	56.2	**570**	**56.5**	570	56.5	4	**558**	**57.7**	560	57.5	558	57.7
429.mcf	4	**498**	**73.3**	496	73.6	499	73.2	2	228	80.0	**228**	**80.1**	227	80.2
445.gobmk	4	886	47.4	869	48.3	**883**	**47.5**	4	847	49.5	867	48.4	**867**	**48.4**
456.hmmer	4	**519**	**71.9**	518	72.1	520	71.8	2	236	78.9	**235**	**79.3**	235	79.4
458.sjeng	4	1021	47.4	1025	47.2	**1023**	**47.3**	4	**982**	**49.3**	982	49.3	984	49.2
462.libquantum	4	**247**	**336**	247	336	247	336	4	**247**	**336**	247	336	247	336
464.h264ref	4	1072	82.6	1074	82.4	**1073**	**82.5**	4	1055	83.9	1066	83.0	**1063**	**83.3**
471.omnetpp	4	535	46.7	**538**	**46.5**	539	46.4	4	**499**	**50.1**	498	50.2	500	50.0
473.astar	4	**690**	**40.7**	692	40.6	690	40.7	4	**690**	**40.7**	692	40.6	690	40.7
483.xalancbmk	4	420	65.7	**420**	**65.7**	421	65.5	4	420	65.7	**420**	**65.7**	421	65.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
Large pages were disabled for this run
```

General Notes

```
Binaries compiled on RHEL5.5
This result was measured on the Gateway GT110 F2.
The Acer AT110 F2 is electronically equivalent.
```

Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

C++ benchmarks:

```
icpc -m32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

C++ benchmarks:
`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs -L/smartheap -lsmartheap -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`

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Peak Portability Flags (Continued)

456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xAVX -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
-auto-ilp32

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

Continued on next page

Peak Optimization Flags (Continued)

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.20110608.html`
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.20110608.xml`
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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