SPEC® CINT2006 Result

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Fujitsu

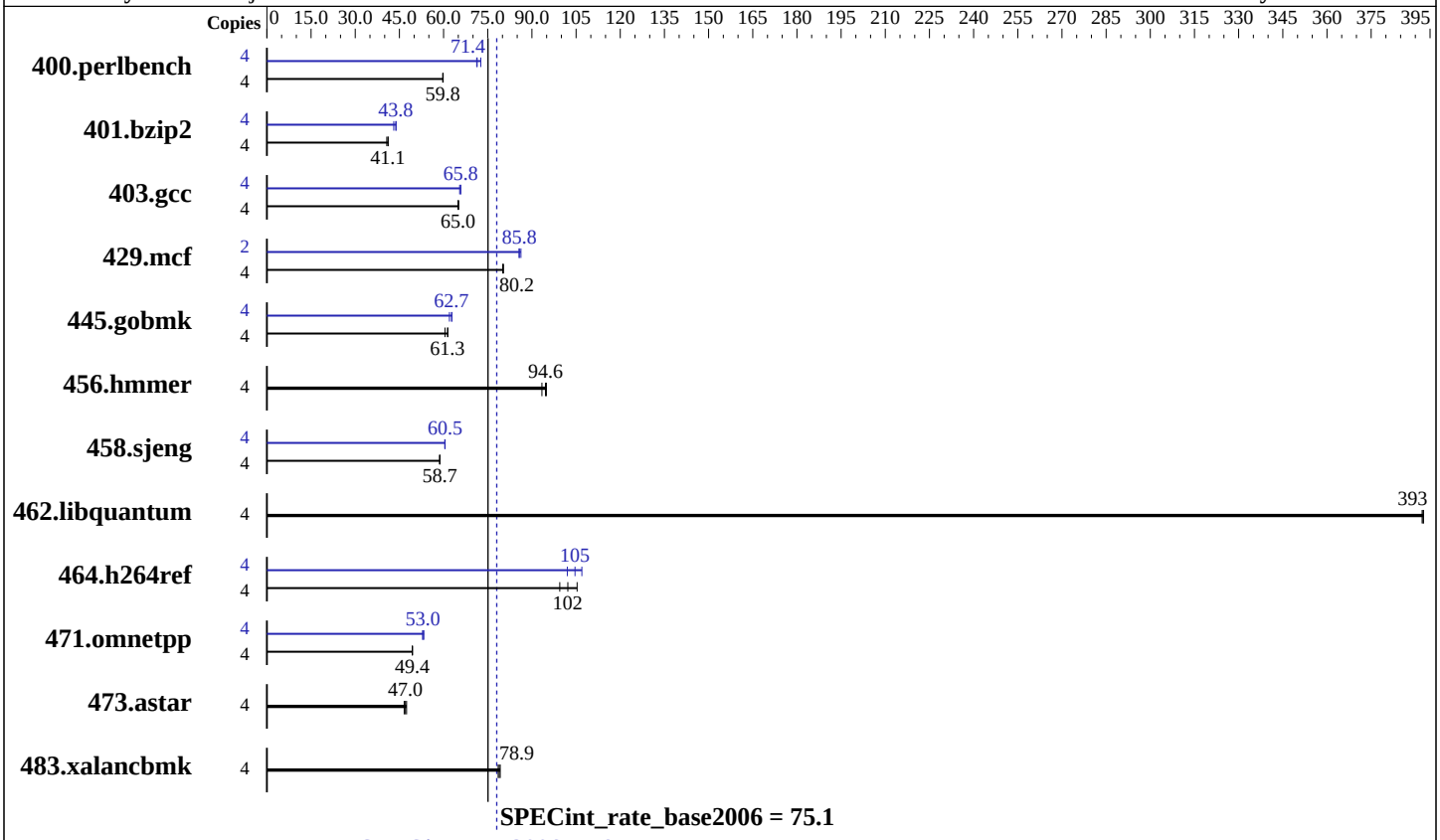
PRIMERGY TX120 S3, Intel Core i3-2100, 3.10 GHz

SPECint®_rate2006 = 78.1

SPECint_rate_base2006 = 75.1

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2011
Hardware Availability: Jun-2011
Software Availability: Jan-2011



Hardware

CPU Name:	Intel Core i3-2100
CPU Characteristics:	
CPU MHz:	3100
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	3 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)
Disk Subsystem:	1 x SATA, 300 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**654**	**59.8**	653	59.9	654	59.7	4	538	72.6	548	71.3	**547**	**71.4**
401.bzip2	4	**939**	**41.1**	938	41.2	948	40.7	4	880	43.9	896	43.1	**882**	**43.8**
403.gcc	4	494	65.1	496	64.9	**496**	**65.0**	4	492	65.5	**489**	**65.8**	489	65.8
429.mcf	4	454	80.3	455	80.1	**455**	**80.2**	2	213	85.6	211	86.3	**213**	**85.8**
445.gobmk	4	694	60.5	**684**	**61.3**	683	61.5	4	677	62.0	**669**	**62.7**	668	62.8
456.hmmer	4	**394**	**94.6**	393	94.9	400	93.4	4	**394**	**94.6**	393	94.9	400	93.4
458.sjeng	4	825	58.7	**824**	**58.7**	824	58.7	4	801	60.4	**800**	**60.5**	800	60.5
462.libquantum	4	**211**	**393**	211	393	211	392	4	**211**	**393**	211	393	211	392
464.h264ref	4	840	105	**866**	**102**	890	99.5	4	**846**	**105**	828	107	868	102
471.omnetpp	4	506	49.4	506	49.4	**506**	**49.4**	4	**472**	**53.0**	473	52.9	469	53.3
473.astar	4	602	46.6	**597**	**47.0**	592	47.4	4	602	46.6	**597**	**47.0**	592	47.4
483.xalancbmk	4	**350**	**78.9**	348	79.2	351	78.5	4	**350**	**78.9**	348	79.2	351	78.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 Large pages were not enabled for this run
```

General Notes

```
For information about Fujitsu please visit: http://www.fujitsu.com
Binaries were compiled on RHEL5.5
This result was measured on the PRIMERGY TX140 S1. The PRIMERGY TX120 S3
and the PRIMERGY TX140 S1 are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

C++ benchmarks:

```
icpc -m32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-xAVX -ipo -O3 -no-prec-div -opt-prefetch`
`-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

C++ benchmarks:
`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs`
`-L/smartheap -lsmartheap`
`-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xAVX -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
-auto-ilp32

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmer: basepeak = yes

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

473.astar: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html`
`http://www.spec.org/cpu2006/flags/Fujitsu-Platform.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml`
`http://www.spec.org/cpu2006/flags/Fujitsu-Platform.xml`

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For other inquiries, please contact webmaster@spec.org.

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