Fujitsu

PRIMERGY TX140 S1, Intel Core i3-2100, 3.10 GHz

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</table>

Hardware

CPU Name: Intel Core i3-2100
CPU Characteristics:
CPU MHz: 3100
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64
Version 12.0.1.116 Build 20101116
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
**SPEC CFP2006 Result**

**Fujitsu**

PRIMERGY TX140 S1, Intel Core i3-2100, 3.10 GHz

| CPU2006 license: | 19 |
| Test sponsor:    | Fujitsu |
| Tested by:       | Fujitsu |

| L3 Cache: | 3 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC) |
| Disk Subsystem: | 1 x SATA, 300 GB, 7200 RPM |
| Other Hardware: | None |

**Peak Pointers:** 32/64-bit  
**Other Software:** None

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Submit Notes**

The config file option 'submit' was used. numactl was used to bind copies to the cores

**Operating System Notes**

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run Large pages were not enabled for this run

**General Notes**

For information about Fujitsu please visit: http://www.fujitsu.com

Binaries were compiled on RHEL5.5

This result was measured on the PRIMERGY TX140 S1. The PRIMERGY TX120 S3

Continued on next page
Fujitsu
PRIMERGY TX140 S1, Intel Core i3-2100, 3.10 GHz

SPECfp_rate2006 = 74.5
SPECfp_rate_base2006 = 73.1

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu
Test date: May-2011
Hardware Availability: Jun-2011
Software Availability: Jan-2011

General Notes (Continued)
and the PRIMERGY TX140 S1 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
  icc  -m64

C++ benchmarks:
  icpc  -m64

Fortran benchmarks:
  ifort  -m64

Benchmarks using both Fortran and C:
  icc  -m64 ifort  -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
  -xAVX  -ipo  -O3  -no-prec-div  -static  -ansi-alias

C++ benchmarks:
  -xAVX  -ipo  -O3  -no-prec-div  -static  -ansi-alias

Fortran benchmarks:
  -xAVX  -ipo  -O3  -no-prec-div  -static
**SPEC CFP2006 Result**

**Fujitsu**

PRIMERGY TX140 S1, Intel Core i3-2100, 3.10 GHz

| SPECfp_rate2006 | 74.5 |
| SPECfp_rate_base2006 | 73.1 |

CPU2006 license: 19  
Test sponsor: Fujitsu  
Tested by: Fujitsu  
Test date: May-2011  
Hardware Availability: Jun-2011  
Software Availability: Jan-2011

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**Base Optimization Flags (Continued)**

Benchmarks using both Fortran and C:

- `xAVX`  
- `ipo`  
- `no-prec-div`  
- `-static`  
- `-ansi-alias`

---

**Peak Compiler Invocation**

C benchmarks:

- `icc -m64`

C++ benchmarks:

- `icpc -m64`

Fortran benchmarks:

- `ifort -m64`

Benchmarks using both Fortran and C:

- `icc -m64 ifort -m64`

---

**Peak Portability Flags**

Same as Base Portability Flags

---

**Peak Optimization Flags**

C benchmarks:

- `433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`  
- `no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32`

- `470.lbm: basepeak = yes`

- `482.sphinx3: basepeak = yes`

C++ benchmarks:

- `444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`  
- `no-prec-div(pass 2) -prof-use(pass 2) -fno-alias`  
- `auto-ilp32`

- `447.dealII: basepeak = yes`

- `450.soplex: basepeak = yes`

- `453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`  
- `no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias`  
- `-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

Continued on next page
Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes
416.gamess: basepeak = yes
434.zeusmp: basepeak = yes
437.leslie3d: -xAVX -ipo -O3 -no-prec-div
   -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
459.GemsFDTD: basepeak = yes
   465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
   -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
   -inline-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
   -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
   -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
   -static -auto-ilp32
436.cactusADM: basepeak = yes
454.calculix: basepeak = yes
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.xml
### SPEC CFP2006 Result

**Fujitsu**

PRIMERGY TX140 S1, Intel Core i3-2100, 3.10 GHz

| SPECfp_rate2006 | 74.5 |
| SPECfp_rate_base2006 | 73.1 |

| CPU2006 license: | 19 |
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| Software Availability: | Jan-2011 |

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