# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Dell Inc.

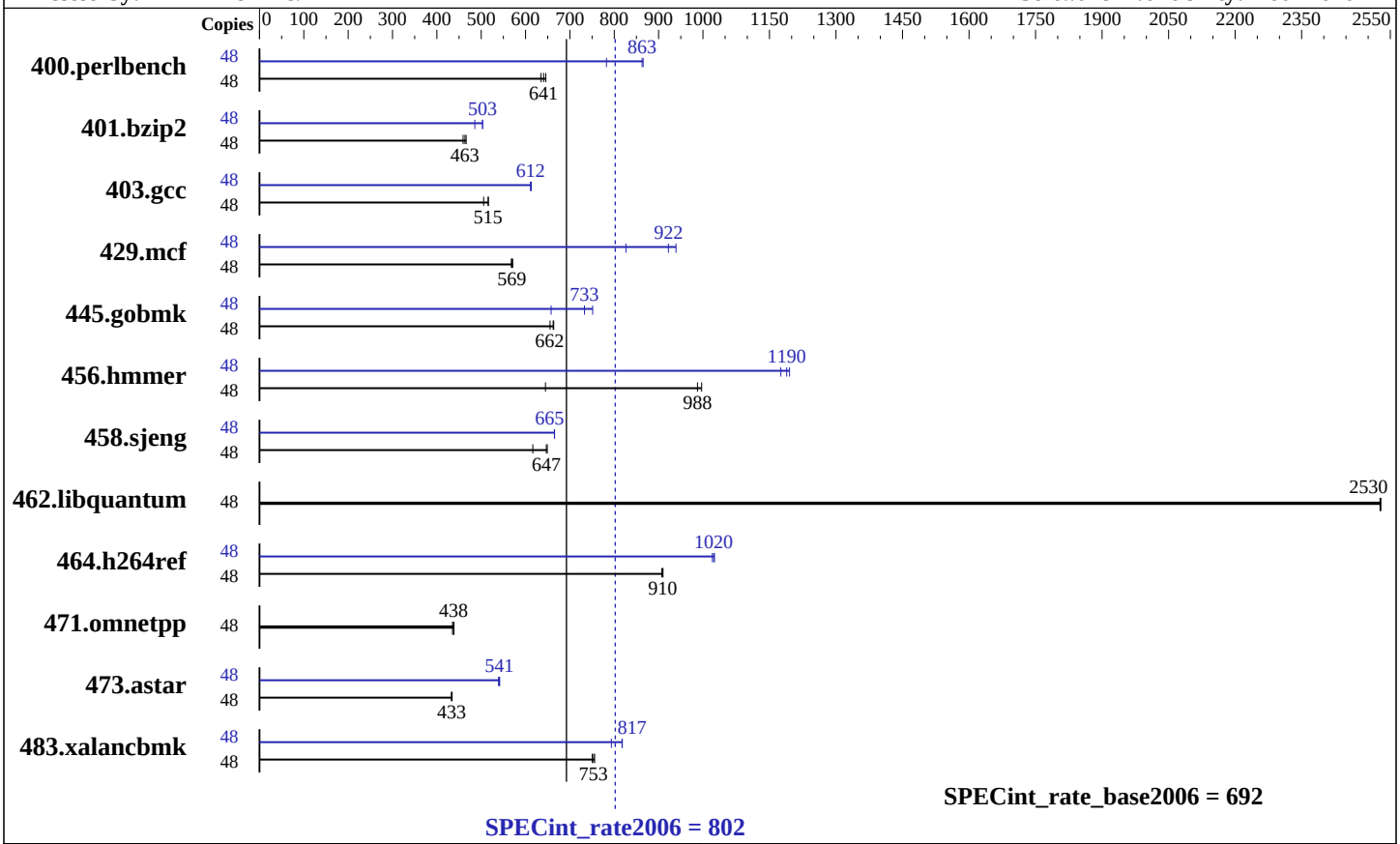
PowerEdge M915 (AMD Opteron 6176, 2.3 GHz)

SPECint®_rate2006 = 802

SPECint_rate_base2006 = 692

**CPU2006 license:** 55
**Test sponsor:** Dell Inc.
**Tested by:** Dell Inc.

**Test date:** May-2011
**Hardware Availability:** May-2011
**Software Availability:** Jul-2010




## Hardware

| | |
|---|---|
| CPU Name: | AMD Opteron 6176 |
| CPU Characteristics: | |
| CPU MHz: | 2300 |
| FPU: | Integrated |
| CPU(s) enabled: | 48 cores, 4 chips, 12 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 64 KB I + 64 KB D on chip per core |
| Secondary Cache: | 512 KB I+D on chip per core |
| L3 Cache: | 12 MB I+D on chip per chip, 6 MB shared / 6 cores |
| Other Cache: | None |
| Memory: | 128 GB (32 x 4 GB 2Rx4 PC3-10600R-9, ECC) |
| Disk Subsystem: | 1 x 146 GB 10000 RPM SAS |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default |
| Compiler: | x86 Open64 4.2.4 Compiler Suite (from AMD) |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Run level 3 (Full multiuser with network) |
| Base Pointers: | 32/64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | SmartHeap 8.1 32-bit Library for Linux |

# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Dell Inc.

PowerEdge M915 (AMD Opteron 6176, 2.3 GHz)

SPECint_rate2006 = 802

SPECint_rate_base2006 = 692

**CPU2006 license:** 55
**Test sponsor:** Dell Inc.
**Tested by:** Dell Inc.

**Test date:** May-2011
**Hardware Availability:** May-2011
**Software Availability:** Jul-2010

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 48 | 739 | 635 | **732** | **641** | 726 | 646 | 48 | 599 | 782 | 542 | 865 | **544** | **863** |
| 401.bzip2 | 48 | 1009 | 459 | 994 | 466 | **1000** | **463** | 48 | 954 | 486 | **921** | **503** | 920 | 503 |
| 403.gcc | 48 | 748 | 516 | 764 | 506 | **750** | **515** | 48 | **632** | **612** | 632 | 611 | 630 | 613 |
| 429.mcf | 48 | 771 | 568 | **769** | **569** | 767 | 571 | 48 | **475** | **922** | 530 | 826 | 466 | 939 |
| 445.gobmk | 48 | **760** | **662** | 768 | 655 | 759 | 664 | 48 | **687** | **733** | 670 | 751 | 766 | 658 |
| 456.hmmer | 48 | 449 | 997 | **453** | **988** | 694 | 645 | 48 | 375 | 1200 | **377** | **1190** | 381 | 1180 |
| 458.sjeng | 48 | 895 | 649 | 942 | 616 | **898** | **647** | 48 | **873** | **665** | 873 | 665 | 873 | 665 |
| 462.libquantum | 48 | 393 | 2530 | 394 | 2530 | **394** | **2530** | 48 | 393 | 2530 | 394 | 2530 | **394** | **2530** |
| 464.h264ref | 48 | **1168** | **910** | 1171 | 907 | 1168 | 910 | 48 | 1040 | 1020 | 1035 | 1030 | **1037** | **1020** |
| 471.omnetpp | 48 | **685** | **438** | 684 | 439 | 689 | 435 | 48 | **685** | **438** | 684 | 439 | 689 | 435 |
| 473.astar | 48 | 778 | 433 | 776 | 434 | **778** | **433** | 48 | 625 | 539 | **623** | **541** | 622 | 542 |
| 483.xalancbmk | 48 | 438 | 756 | **440** | **753** | 441 | 750 | 48 | 417 | 793 | 405 | 818 | **405** | **817** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152'  was used to set environment locked pages in memory limit

Set vm/nr_hugepages=21600 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages
```

## General Notes

```
environment variables set by runspec before the start of the run:
HUGETLB_LIMIT = "450"
LD_LIBRARY_PATH = "/root/cpu2006-1.1/amd1002-rate-libs-revC/64:/root/cpu2006-1.1/amd1002-rate-libs-revC/32"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at
http://developer.amd.com/cpu/open64
Binaries were compiled on SLES10 SP2 with binutils 2.18
```

## Base Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-march=barcelona -mso -Ofast -CG:local_sched_alg=1
-INLINE:aggressive=on -IPA:plimit=8000 -IPA:small_pu=100
-HP:bdt=2m:heap=2m

C++ benchmarks:
-march=barcelona -mso -Ofast -m32 -INLINE:aggressive=on
-CG:cmp_peep=on -L/root/work/libraries/SmartHeap-8.1/lib -lsmartheap

## Peak Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

Continued on next page

## Peak Portability Flags (Continued)

401.bzip2: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -IPA:plimit=20000 -LNO:opt=0
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -OPT:keep_ext=on -WOPT:if_conv=0
-CG:local_sched_alg=1 -CG:unroll_fb_req=on
-HP:bdt=2m:heap=2m

401.bzip2: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -OPT:alias=disjoint
-OPT:goto=off -CG:local_sched_alg=1 -HP:bdt=2m:heap=2m

403.gcc: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:trip_count=256
-LNO:prefetch_ahead=10 -CG:cmp_peep=on -m32
-HP:bdt=2m:heap=2m -GRA:unspill=on -IPA:small_pu=200

429.mcf: -march=barcelona -mso -O3 -ipa -INLINE:aggressive=on
-CG:gcm=off -GRA:prioritize_by_density=on -m32
-HP:bdt=2m:heap=2m

445.gobmk: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -OPT:alias=restrict
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -OPT:keep_ext=on -ipa -IPA:plimit=750
-IPA:min_hotness=300 -IPA:pu_reorder=1 -LNO:prefetch=1
-LNO:ignore_feedback=off -CG:p2align=on
-CG:unroll_fb_req=on -HP:bdt=2m:heap=2m

456.hmmer: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:prefetch=0
-OPT:alias=disjoint -OPT:unroll_times_max=8
-OPT:unroll_size=256 -OPT:unroll_level=2 -OPT:keep_ext=on
-CG:local_sched_alg=1 -CG:cflow=0
-CG:push_pop_int_saved_regs=off -CG:cmp_peep=on
-HP:bdt=2m:heap=2m

Continued on next page

## Peak Optimization Flags (Continued)

458.sjeng: `-march=barcelona -mso -fb_create fbdata`(pass 1)
`-fb_opt fbdata`(pass 2) `-O3 -ipa -LNO:ignore_feedback=off`
`-LNO:full_unroll=10 -LNO:fusion=0 -LNO:fission=2`
`-IPA:pu_reorder=2 -CG:ptr_load_use=0`
`-OPT:unroll_times_max=8 -INLINE:aggressive=on`

462.libquantum: `basepeak = yes`

464.h264ref: `-march=barcelona -mso -fb_create fbdata`(pass 1)
`-fb_opt fbdata`(pass 2) `-O3 -IPA:plimit=20000`
`-OPT:alias=disjoint -LNO:prefetch=0 -CG:ptr_load_use=0`
`-CG:push_pop_int_saved_regs=off`

C++ benchmarks:

471.omnetpp: `basepeak = yes`

473.astar: `-march=barcelona -mso -fb_create fbdata`(pass 1)
`-fb_opt fbdata`(pass 2) `-Ofast -TENV:frame_pointer=off`
`-WOPT:if_conv=0 -GRA:optimize_boundary=on`
`-OPT:alias=disjoint -INLINE:aggressive=on`
`-IPA:small_pu=3000 -IPA:plimit=3000 -m32`
`-HP:bdt=2m:heap=2m`

483.xalancbmk: `-march=barcelona -mso -Ofast -INLINE:aggressive=on -m32`
`-CG:cmp_peep=on -GRA:unspill=on -TENV:frame_pointer=off`
`-fno-emit-exceptions`
`-L/root/work/libraries/SmartHeap-8.1/lib -lsmartheap`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.html`
`http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.xml`
`http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.xml`


SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.


For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 18:25:00 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 7 June 2011.