# SPEC® CINT2006 Result

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## Bull SAS

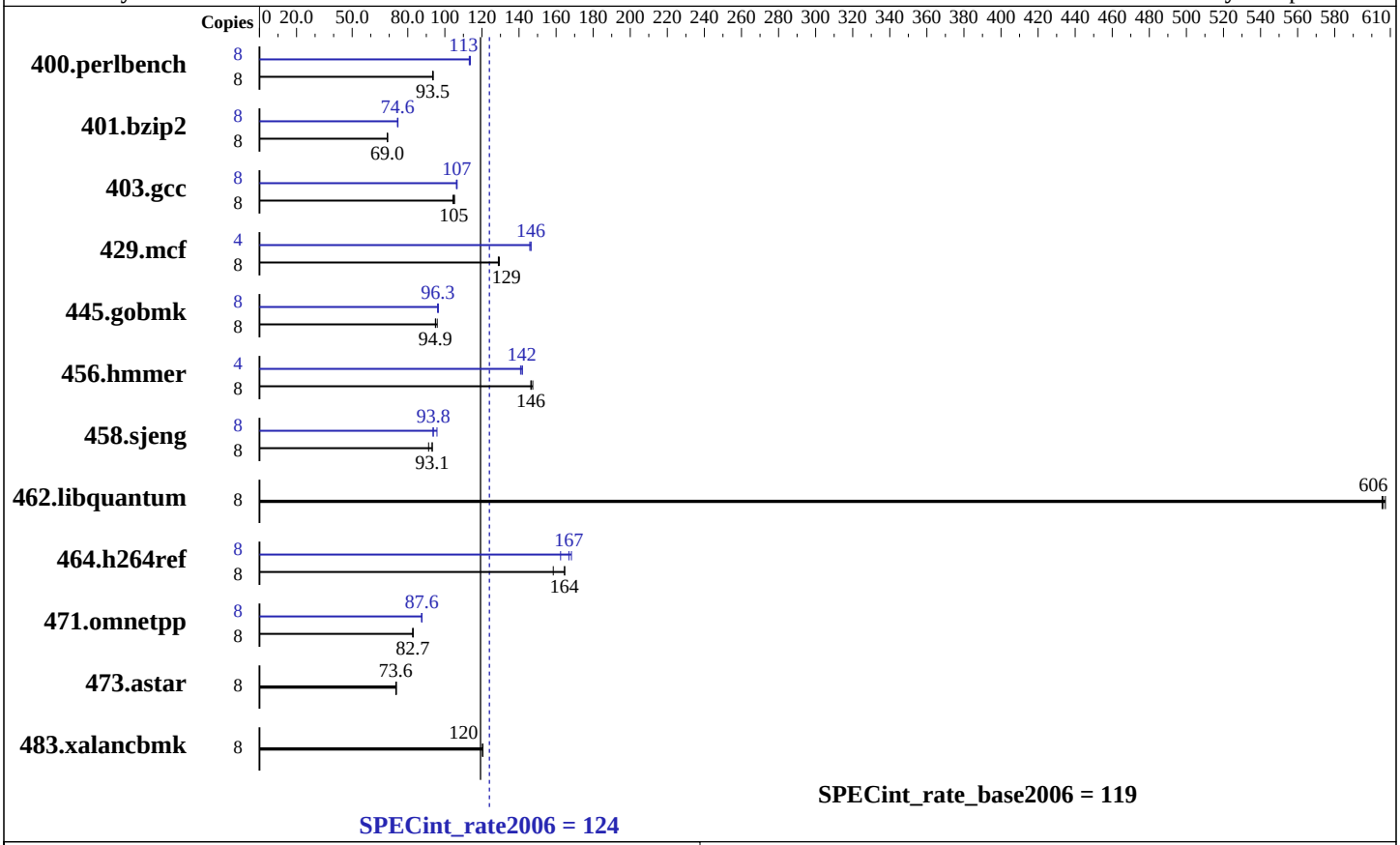
NovaScale R410B F2 (Intel Xeon E3-1260L, 2.40 GHz)

SPECint®_rate2006 = 124

SPECint_rate_base2006 = 119

**CPU2006 license:** 20
**Test sponsor:** Bull SAS
**Tested by:** Dell Inc.

**Test date:** Mar-2011
**Hardware Availability:** May-2011
**Software Availability:** Apr-2011




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E3-1260L |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.30 GHz |
| CPU MHz: | 2400 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 1 chip, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 8 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 8 GB (4 x 2 GB 2Rx4 PC3-10600R-9, ECC) |
| Disk Subsystem: | 1 x 146 GB 15000 RPM SAS |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default |
| Compiler: | Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | SmartHeap 8.1 32-bit Library for Linux |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | 834 | 93.7 | **836** | **93.5** | 837 | 93.4 | 8 | **689** | **113** | 687 | 114 | 691 | 113 |
| 401.bzip2 | 8 | **1119** | **69.0** | 1120 | 68.9 | 1116 | 69.2 | 8 | **1034** | **74.6** | 1037 | 74.4 | 1033 | 74.7 |
| 403.gcc | 8 | 612 | 105 | **614** | **105** | 617 | 104 | 8 | **605** | **107** | 604 | 107 | 606 | 106 |
| 429.mcf | 8 | 566 | 129 | **566** | **129** | 564 | 129 | 4 | 249 | 146 | 250 | 146 | **249** | **146** |
| 445.gobmk | 8 | **884** | **94.9** | 875 | 95.9 | 884 | 94.9 | 8 | 870 | 96.5 | **871** | **96.3** | 873 | 96.1 |
| 456.hmmer | 8 | 506 | 147 | 510 | 146 | **510** | **146** | 4 | 265 | 141 | **263** | **142** | 263 | 142 |
| 458.sjeng | 8 | **1039** | **93.1** | 1039 | 93.2 | 1061 | 91.2 | 8 | 1034 | 93.6 | 1011 | 95.8 | **1032** | **93.8** |
| 462.libquantum | 8 | **273** | **606** | 274 | 606 | 273 | 607 | 8 | **273** | **606** | 274 | 606 | 273 | 607 |
| 464.h264ref | 8 | **1076** | **164** | 1117 | 158 | 1075 | 165 | 8 | 1052 | 168 | **1061** | **167** | 1090 | 162 |
| 471.omnetpp | 8 | 603 | 82.9 | 605 | 82.7 | **604** | **82.7** | 8 | 572 | 87.5 | **571** | **87.6** | 570 | 87.6 |
| 473.astar | 8 | **763** | **73.6** | 764 | 73.6 | 760 | 73.9 | 8 | **763** | **73.6** | 764 | 73.6 | 760 | 73.9 |
| 483.xalancbmk | 8 | 459 | 120 | 458 | 121 | **458** | **120** | 8 | 459 | 120 | 458 | 121 | **458** | **120** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
 'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 3600> /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

```
 BIOS Settings:
 Power Management = Maximum Performance (Default = Active Power Controller)
```

## General Notes

```
 The Dell PowerEdge R210 II and
 the Bull NovaScale R410B F2 models are electronically equivalent.
 The results have been measured on a Dell PowerEdge R210 II model
 Binaries were compiled on RHEL5.5
```

## Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

Continued on next page

## Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xAVX -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
-auto-ilp32

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

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## Peak Optimization Flags (Continued)

464.h264ref: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll2` `-ansi-alias`

C++ benchmarks:

471.omnetpp: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-ansi-alias` `-opt-ra-region-strategy=block -Wl,-z,muldefs` `-L/smartheap -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml`


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