SPEC® CFP2006 Result

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HITACHI

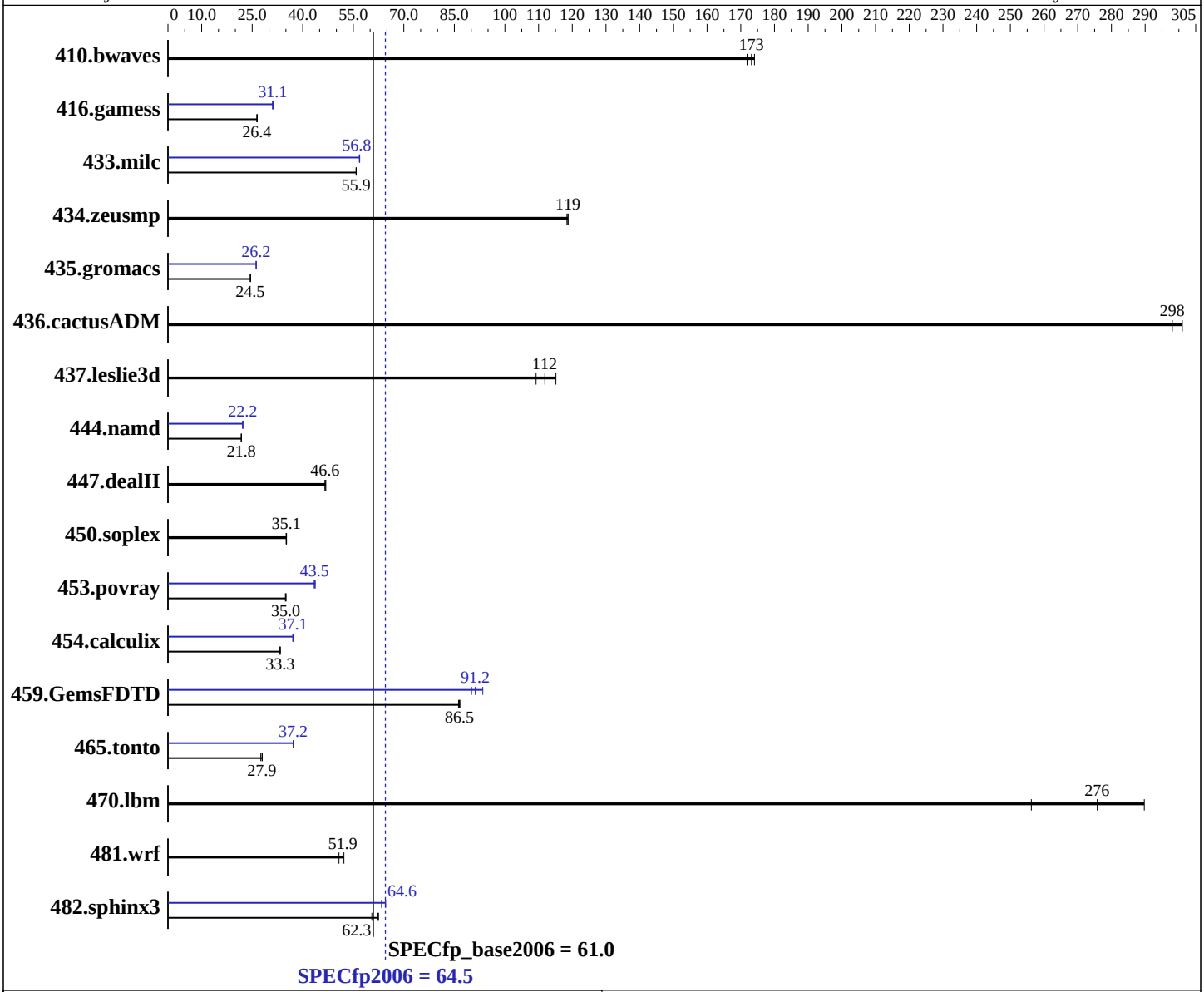
Compute Blade 2000 (Intel Xeon X5690)

SPECfp®2006 = 64.5

SPECfp_base2006 = 61.0

CPU2006 license: 35
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Jun-2011
Hardware Availability: Feb-2011
Software Availability: Jan-2011



Hardware

CPU Name:	Intel Xeon X5690
CPU Characteristics:	Intel Turbo Boost Technology up to 3.73 GHz
CPU MHz:	3466
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SuSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ Compiler XE for Linux Version 12.0.3.174 Build 20110309 Intel Fortran Compiler XE for Linux Version 12.0.3.174 Build 20110309
Auto Parallel:	Yes

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L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (6 x 8 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem:	2 x 146 GB 10000 rpm SAS RAID1 configuration
Other Hardware:	None

File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	78.1	174	79.1	172	**78.5**	**173**	78.1	174	79.1	172	**78.5**	**173**
416.gamess	741	26.4	739	26.5	**741**	**26.4**	628	31.2	**630**	**31.1**	630	31.1
433.milc	164	55.9	**164**	**55.9**	164	55.9	**161**	**56.8**	162	56.8	161	56.9
434.zeusmp	76.8	118	**76.7**	**119**	76.6	119	76.8	118	**76.7**	**119**	76.6	119
435.gromacs	**292**	**24.5**	291	24.5	292	24.4	273	26.2	273	26.2	**273**	**26.2**
436.cactusADM	39.7	301	**40.1**	**298**	40.1	298	39.7	301	**40.1**	**298**	40.1	298
437.leslie3d	**84.0**	**112**	86.0	109	81.6	115	**84.0**	**112**	86.0	109	81.6	115
444.namd	368	21.8	**368**	**21.8**	369	21.7	**361**	**22.2**	361	22.2	361	22.2
447.dealII	**245**	**46.6**	244	46.9	245	46.6	**245**	**46.6**	244	46.9	245	46.6
450.soplex	237	35.2	238	35.1	**238**	**35.1**	237	35.2	238	35.1	**238**	**35.1**
453.povray	153	34.8	**152**	**35.0**	152	35.0	123	43.4	122	43.7	**122**	**43.5**
454.calculix	249	33.1	**248**	**33.3**	247	33.4	222	37.1	223	37.1	**222**	**37.1**
459.GemsFDTD	123	86.2	122	86.7	**123**	**86.5**	118	90.1	114	93.4	**116**	**91.2**
465.tonto	357	27.6	351	28.0	**353**	**27.9**	**265**	**37.2**	265	37.1	265	37.2
470.lbm	**49.8**	**276**	47.4	290	53.6	256	**49.8**	**276**	47.4	290	53.6	256
481.wrf	**215**	**51.9**	214	52.2	220	50.8	**215**	**51.9**	214	52.2	220	50.8
482.sphinx3	312	62.5	**313**	**62.3**	322	60.5	308	63.4	302	64.6	**302**	**64.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
Hugepages was enabled with the following:
'nodev   /mnt/hugepages   hugetlbfs  defaults   0 0' added to /etc/fstab
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

```
BIOS Settings:
Intel HT Technology = Disabled
Data Reuse Optimization = Disabled
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/PlatformHitachi.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/PlatformHitachi.xml

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