**Acer Incorporated**

Gateway GW2000ht-GW170ht F1 (Intel Xeon X5670, 2.93GHz)

| SPECint_rate2006 | 386 |
| SPECint_rate_base2006 | 362 |

**Hardware**

<table>
<thead>
<tr>
<th>Specification</th>
<th>Details</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name:</td>
<td>Intel Xeon X5670</td>
</tr>
<tr>
<td>CPU Characteristics:</td>
<td>Intel Turbo Boost Technology up to 3.33 GHz</td>
</tr>
<tr>
<td>CPU MHz:</td>
<td>2933</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>12 cores, 2 chips, 6 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>1, 2 chips</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache:</td>
<td>256 KB I+D on chip per core</td>
</tr>
<tr>
<td>L3 Cache:</td>
<td>12 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other Cache:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC)</td>
</tr>
<tr>
<td>Disk Subsystem:</td>
<td>1 x 1000 GB SATA 7200RPM</td>
</tr>
<tr>
<td>Other Hardware:</td>
<td>None</td>
</tr>
</tbody>
</table>

**Operating System:** SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default

**Compiler:** Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116

**Auto Parallel:** No

**File System:** ReiserFS

**System State:** Run level 3 (multi-user)

**Base Pointers:** 32-bit

**Peak Pointers:** 32/64-bit

**Other Software:** Microquill SmartHeap V9.01
### SPEC CINT2006 Result

**Acer Incorporated**

Gateway GW2000ht-GW170ht F1 (Intel Xeon X5670, 2.93GHz)

**SPECint_rate2006 = 386**

**SPECint_rate_base2006 = 362**

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Benchmarks</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>24</td>
<td>737</td>
<td>318</td>
<td>732</td>
<td>320</td>
<td>732</td>
<td>321</td>
<td>Base</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>401.bzip2</td>
<td>24</td>
<td>1080</td>
<td>214</td>
<td>1079</td>
<td>215</td>
<td>1072</td>
<td>216</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>403.gcc</td>
<td>24</td>
<td>775</td>
<td>249</td>
<td>773</td>
<td>250</td>
<td>780</td>
<td>248</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>429.mcf</td>
<td>24</td>
<td>726</td>
<td>302</td>
<td>718</td>
<td>305</td>
<td>716</td>
<td>306</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>445.gobmk</td>
<td>24</td>
<td>690</td>
<td>365</td>
<td>689</td>
<td>366</td>
<td>695</td>
<td>362</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>456.hmmer</td>
<td>24</td>
<td>485</td>
<td>461</td>
<td>484</td>
<td>463</td>
<td>485</td>
<td>461</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
</tr>
<tr>
<td>458.sjeng</td>
<td>24</td>
<td>825</td>
<td>352</td>
<td>824</td>
<td>352</td>
<td>826</td>
<td>351</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>462.libquantum</td>
<td>24</td>
<td>269</td>
<td>1850</td>
<td>272</td>
<td>1830</td>
<td>273</td>
<td>1820</td>
<td></td>
<td></td>
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<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>464.h264ref</td>
<td>24</td>
<td>1081</td>
<td>491</td>
<td>1079</td>
<td>492</td>
<td>1101</td>
<td>482</td>
<td></td>
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<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>24</td>
<td>656</td>
<td>229</td>
<td>656</td>
<td>229</td>
<td>657</td>
<td>228</td>
<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>473.astar</td>
<td>24</td>
<td>753</td>
<td>224</td>
<td>752</td>
<td>224</td>
<td>753</td>
<td>224</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>24</td>
<td>467</td>
<td>355</td>
<td>466</td>
<td>355</td>
<td>467</td>
<td>355</td>
<td></td>
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<td></td>
<td></td>
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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Submit Notes**

The config file option 'submit' was used.
numactl was used to bind copies to the cores

**Operating System Notes**

'ulimit -s unlimited' was used to set environment stack size.
Large pages were disabled for this run

**Platform Notes**

BIOS Settings:
Fan speed = full speed (Default = Energy Saving)
Data Reuse = Disabled (Default = Enabled)

**General Notes**

Binaries compiled on RHEL5.5
The Acer AW2000h-AW170h F1, Gateway GW2000h-GW170h F1, Acer AW2000ht-AW170ht F1 and Gateway GW2000ht-GW170ht F1 are electronically equivalent. This result was measured on Gateway GW2000ht-GW170ht F1.

**Base Compiler Invocation**

C benchmarks:
```
icc -m32
```

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Acer Incorporated

Gateway GW2000ht-GW170ht F1 (Intel Xeon X5670, 2.93GHz)

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Test sponsor: Acer Incorporated
 Tested by: Acer Incorporated

Test date: Jun-2011
Hardware Availability: Aug-2010
Software Availability: Jan-2011

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:
403.gcc: -Dallos=alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
  icc -m32
  400.perlbench: icc -m64
  401.bzip2: icc -m64
  456.hmmer: icc -m64
  458.sjeng: icc -m64

C++ benchmarks:
icpc -m32
Acer Incorporated
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**Peak Portability Flags**

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

---

**Peak Optimization Flags**

C benchmarks:

400.perlbench: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
-03 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
401.bzip2: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
-03 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
429.mcf: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
-03 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
-ansi-alias -auto-ilp32
445.gobmk: -xSSE4.2 (pass 2) -prof-gen (pass 1) -prof-use (pass 2)
-ansi-alias -auto-ilp32
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
458.sjeng: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
-03 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
-unroll14 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
462.libquantum: basepeak = yes
464.h264ref: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
-03 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
-03 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

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### SPEC CINT2006 Result

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</tr>
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<tr>
<td>Software Availability</td>
<td>Jan-2011</td>
</tr>
</tbody>
</table>

### Peak Optimization Flags (Continued)

471.omnetpp (continued):
- `-L/smartheap` `-lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

### Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


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