### SPECint® CINT2006 Result

**Acer Incorporated**

Gateway GW2000ht-GW170ht F1 (Intel Xeon X5667, 3.06GHz)

<table>
<thead>
<tr>
<th>Test sponsor:</th>
<th>Acer Incorporated</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tested by:</td>
<td>Acer Incorporated</td>
</tr>
<tr>
<td>CPU2006 license:</td>
<td>97</td>
</tr>
<tr>
<td>Test date:</td>
<td>Jun-2011</td>
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<tr>
<td>Software Availability:</td>
<td>Jan-2011</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Aug-2010</td>
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<table>
<thead>
<tr>
<th>Test</th>
<th>Copies</th>
<th>SPECint_rate2006</th>
<th>SPECint_rate_base2006</th>
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</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>16</td>
<td>273</td>
<td>282</td>
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<tr>
<td>401.bzip2</td>
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<td>403.gcc</td>
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<td>458.sjeng</td>
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<td>462.libquantum</td>
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<td>464.h264ref</td>
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<td>471.omnetpp</td>
<td>16</td>
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<td>473.astar</td>
<td>16</td>
<td>178</td>
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<tr>
<td>483.xalancbmk</td>
<td>16</td>
<td>285</td>
<td>285</td>
</tr>
</tbody>
</table>

**SPECint_rate2006 = 297**

**SPECint_rate_base2006 = 282**

### Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon X5667</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Characteristics:</td>
<td>Intel Turbo Boost Technology up to 3.46 GHz</td>
</tr>
<tr>
<td>CPU MHz:</td>
<td>3067</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>8 cores, 2 chips, 4 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>1, 2 chips</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache:</td>
<td>256 KB I+D on chip per core</td>
</tr>
<tr>
<td>L3 Cache:</td>
<td>12 MB I+D on chip per chip</td>
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<tr>
<td>Other Cache:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC)</td>
</tr>
<tr>
<td>Disk Subsystem:</td>
<td>1 x 1000 GB SATA 7200RPM</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Operating System:</th>
<th>SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default</th>
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<tbody>
<tr>
<td>Compiler:</td>
<td>Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116</td>
</tr>
<tr>
<td>Auto Parallel:</td>
<td>No</td>
</tr>
<tr>
<td>File System:</td>
<td>ReiserFS</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>32-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>32/64-bit</td>
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<td>Other Software:</td>
<td>Microquill SmartHeap V9.01</td>
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</tbody>
</table>
## SPEC CINT2006 Result

**Acer Incorporated**

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<th>Benchmark</th>
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<th>Seconds</th>
<th>Ratio</th>
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<td>285</td>
<td>391</td>
<td>283</td>
</tr>
</tbody>
</table>

**Base Compiler Invocation**

C benchmarks:

```
icc  -m32
```

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The config file option 'submit' was used.

numactl was used to bind copies to the cores

### Operating System Notes

'unlimit -s unlimited' was used to set environment stack size

Large pages were disabled for this run

### Platform Notes

BIOS Settings:

- Fan speed = full speed (Default = Energy Saving)
- Data Reuse = Disabled (Default = Enabled)

### General Notes

Binaries compiled on RHEL5.5
The Acer AW2000h-AW170h F1, Gateway GW2000h-GW170h F1, Acer AW2000ht-AW170ht F1 and Gateway GW2000ht-GW170ht F1 are electronically equivalent. This result was measured on Gateway GW2000ht-GW170ht F1.

### Base Compiler Invocation

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Spec CINT2006 Result

Acer Incorporated
Gateway GW2000ht-GW170ht F1 (Intel Xeon X5667, 3.06GHz)

SPECint_rate2006 = 297
SPECint_rate_base2006 = 282

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Jun-2011
Hardware Availability: Aug-2010
Software Availability: Jan-2011

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32
Acer Incorporated

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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
-03 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
-03 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
-03 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2 (pass 2) -prof-gen (pass 1) -prof-use (pass 2)
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
-03 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
-unroll2 -ansi-alias

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
-03 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)

C++ benchmarks:

471.omnetpp: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
-03 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

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Peak Optimization Flags (Continued)

471.omnetpp (continued):
   -L/smartheap -lsmartheap

473.astar: basepeak = yes
483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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