



# SPEC® CFP2006 Result

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## Acer Incorporated

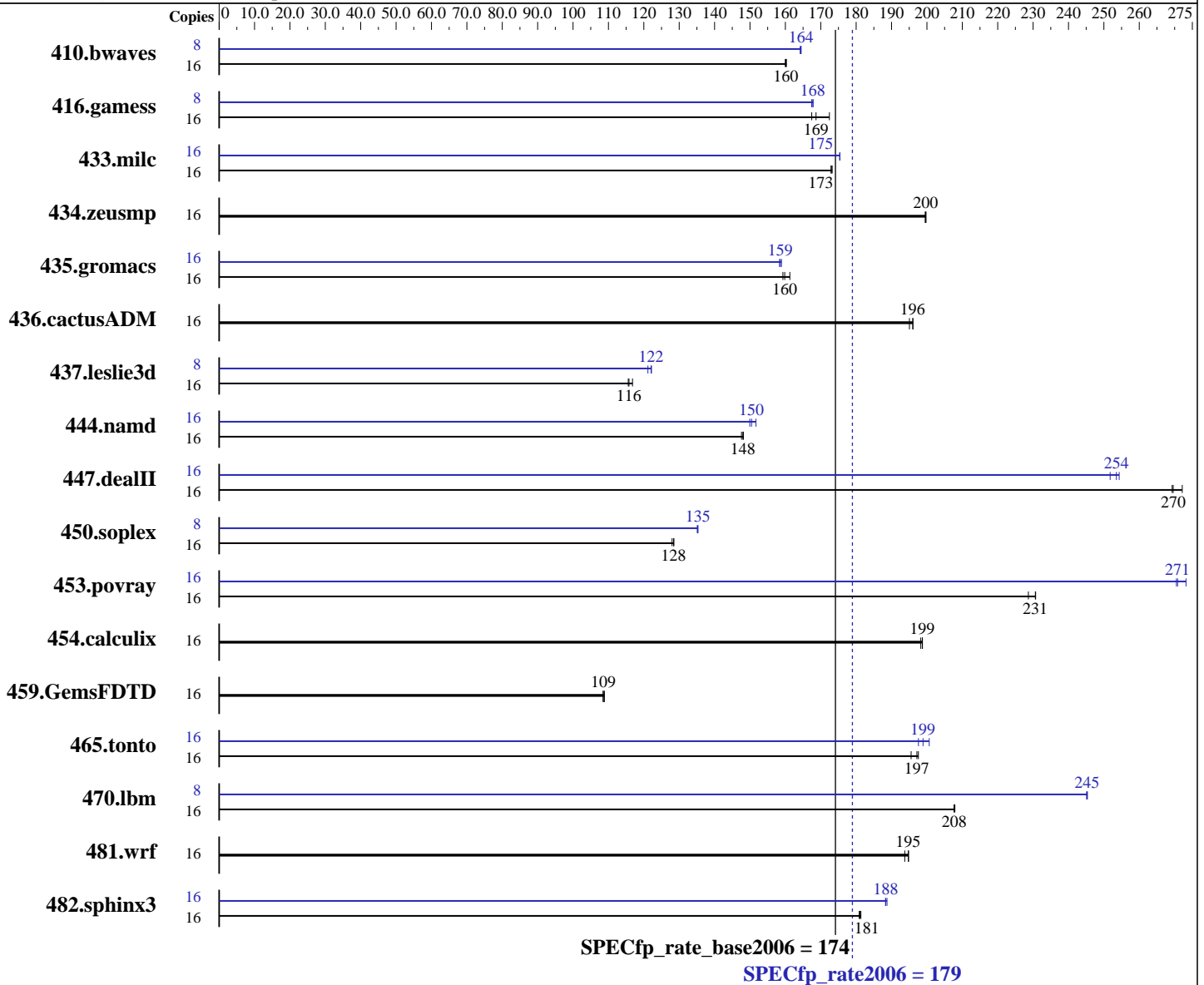
Gateway GW2000ht-GW170ht F1 (Intel Xeon E5620, 2.40GHz)

**SPECfp®\_rate2006 = 179**

**SPECfp\_rate\_base2006 = 174**

CPU2006 license: 97  
Test sponsor: Acer Incorporated  
Tested by: Acer Incorporated

Test date: Jun-2011  
Hardware Availability: Aug-2010  
Software Availability: Jan-2011



### Hardware

CPU Name: Intel Xeon E5620  
CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz  
CPU MHz: 2400  
FPU: Integrated  
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
CPU(s) orderable: 1, 2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) SP1, Kernel 2.6.32.12-0.7-default  
Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
Auto Parallel: No  
File System: ReiserFS  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit

Continued on next page



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L3 Cache: 12 MB I+D on chip per chip  
Other Cache: None  
Memory: 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC)  
Disk Subsystem: 1 x 1000 GB SATA 7200RPM  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1357	160	<u>1358</u>	<u>160</u>	1359	160	8	661	164	<u>661</u>	<u>164</u>	662	164
416.gamess	16	1817	172	1871	167	<u>1857</u>	<u>169</u>	8	936	167	<u>934</u>	<u>168</u>	933	168
433.milc	16	848	173	<u>849</u>	<u>173</u>	849	173	16	837	175	<u>838</u>	<u>175</u>	838	175
434.zeusmp	16	729	200	730	199	<u>729</u>	<u>200</u>	16	729	200	730	199	<u>729</u>	<u>200</u>
435.gromacs	16	708	161	<u>715</u>	<u>160</u>	717	159	16	719	159	721	158	<u>720</u>	<u>159</u>
436.cactusADM	16	<u>976</u>	<u>196</u>	975	196	980	195	16	<u>976</u>	<u>196</u>	975	196	980	195
437.leslie3d	16	<u>1299</u>	<u>116</u>	1288	117	1301	116	8	621	121	<u>616</u>	<u>122</u>	616	122
444.namd	16	866	148	869	148	<u>867</u>	<u>148</u>	16	846	152	<u>853</u>	<u>150</u>	856	150
447.dealII	16	<u>679</u>	<u>270</u>	673	272	680	269	16	720	254	727	252	<u>722</u>	<u>254</u>
450.soplex	16	1038	128	1043	128	<u>1039</u>	<u>128</u>	8	<u>493</u>	<u>135</u>	493	135	494	135
453.povray	16	<u>369</u>	<u>231</u>	369	231	372	229	16	<u>314</u>	<u>271</u>	312	273	315	270
454.calculix	16	666	198	<u>664</u>	<u>199</u>	664	199	16	666	198	<u>664</u>	<u>199</u>	664	199
459.GemsFDTD	16	1565	108	1559	109	<u>1564</u>	<u>109</u>	16	1565	108	1559	109	<u>1564</u>	<u>109</u>
465.tonto	16	797	198	805	196	<u>799</u>	<u>197</u>	16	797	198	<u>792</u>	<u>199</u>	785	201
470.lbm	16	1057	208	1059	208	<u>1058</u>	<u>208</u>	8	448	245	448	245	<u>448</u>	<u>245</u>
481.wrf	16	917	195	<u>918</u>	<u>195</u>	923	194	16	917	195	<u>918</u>	<u>195</u>	923	194
482.sphinx3	16	1720	181	1724	181	<u>1722</u>	<u>181</u>	16	1652	189	1656	188	<u>1656</u>	<u>188</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
Large pages were disabled for this run

## Platform Notes

BIOS Settings:  
Fan speed = full speed (Default = Energy Saving)  
Data Reuse = Disabled (Default = Enabled)



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## General Notes

Binaries compiled on RHEL5.5

The Acer AW2000h-AW170h F1, Gateway GW2000h-GW170h F1, Acer AW2000ht-AW170ht F1 and Gateway GW2000ht-GW170ht F1 are electronically equivalent. This result was measured on Gateway GW2000ht-GW170ht F1.

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

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## Base Optimization Flags (Continued)

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

## Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

`482.sphinx3:icc -m32`

C++ benchmarks (except as noted below):

`icpc -m64`

`450.soplex:icpc -m32`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`  
 416.gamess: `-DSPEC_CPU_LP64`  
 433.milc: `-DSPEC_CPU_LP64`  
 434.zeusmp: `-DSPEC_CPU_LP64`  
 435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`  
 436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`  
 437.leslie3d: `-DSPEC_CPU_LP64`  
 444.namd: `-DSPEC_CPU_LP64`  
 447.dealII: `-DSPEC_CPU_LP64`  
 453.povray: `-DSPEC_CPU_LP64`  
 454.calculix: `-DSPEC_CPU_LP64 -nofor_main`  
 459.GemsFDTD: `-DSPEC_CPU_LP64`  
 465.tonto: `-DSPEC_CPU_LP64`  
 470.lbm: `-DSPEC_CPU_LP64`  
 481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`



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## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

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## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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