



SPEC® CFP2006 Result

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Intel Corporation

SPECfp®_rate2006 = 124

Intel DX58SO2 Motherboard (Intel Core i7-990X)

SPECfp_rate_base2006 = 121

CPU2006 license: 13

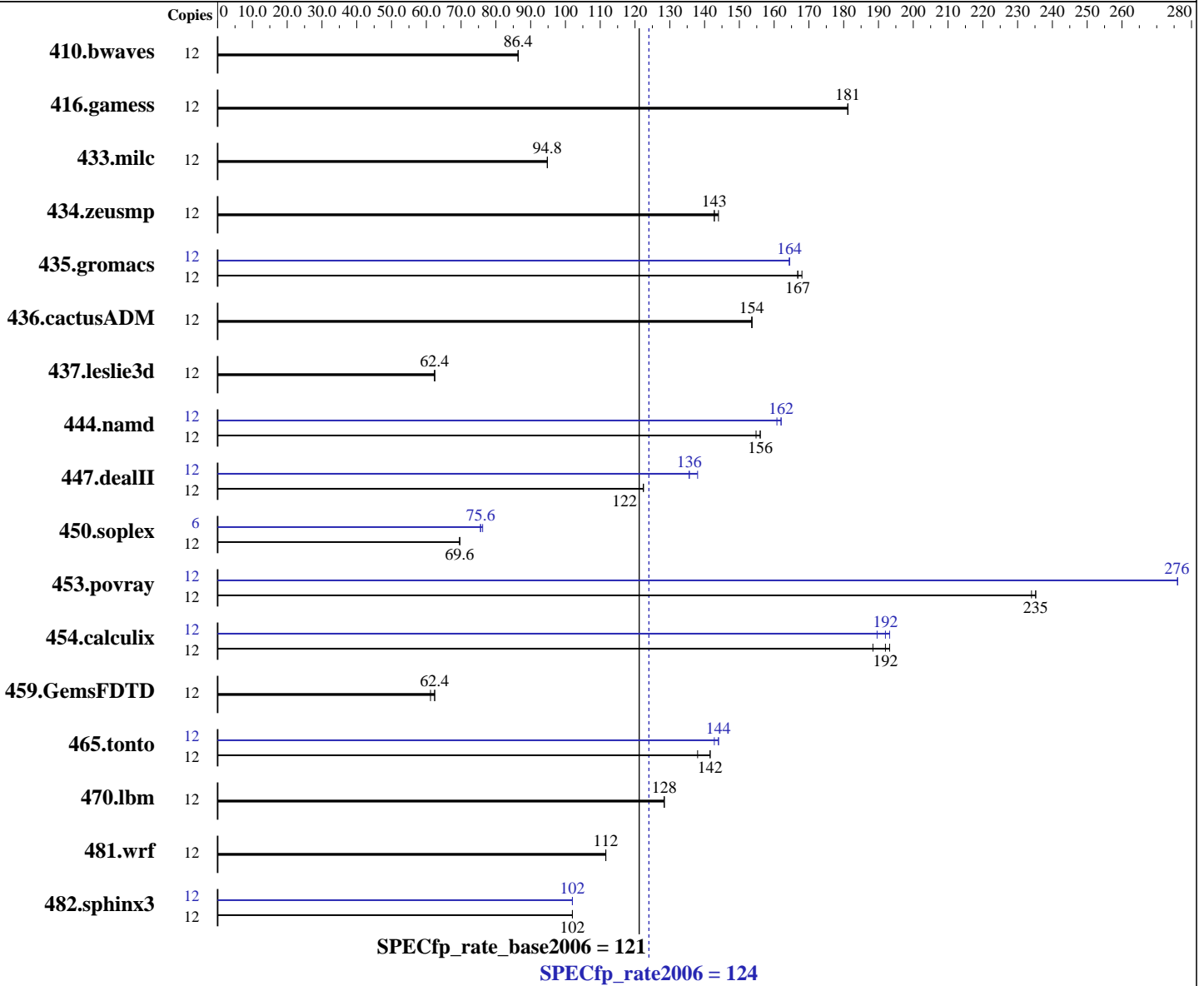
Test date: Jun-2011

Test sponsor: Intel Corporation

Hardware Availability: Mar-2011

Tested by: Intel Corporation

Software Availability: Apr-2011



Hardware

CPU Name: Intel Core i7-990X
 CPU Characteristics: Intel Turbo Boost Technology up to 3.73 GHz
 CPU MHz: 3466
 FPU: Integrated
 CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Windows 7 Ultimate (64-bit)
 Compiler: Intel C++ Compiler XE for Intel 64
 Version 12.0.3.176 Build 20110309
 Intel Visual Fortran Compiler XE for Intel 64
 Version 12.0.3.176 Build 20110309
 Microsoft Visual Studio 2008 Professional SP1
 (for libraries)
 Auto Parallel: No
 File System: NTFS

Continued on next page



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L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 12 GB (3 x 4 GB 2Rx8 PC3-8500U-7)
 Disk Subsystem: Intel 160GB SSD SSDSA2M160G2GN
 Other Hardware: None

System State: Default
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap Library Version 9.01 from
<http://www.microquill.com/>

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	1898	86.4	1886	86.4	1888	86.4	12	1898	86.4	1886	86.4	1888	86.4
416.gamess	12	1294	181	1300	181	1297	181	12	1294	181	1300	181	1297	181
433.milc	12	1156	94.8	1157	94.8	1157	94.8	12	1156	94.8	1157	94.8	1157	94.8
434.zeusmp	12	761	144	763	143	763	143	12	761	144	763	143	763	143
435.gromacs	12	513	167	509	168	515	167	12	522	164	521	164	522	164
436.cactusADM	12	931	154	932	154	932	154	12	931	154	932	154	932	154
437.leslie3d	12	1801	62.4	1804	62.4	1799	62.4	12	1801	62.4	1804	62.4	1799	62.4
444.namd	12	621	155	617	156	618	156	12	595	162	600	161	596	162
447.dealII	12	1118	122	1126	122	1127	122	12	1013	136	1010	136	998	138
450.soplex	12	1437	69.6	1437	69.6	1437	69.6	6	659	76.2	659	75.6	659	75.6
453.povray	12	272	234	271	235	271	235	12	231	276	232	276	232	276
454.calculix	12	517	192	513	193	526	188	12	513	193	514	192	521	190
459.GemsFDTD	12	2060	62.4	2060	62.4	2061	61.2	12	2060	62.4	2060	62.4	2061	61.2
465.tonto	12	857	138	836	142	836	142	12	824	143	818	144	821	144
470.lbm	12	1279	128	1279	128	1279	128	12	1279	128	1279	128	1279	128
481.wrf	12	1197	112	1198	112	1199	112	12	1197	112	1198	112	1199	112
482.sphinx3	12	2298	102	2299	102	2297	102	12	2299	102	2299	102	2297	102

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

The start command with the /affinity switch was used to bind processes to cores

General Notes

Tested systems can be used with Shin-G ATX case,

PC Power and Cooling 1200W power supply

System was configured with an ATI HD6970 discrete graphics card



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Base Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 -names:lowercase
 416.gamess: -DSPEC_CPU_P64
 433.milc: -DSPEC_CPU_P64
 434.zeusmp: -DSPEC_CPU_P64
 435.gromacs: -DSPEC_CPU_P64
 436.cactusADM: -DSPEC_CPU_P64 -names:lowercase /assume:underscore
 437.lelie3d: -DSPEC_CPU_P64
 444.namd: -DSPEC_CPU_P64 /TP
 447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
 450.soplex: -DSPEC_CPU_P64
 453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
 454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -names:lowercase
 459.GemsFDTD: -DSPEC_CPU_P64
 465.tonto: -DSPEC_CPU_P64
 470.lbm: -DSPEC_CPU_P64
 481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
 482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE

C++ benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qansi-alias -Qcxx-features
-Qauto-ilp32 /F1000000000 shlw64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qansi-alias /F1000000000
-link /FORCE:MULTIPLE

Continued on next page



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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Fortran benchmarks:

```
ifort
```

Benchmarks using both Fortran and C:

```
icl -Qvc9 -Qstd=c99 ifort
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: basepeak = yes
```

```
470.lbm: basepeak = yes
```

```
482.sphinx3: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
444.namd: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000
shlw64M.lib -link /FORCE:MULTIPLE
```

```
447.dealIII: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qscalar-rep- -Qauto-ilp32 /F1000000000 shlw64M.lib
-link /FORCE:MULTIPLE
```

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Peak Optimization Flags (Continued)

450.soplex: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qauto-ilp32 /F1000000000 sh1W64M.lib
-link /FORCE:MULTIPLE

453.povray: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F1000000000 sh1W64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

435.gromacs: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE

436.cactusADM: basepeak = yes

454.calculix: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qauto-ilp32 /F1000000000
-link /FORCE:MULTIPLE

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.html>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.xml>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.xml>



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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