SPEC® CFP2006 Result

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Intel Corporation

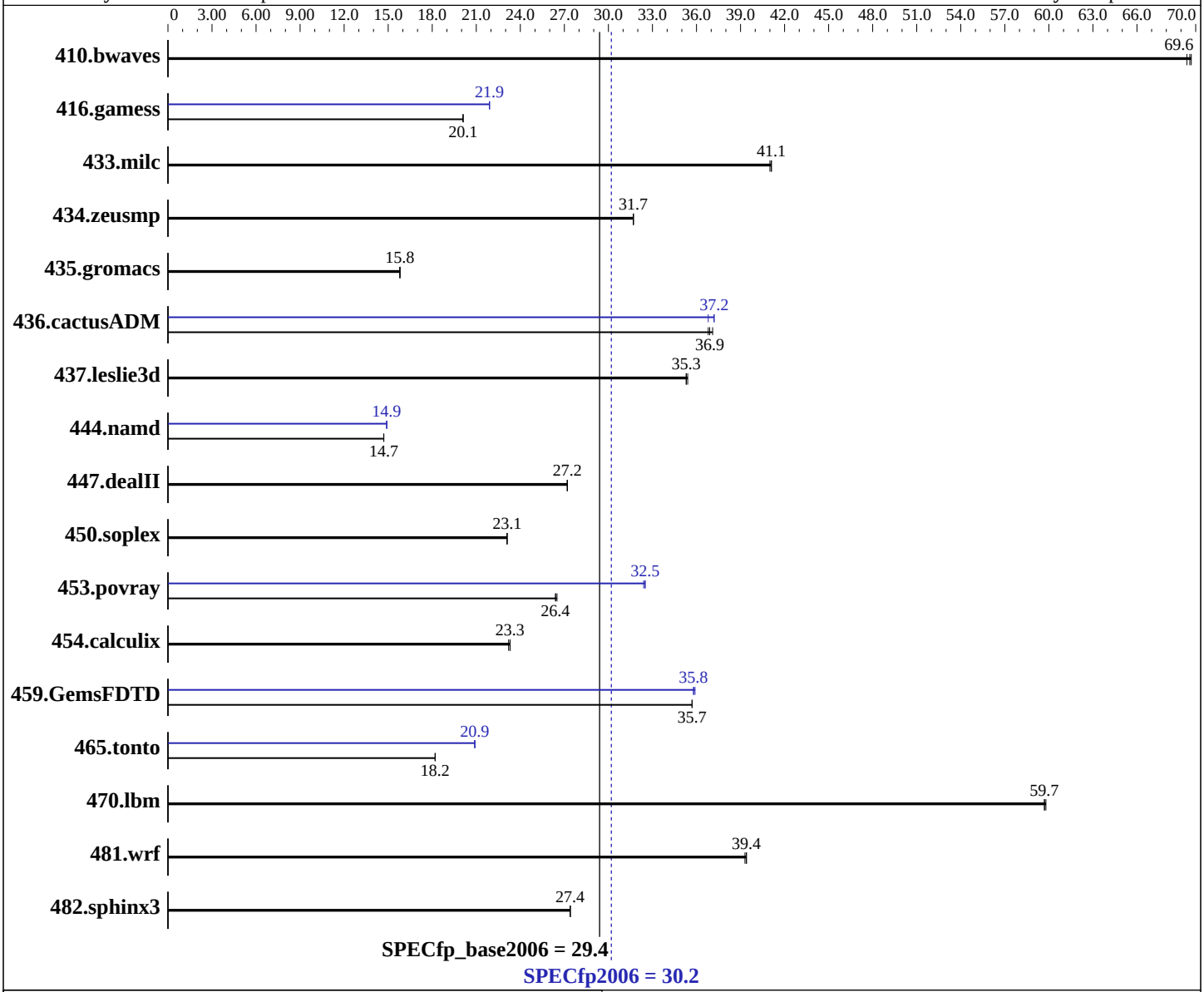
Intel DH67BLB3 Motherboard (Intel Pentium G620T)

SPECfp®2006 = 30.2

SPECfp_base2006 = 29.4

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jul-2011
Hardware Availability: May-2011
Software Availability: Apr-2011



Hardware

CPU Name:	Intel Pentium G620T
CPU Characteristics:	
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Windows 7 Ultimate (64-bit)
Compiler:	Intel C++ Compiler XE for Intel64 Version 12.0.3.176 Build 20110309 Intel Visual Fortran Compiler XE for Intel64 Version 12.0.3.176 Build 20110309 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS

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L3 Cache: 3 MB I+D on chip per chip
Other Cache: None
Memory: 4 GB (2 x 2 GB 2Rx8 PC3-10600U-9, running at 1066 MHz)
Disk Subsystem: Seagate 1 TB SATA, 7200 RPM
Other Hardware: None

System State: Default
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: SmartHeap Library Version 9.01 from http://www.microquill.com/

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**195**	**69.6**	195	69.7	196	69.4	**195**	**69.6**	195	69.7	196	69.4
416.gamess	973	20.1	974	20.1	**973**	**20.1**	894	21.9	**894**	**21.9**	894	21.9
433.milc	224	41.1	**224**	**41.1**	224	41.0	224	41.1	**224**	**41.1**	224	41.0
434.zeusmp	287	31.7	287	31.7	**287**	**31.7**	287	31.7	287	31.7	**287**	**31.7**
435.gromacs	451	15.8	**451**	**15.8**	451	15.8	451	15.8	**451**	**15.8**	451	15.8
436.cactusADM	324	36.8	**324**	**36.9**	322	37.1	325	36.8	322	37.2	**322**	**37.2**
437.leslie3d	266	35.4	266	35.3	**266**	**35.3**	266	35.4	266	35.3	**266**	**35.3**
444.namd	546	14.7	**546**	**14.7**	546	14.7	**537**	**14.9**	537	14.9	537	14.9
447.dealII	420	27.2	420	27.2	**420**	**27.2**	420	27.2	420	27.2	**420**	**27.2**
450.soplex	361	23.1	**361**	**23.1**	361	23.1	361	23.1	**361**	**23.1**	361	23.1
453.povray	201	26.4	**201**	**26.4**	201	26.5	164	32.4	164	32.5	**164**	**32.5**
454.calculix	355	23.2	**355**	**23.3**	355	23.3	355	23.2	**355**	**23.3**	355	23.3
459.GemsFDTD	297	35.7	**297**	**35.7**	297	35.7	296	35.9	296	35.8	**296**	**35.8**
465.tonto	540	18.2	540	18.2	**540**	**18.2**	470	20.9	470	20.9	**470**	**20.9**
470.lbm	230	59.8	230	59.7	**230**	**59.7**	230	59.8	230	59.7	**230**	**59.7**
481.wrf	284	39.3	283	39.4	**283**	**39.4**	284	39.3	283	39.4	**283**	**39.4**
482.sphinx3	712	27.4	712	27.4	**712**	**27.4**	712	27.4	712	27.4	**712**	**27.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 -names:lowercase
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -names:lowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -names:lowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias
-Qopt-prefetch -Qauto-ilp32 /F1000000000

C++ benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias
-Qopt-prefetch -Qcxx-features -Qauto-ilp32 /F1000000000 shlW64M.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias
-Qopt-prefetch /F1000000000

Benchmarks using both Fortran and C:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias
-Qopt-prefetch -Qauto-ilp32 /F1000000000

Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000
shlW64M.lib -link /FORCE:MULTIPLE

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias -Qauto-ilp32
/F1000000000 shlW64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias
-Qscalar-rep- /F1000000000

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qopt-prefetch -Qparallel /F1000000000

465.tonto: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto -Qinline-calloc /F1000000000

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qunroll2 -Qauto-ilp32 /F1000000000

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.html
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.xml
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.xml

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