



# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

**SPECfp®2006 = 42.6**

PowerEdge R710 (Intel Xeon L5630, 2.13 GHz)

**SPECfp\_base2006 = 39.7**

CPU2006 license: 55

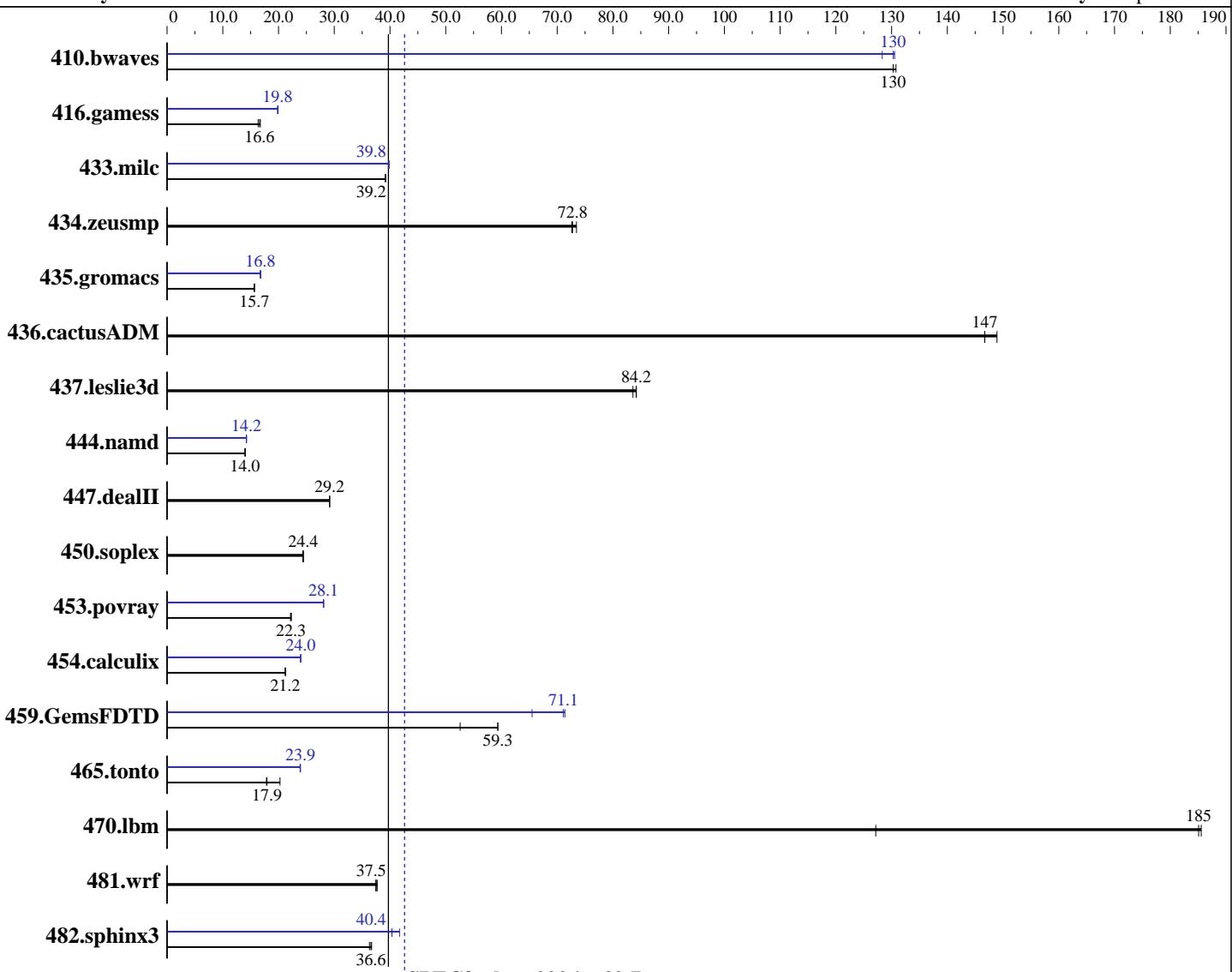
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Jul-2011

Hardware Availability: Mar-2010

Software Availability: Apr-2011



## Hardware

CPU Name: Intel Xeon L5630  
CPU Characteristics: Intel Turbo Boost Technology up to 2.40 GHz  
CPU MHz: 2133  
FPU: Integrated  
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
CPU(s) orderable: 1,2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3  
Auto Parallel: Yes  
File System: ext3  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC, running at 1066 MHz)  
 Disk Subsystem: 1 x 146 GB 15000 RPM SAS  
 Other Hardware: None

Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<b>104</b>	<b>130</b>	104	131	104	130	<b>104</b>	<b>130</b>	104	131	106	128
416.gamess	1198	16.3	<b>1179</b>	<b>16.6</b>	1170	16.7	<b>986</b>	<b>19.8</b>	983	19.9	987	19.8
433.milc	<b>234</b>	<b>39.2</b>	234	39.2	234	39.2	<b>231</b>	<b>39.8</b>	231	39.8	<b>231</b>	<b>39.8</b>
434.zeusmp	124	73.5	125	72.6	<b>125</b>	<b>72.8</b>	<b>124</b>	<b>73.5</b>	125	72.6	<b>125</b>	<b>72.8</b>
435.gromacs	<b>456</b>	<b>15.7</b>	456	15.7	456	15.7	<b>425</b>	<b>16.8</b>	426	16.7	<b>426</b>	<b>16.8</b>
436.cactusADM	<b>81.4</b>	<b>147</b>	80.3	149	81.5	147	<b>81.4</b>	<b>147</b>	80.3	149	81.5	147
437.leslie3d	112	84.2	112	83.6	<b>112</b>	<b>84.2</b>	<b>112</b>	<b>84.2</b>	112	83.6	<b>112</b>	<b>84.2</b>
444.namd	<b>573</b>	<b>14.0</b>	573	14.0	573	14.0	<b>564</b>	<b>14.2</b>	<b>563</b>	<b>14.2</b>	563	14.2
447.dealII	<b>392</b>	<b>29.2</b>	392	29.2	392	29.2	<b>392</b>	<b>29.2</b>	392	29.2	392	29.2
450.soplex	343	24.3	<b>341</b>	<b>24.4</b>	341	24.4	<b>343</b>	<b>24.3</b>	<b>341</b>	<b>24.4</b>	341	24.4
453.povray	239	22.3	<b>239</b>	<b>22.3</b>	240	22.1	<b>189</b>	<b>28.1</b>	<b>190</b>	<b>28.1</b>	190	28.0
454.calculix	388	21.3	<b>389</b>	<b>21.2</b>	389	21.2	<b>344</b>	<b>24.0</b>	<b>344</b>	<b>24.0</b>	344	24.0
459.GemsFDTD	202	52.6	<b>179</b>	<b>59.3</b>	179	59.4	<b>162</b>	<b>65.5</b>	149	71.4	<b>149</b>	<b>71.1</b>
465.tonto	<b>549</b>	<b>17.9</b>	552	17.8	486	20.3	<b>412</b>	<b>23.9</b>	411	23.9	<b>411</b>	<b>23.9</b>
470.lbm	<b>74.2</b>	<b>185</b>	108	127	74.0	186	<b>74.2</b>	<b>185</b>	108	127	74.0	186
481.wrf	298	37.5	296	37.7	<b>298</b>	<b>37.5</b>	298	37.5	296	37.7	<b>298</b>	<b>37.5</b>
482.sphinx3	537	36.3	531	36.7	<b>533</b>	<b>36.6</b>	<b>483</b>	<b>40.3</b>	467	41.7	<b>483</b>	<b>40.4</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

```
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

BIOS Settings:

Power Management = Maximum Performance (Default = Active Power Controller)  
 Logical Processor = Disabled (Default = Enabled)



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## General Notes

OMP\_NUM\_THREADS set to number of cores

The Dell PowerEdge R710 and

the Bull NovaScale R460 F2 models are electronically equivalent.

The results have been measured on a Dell PowerEdge R710 model

Binaries were compiled on RHEL5.5

## Base Compiler Invocation

C benchmarks:

    icc -m64

C++ benchmarks:

    icpc -m64

Fortran benchmarks:

    ifort -m64

Benchmarks using both Fortran and C:

    icc -m64 ifort -m64

## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

    -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
 -ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfsl -Wl,-melf\_x86\_64 -Wl,-hugetlbfsl-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel  
-B /usr/share/libhugetlbfsl -Wl,-melf\_x86\_64 -Wl,-hugetlbfsl-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4  
-B /usr/share/libhugetlbfsl -Wl,-melf\_x86\_64 -Wl,-hugetlbfsl-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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