Fujitsu

PRIMERGY TX100 S3, Intel Core i3-2120, 3.30 GHz

SPECint\_rate2006 = 82.5

SPECint\_rate_base2006 = 79.3

Hardware

- CPU Name: Intel Core i3-2120
- CPU Characteristics: 3300
- FPU: Integrated
- CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
- CPU(s) orderable: 1 chip
- Primary Cache: 32 KB I + 32 KB D on chip per core
- Secondary Cache: 256 KB I+D on chip per core
- L3 Cache: 3 MB I+D on chip per chip
- Other Cache: None
- Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)
- Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
- Other Hardware: None

Software

- Operating System: SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12.0.7-default
- Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
- Auto Parallel: No
- File System: ext3
- System State: Run level 3 (multi-user)
- Base Pointers: 32-bit
- Peak Pointers: 32/64-bit
- Other Software: Microquill SmartHeap V9.01
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SPECint_rate_base2006 = 79.3

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>4</td>
<td>620</td>
<td>63.1</td>
<td>618</td>
<td>63.2</td>
<td><strong>619</strong></td>
<td><strong>63.1</strong></td>
<td>4</td>
<td>509</td>
<td>76.8</td>
<td>509</td>
<td>76.8</td>
<td>509</td>
<td>76.7</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>4</td>
<td><strong>883</strong></td>
<td><strong>43.7</strong></td>
<td>884</td>
<td>43.7</td>
<td>883</td>
<td>43.7</td>
<td>4</td>
<td>839</td>
<td>46.0</td>
<td><strong>831</strong></td>
<td><strong>46.5</strong></td>
<td>831</td>
<td>46.5</td>
</tr>
<tr>
<td>403.gcc</td>
<td>4</td>
<td>458</td>
<td>70.3</td>
<td><strong>462</strong></td>
<td><strong>69.6</strong></td>
<td>465</td>
<td>69.3</td>
<td>4</td>
<td>458</td>
<td>70.3</td>
<td><strong>462</strong></td>
<td><strong>69.6</strong></td>
<td>465</td>
<td>69.3</td>
</tr>
<tr>
<td>429.mcf</td>
<td>4</td>
<td>441</td>
<td>82.7</td>
<td><strong>438</strong></td>
<td><strong>83.3</strong></td>
<td>437</td>
<td>83.6</td>
<td>2</td>
<td>203</td>
<td>89.9</td>
<td>208</td>
<td>87.5</td>
<td><strong>204</strong></td>
<td><strong>89.5</strong></td>
</tr>
<tr>
<td>445.gobmk</td>
<td>4</td>
<td>634</td>
<td>66.2</td>
<td>635</td>
<td>66.0</td>
<td><strong>635</strong></td>
<td><strong>66.1</strong></td>
<td>4</td>
<td>630</td>
<td>66.6</td>
<td><strong>625</strong></td>
<td><strong>67.2</strong></td>
<td>624</td>
<td>67.2</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>4</td>
<td>353</td>
<td>106</td>
<td>352</td>
<td>106</td>
<td>354</td>
<td>105</td>
<td>4</td>
<td>353</td>
<td>106</td>
<td>352</td>
<td>106</td>
<td>354</td>
<td>105</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>4</td>
<td>765</td>
<td>63.2</td>
<td>764</td>
<td>63.4</td>
<td><strong>765</strong></td>
<td><strong>63.3</strong></td>
<td>4</td>
<td><strong>735</strong></td>
<td><strong>65.9</strong></td>
<td>741</td>
<td>65.3</td>
<td>733</td>
<td>66.0</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>4</td>
<td><strong>215</strong></td>
<td><strong>386</strong></td>
<td>213</td>
<td>389</td>
<td>215</td>
<td>385</td>
<td>4</td>
<td><strong>215</strong></td>
<td><strong>386</strong></td>
<td>213</td>
<td>389</td>
<td>215</td>
<td>385</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>4</td>
<td><strong>826</strong></td>
<td><strong>107</strong></td>
<td>825</td>
<td>107</td>
<td>828</td>
<td>107</td>
<td>4</td>
<td>769</td>
<td>115</td>
<td>816</td>
<td>108</td>
<td><strong>804</strong></td>
<td><strong>110</strong></td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>4</td>
<td><strong>485</strong></td>
<td><strong>51.6</strong></td>
<td>488</td>
<td>51.2</td>
<td>484</td>
<td>51.7</td>
<td>4</td>
<td>457</td>
<td>54.7</td>
<td>455</td>
<td>55.0</td>
<td><strong>455</strong></td>
<td><strong>54.9</strong></td>
</tr>
<tr>
<td>473.astar</td>
<td>4</td>
<td>563</td>
<td>49.9</td>
<td>560</td>
<td>50.1</td>
<td><strong>562</strong></td>
<td><strong>49.9</strong></td>
<td>4</td>
<td>563</td>
<td>49.9</td>
<td>560</td>
<td>50.1</td>
<td><strong>562</strong></td>
<td><strong>49.9</strong></td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>4</td>
<td><strong>335</strong></td>
<td><strong>82.3</strong></td>
<td>334</td>
<td>82.6</td>
<td>336</td>
<td>82.3</td>
<td>4</td>
<td><strong>335</strong></td>
<td><strong>82.3</strong></td>
<td>334</td>
<td>82.6</td>
<td>336</td>
<td>82.3</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
Large pages were not enabled for this run

General Notes
For information about Fujitsu please visit: http://www.fujitsu.com
Binaries were compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:
icc  -m32

C++ benchmarks:
icpc -m32
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Test date: Jun-2011
Hardware Availability: Sep-2011
Software Availability: Jan-2011

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32
400.perlbench: icc -m64
401.bzip2: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page
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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -prof-use(pass 2)
  -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -prof-use(pass 2)
  -opt-prefetch -auto-ilp32 -ansi-alias
  -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -prof-use(pass 2)
  -ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
  -ansi-alias -auto-ilp32

456.hmmer: basepeak = yes

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -prof-use(pass 2)
  -unroll4 -auto-ilp32
  -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -prof-use(pass 2)
  -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
  -prof-use(pass 2)
  -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
  -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes
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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.xml

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