



SPEC® CINT2006 Result

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SGI

SGI Rackable C2005-TY3 (Intel Xeon X5687, 3.60 GHz)

SPECint®2006 = 49.3

SPECint_base2006 = 47.0

CPU2006 license: 4

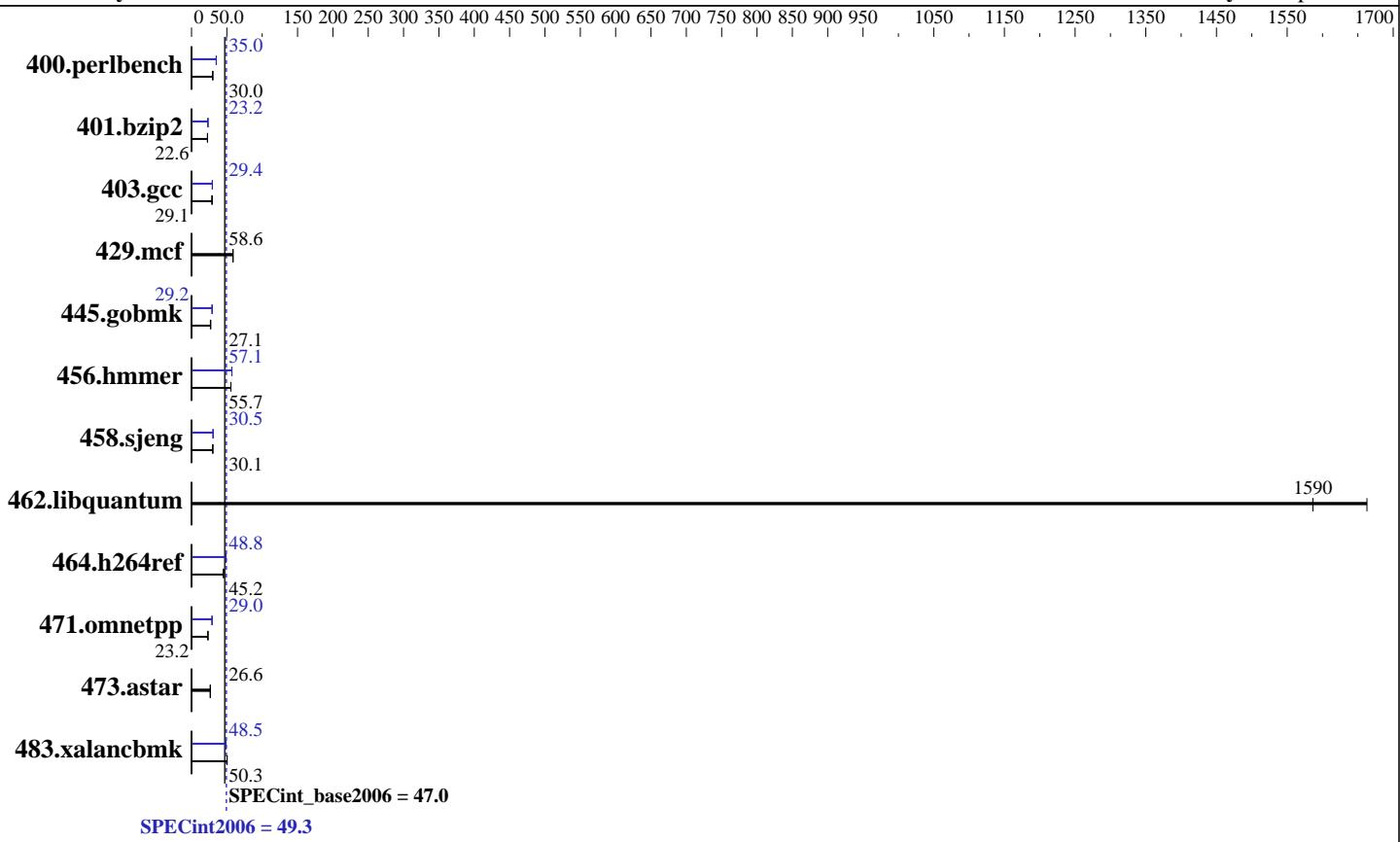
Test sponsor: SGI

Tested by: SGI

Test date: Aug-2011

Hardware Availability: Feb-2011

Software Availability: Sep-2011



Hardware

CPU Name: Intel Xeon X5687
CPU Characteristics: Intel Turbo Boost Technology up to 3.86 GHz
CPU MHz: 3600
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (6 x 8 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 876 GB RAID 5
Other Hardware: 6 x 146 GB SAS, 15000 RPM

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP1, kernel 2.6.32.36-0.5-default
Compiler: Intel C++ Compiler XE for applications running on IA32 and Intel 64 12.1.0.225 Build 20110803
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	326	30.0	325	30.0	325	30.0	280	34.9	279	35.0	279	35.0
401.bzip2	427	22.6	427	22.6	427	22.6	415	23.2	415	23.2	415	23.2
403.gcc	277	29.1	277	29.1	277	29.0	273	29.4	274	29.4	274	29.4
429.mcf	156	58.6	155	58.8	156	58.6	156	58.6	155	58.8	156	58.6
445.gobmk	386	27.2	394	26.7	388	27.1	360	29.1	360	29.2	360	29.2
456.hmmer	167	55.7	167	55.7	167	55.7	163	57.1	163	57.1	163	57.1
458.sjeng	403	30.1	403	30.1	402	30.1	396	30.5	396	30.5	396	30.5
462.libquantum	13.1	1590	12.5	1660	13.1	1590	13.1	1590	12.5	1660	13.1	1590
464.h264ref	489	45.3	489	45.2	490	45.2	454	48.8	454	48.8	454	48.8
471.omnetpp	270	23.2	269	23.3	269	23.2	215	29.0	215	29.0	215	29.1
473.astar	264	26.6	264	26.6	264	26.6	264	26.6	264	26.6	264	26.6
483.xalancbmk	137	50.3	137	50.5	137	50.2	142	48.5	142	48.5	143	48.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/scratch/cma/cpu2006-1.1/smartheap:/scratch/cma/cpu2006-1.1/icl2.1-libs/ia32:/scratch/cma/cpu2006-1.1/icl2.1-libs/intel64"

OMP_NUM_THREADS = "8"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory
using RHEL5.5 with binutils-2.17.50.0.6-14.el5
Stack size set to unlimited using "ulimit -s unlimited"

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

445.gobmk: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/smartheap -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
   403.gcc: -DSPEC_CPU_LP64
   429.mcf: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
   473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
               -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc
          -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32
               -ansi-alias

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll14

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll12 -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs
               -L/smartheap -lsmartheap

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Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html>

<http://www.spec.org/cpu2006/flags/platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.xml>

<http://www.spec.org/cpu2006/flags/platform.xml>

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For other inquiries, please contact webmaster@spec.org.

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