



SPEC® CINT2006 Result

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Fujitsu

SPECint®_rate2006 = 50.8

PRIMERGY TX120 S3, Intel Celeron G530, 2.40 GHz

SPECint_rate_base2006 = 48.0

CPU2006 license: 19

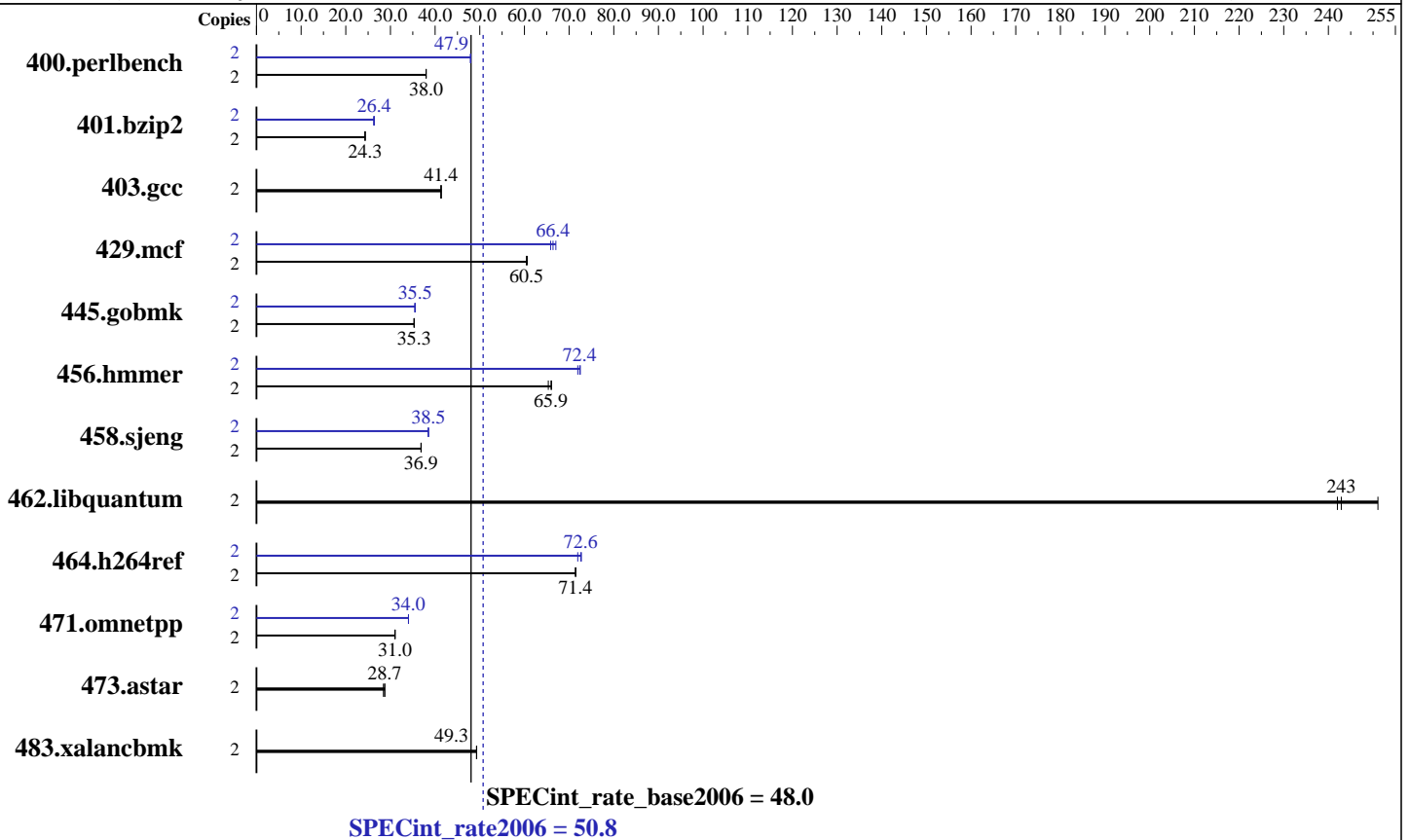
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Sep-2011

Hardware Availability: Sep-2011

Software Availability: Jan-2011



Hardware

CPU Name: Intel Celeron G530
 CPU Characteristics:
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 2 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC, running at 1067 MHz and CL7)
 Disk Subsystem: 1 x SATA, 300 GB, 7200 RPM
 Other Hardware: --

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	514	38.0	<u>514</u>	<u>38.0</u>	515	38.0	2	407	48.0	<u>408</u>	<u>47.9</u>	408	47.9
401.bzip2	2	<u>794</u>	<u>24.3</u>	792	24.4	795	24.3	2	736	26.2	731	26.4	<u>732</u>	<u>26.4</u>
403.gcc	2	390	41.3	389	41.4	<u>389</u>	<u>41.4</u>	2	390	41.3	389	41.4	<u>389</u>	<u>41.4</u>
429.mcf	2	301	60.7	<u>302</u>	<u>60.5</u>	302	60.4	2	277	65.9	<u>275</u>	<u>66.4</u>	272	67.0
445.gobmk	2	594	35.3	<u>594</u>	<u>35.3</u>	595	35.3	2	590	35.6	592	35.4	<u>591</u>	<u>35.5</u>
456.hammer	2	286	65.3	<u>283</u>	<u>65.9</u>	282	66.1	2	259	71.9	257	72.5	<u>258</u>	<u>72.4</u>
458.sjeng	2	<u>657</u>	<u>36.9</u>	657	36.8	656	36.9	2	628	38.5	629	38.5	<u>629</u>	<u>38.5</u>
462.libquantum	2	171	242	165	251	<u>171</u>	<u>243</u>	2	171	242	165	251	<u>171</u>	<u>243</u>
464.h264ref	2	620	71.4	619	71.5	<u>620</u>	<u>71.4</u>	2	615	71.9	608	72.8	<u>610</u>	<u>72.6</u>
471.omnetpp	2	403	31.1	403	31.0	<u>403</u>	<u>31.0</u>	2	<u>367</u>	<u>34.0</u>	367	34.0	367	34.0
473.astar	2	489	28.7	494	28.4	<u>489</u>	<u>28.7</u>	2	489	28.7	494	28.4	<u>489</u>	<u>28.7</u>
483.xalancbmk	2	280	49.3	<u>280</u>	<u>49.3</u>	280	49.3	2	280	49.3	<u>280</u>	<u>49.3</u>	280	49.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
Large pages were not enabled for this run

General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

456.hmmcr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzp2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll14 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

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Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110316.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110316.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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