



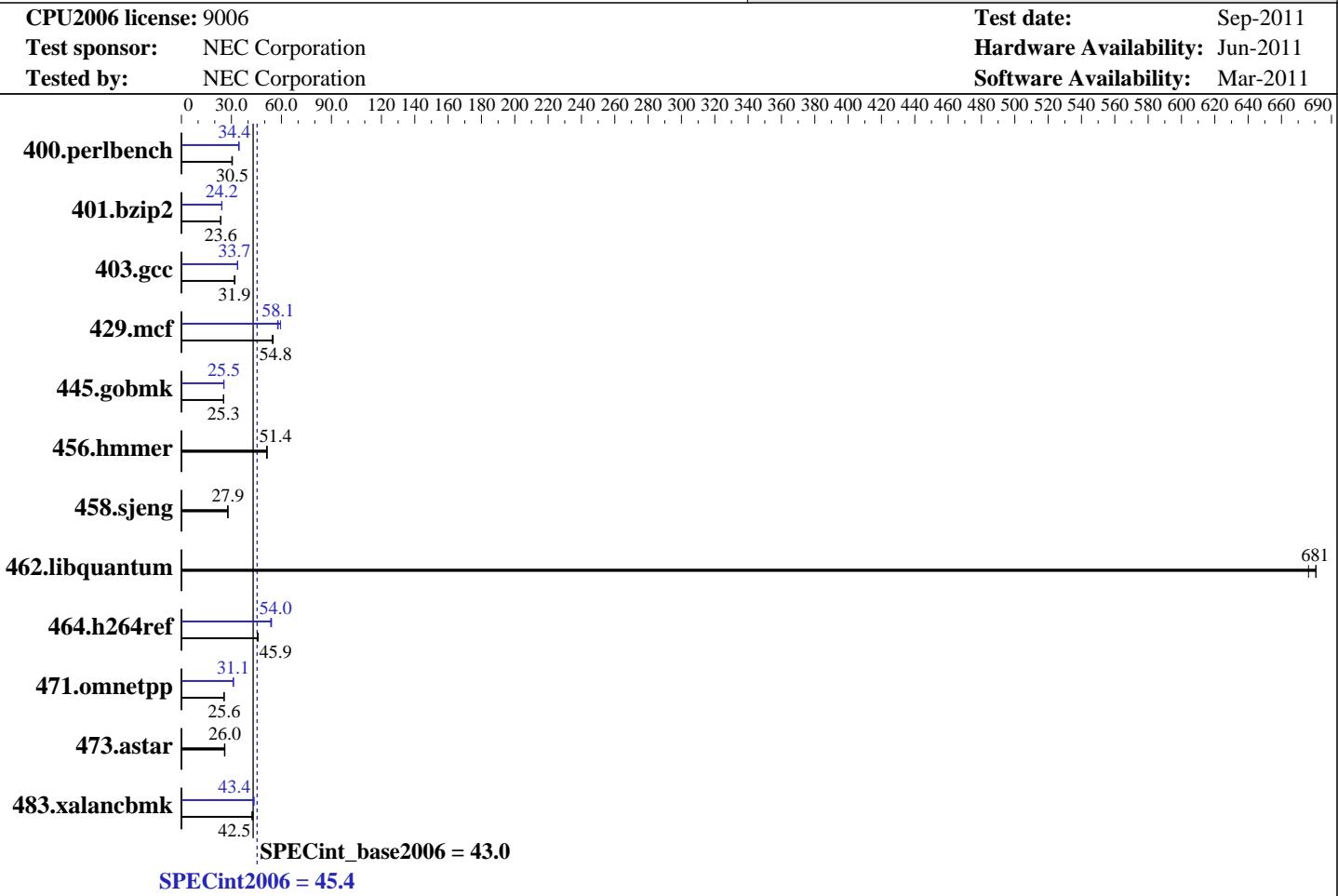
SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/R110d-1E (Intel Xeon E3-1220)

SPECint®2006 = 45.4



Hardware

CPU Name: Intel Xeon E3-1220
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz
 CPU MHz: 3100
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)
 Disk Subsystem: 1 x 160 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Intel 64 Compiler XE for applications running on Intel 64, Version 12.0.3.174 Build 20110309
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/R110d-1E (Intel Xeon E3-1220)

SPECint2006 = 45.4

CPU2006 license: 9006

Test date: Sep-2011

Test sponsor: NEC Corporation

Hardware Availability: Jun-2011

Tested by: NEC Corporation

Software Availability: Mar-2011

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	323	30.2	320	30.5	320	30.5	284	34.4	282	34.6	284	34.4
401.bzip2	409	23.6	410	23.5	409	23.6	399	24.2	397	24.3	398	24.2
403.gcc	253	31.9	253	31.8	250	32.2	239	33.7	239	33.7	239	33.7
429.mcf	166	55.1	166	54.8	167	54.5	157	58.1	153	59.5	158	57.7
445.gobmk	416	25.2	414	25.3	414	25.3	412	25.5	412	25.5	412	25.5
456.hammer	181	51.4	181	51.4	183	51.1	181	51.4	181	51.4	183	51.1
458.sjeng	436	27.7	434	27.9	434	27.9	436	27.7	434	27.9	434	27.9
462.libquantum	30.6	676	30.4	681	30.4	681	30.6	676	30.4	681	30.4	681
464.h264ref	483	45.9	481	46.0	485	45.6	410	54.0	413	53.6	410	54.0
471.omnetpp	244	25.6	244	25.6	244	25.6	201	31.0	201	31.1	200	31.2
473.astar	270	26.0	270	26.0	272	25.8	270	26.0	270	26.0	272	25.8
483.xalancbmk	163	42.3	162	42.5	161	42.9	159	43.5	159	43.4	159	43.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 1800 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

Default BIOS settings were used.

General Notes

OMP_NUM_THREADS set to number of cores

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/R110d-1E (Intel Xeon E3-1220)

SPECint2006 = 45.4

CPU2006 license: 9006

Test date: Sep-2011

Test sponsor: NEC Corporation

Hardware Availability: Jun-2011

Tested by: NEC Corporation

Software Availability: Mar-2011

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64  
401.bzip2: -DSPEC_CPU_LP64  
403.gcc: -DSPEC_CPU_LP64  
429.mcf: -DSPEC_CPU_LP64  
445.gobmk: -DSPEC_CPU_LP64  
456.hmmer: -DSPEC_CPU_LP64  
458.sjeng: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX  
464.h264ref: -DSPEC_CPU_LP64  
471.omnetpp: -DSPEC_CPU_LP64  
473.astar: -DSPEC_CPU_LP64  
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/opt/SmartHeap_8.1/lib64 -lsmartheap64  
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
400.perlbench: icc -m32
```

```
429.mcf: icc -m32
```

```
445.gobmk: icc -m32
```

```
464.h264ref: icc -m32
```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/R110d-1E (Intel Xeon E3-1220)

SPECint2006 = 45.4

SPECint_base2006 = 43.0

CPU2006 license: 9006

Test date: Sep-2011

Test sponsor: NEC Corporation

Hardware Availability: Jun-2011

Tested by: NEC Corporation

Software Availability: Mar-2011

Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
               -ansi-alias
               -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
               -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
          -opt-malloc-options=3 -auto-ilp32
          -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

429.mcf: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
          -ansi-alias
          -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -auto-ilp32 -ansi-alias
               -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

456.hmmer: basepeak = yes

458.sjeng: basepeak = yes
```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/R110d-1E (Intel Xeon E3-1220)

SPECint2006 = 45.4

CPU2006 license: 9006

Test date: Sep-2011

Test sponsor: NEC Corporation

Hardware Availability: Jun-2011

Tested by: NEC Corporation

Software Availability: Mar-2011

Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

```
464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
              -ansi-alias
              -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

C++ benchmarks:

```
471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2)
              -opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs
              -L/opt/SmartHeap_8.1/lib -lsmartheap
              -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

473.astar: basepeak = yes

```
483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
                 -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap
                 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>
<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revF.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>
<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revF.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 21:39:05 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 27 September 2011.