SPEC® CINT2006 Result

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Intel Corporation

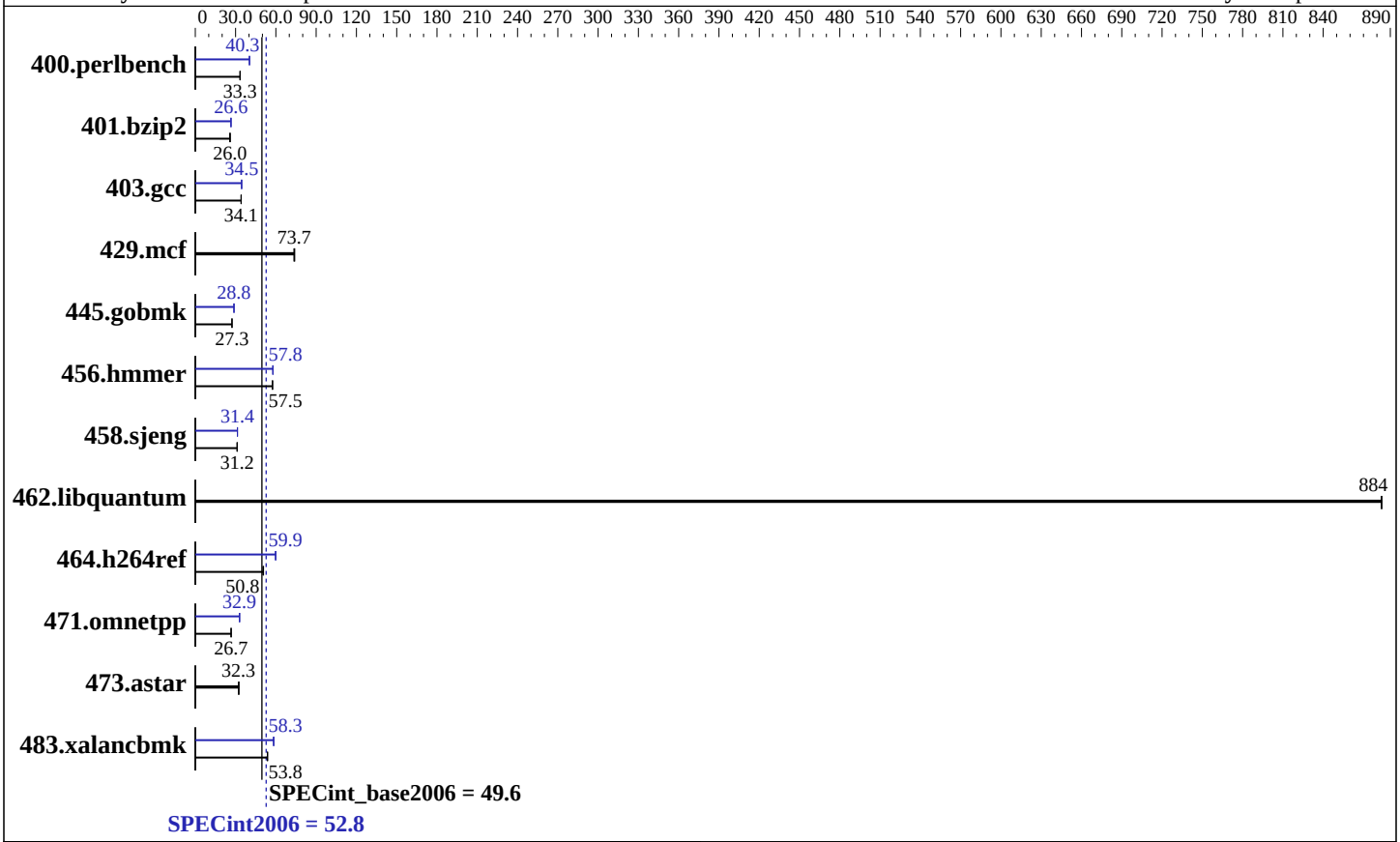
Intel DZ68DB motherboard (Intel Core i7-2600K)

SPECint®2006 = 52.8

SPECint_base2006 = 49.6

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Aug-2011
Hardware Availability: May-2011
Software Availability: Sep-2011



Hardware

CPU Name:	Intel Core i7-2600K
CPU Characteristics:	Intel Turbo Boost Technology up to 3.8 GHz
CPU MHz:	3401
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (2 x 4 GB 2Rx8 PC3-10600U-9)
Disk Subsystem:	1 TB Seagate SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.1 2.6.32-131.0.15.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 5 (X11)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	293	33.3	**293**	**33.3**	293	33.4	**242**	**40.3**	242	40.3	242	40.3
401.bzip2	377	25.6	371	26.0	**371**	**26.0**	**363**	**26.6**	363	26.6	363	26.6
403.gcc	235	34.2	**236**	**34.1**	236	34.1	**233**	**34.5**	234	34.5	233	34.5
429.mcf	123	74.1	**124**	**73.7**	124	73.5	123	74.1	**124**	**73.7**	124	73.5
445.gobmk	384	27.3	384	27.3	**384**	**27.3**	364	28.8	**364**	**28.8**	364	28.8
456.hmmer	162	57.5	162	57.5	**162**	**57.5**	161	57.8	**161**	**57.8**	162	57.8
458.sjeng	388	31.2	**388**	**31.2**	388	31.2	385	31.4	386	31.4	**386**	**31.4**
462.libquantum	23.5	883	**23.5**	**884**	23.4	884	23.5	883	**23.5**	**884**	23.4	884
464.h264ref	**436**	**50.8**	440	50.3	435	50.8	369	59.9	**370**	**59.9**	371	59.7
471.omnetpp	**234**	**26.7**	235	26.6	234	26.7	190	32.9	**190**	**32.9**	189	33.0
473.astar	**218**	**32.3**	218	32.2	216	32.5	**218**	**32.3**	218	32.2	216	32.5
483.xalancbmk	128	53.9	**128**	**53.8**	129	53.7	118	58.3	**118**	**58.3**	118	58.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
Sysinfo program /home/spec/cpu2006.1.2/Docs/sysinfo
$Rev: 6775 $ $Date:: 2011-08-16 #$ 8787f7622badcf24e01c368b1db4377c
running on rhel61-rahul.sc.intel.com Fri Aug 19 09:55:05 2011

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Core(TM) i7-2600K CPU @ 3.40GHz
      1 "physical id"s (chips)
      8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 8
      physical 0: cores 0 1 2 3
   cache size : 8192 KB

From /proc/meminfo
   MemTotal:        7966960 kB
   HugePages_Total:       0
```

Continued on next page

Platform Notes (Continued)

```
      Hugepagesize:       2048 kB

   /usr/bin/lsb_release -d
      Red Hat Enterprise Linux Server release 6.1 (Santiago)

   From /etc/*release* /etc/*version*
      redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
      system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
      system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

   uname -a:
      Linux rhel61-rahul.sc.intel.com 2.6.32-131.0.15.el6.x86_64 #1 SMP Tue May 10
      15:42:40 EDT 2011 x86_64 x86_64 x86_64 GNU/Linux

   run-level 5 Aug 19 09:53

   SPEC is set to: /home/spec/cpu2006.1.2
      Filesystem     Type    Size  Used Avail Use% Mounted on
      /dev/mapper/vg_rhel61rahul-lv_home
                     ext4    862G  3.7G  815G   1% /home

   (End of data from sysinfo program)
```

Component Notes

```
Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/spec/cpu2006.1.2/smartheap:/home/spec/cpu2006.1.2/ic12.1-libs/ia32/:/home/spec/cpu2006.1.2/ic12.1-libs/intel64"
OMP_NUM_THREADS = "4"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory
 using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:

```
icc  -m64
```

C++ benchmarks:

```
icpc -m64
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs
-L/smartheap -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32 -ansi-alias

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs -L/smartheap -lsmartheap

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Peak Optimization Flags (Continued)

473.astar: `basepeak = yes`

483.xalancbmk: `-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -Wl,-z,muldefs -L/smartheap -lsmartheap`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.html`
`http://www.spec.org/cpu2006/flags/Intel-Platform-Settings-V1.2-revA.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.xml`
`http://www.spec.org/cpu2006/flags/Intel-Platform-Settings-V1.2-revA.xml`

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Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 01:43:02 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 October 2011.