SPEC® CINT2006 Result

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NEC Corporation

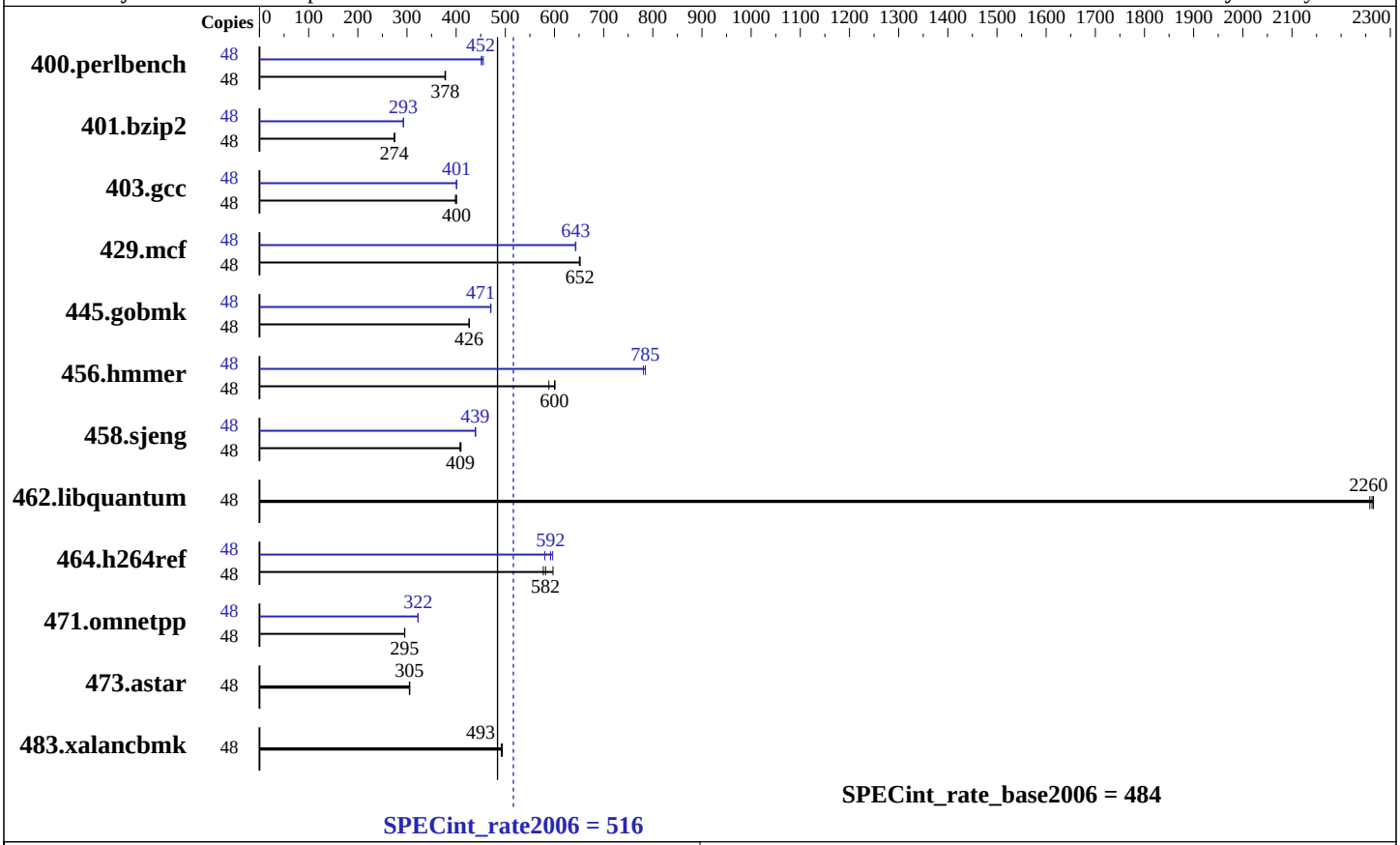
Express5800/A1080a-D (Intel Xeon E7-4807)

SPECint®_rate2006 = 516

SPECint_rate_base2006 = 484

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Aug-2011
Hardware Availability: Nov-2011
Software Availability: May-2011



Hardware

CPU Name:	Intel Xeon E7-4807
CPU Characteristics:	
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	24 cores, 4 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,3,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	18 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (64 x 4 GB 2Rx4 PC3-8500R-7, ECC)
Disk Subsystem:	2x300 GB SAS, 10000 RPM, RAID 0
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64 on an x86_64
Compiler:	C/C++: Version 12.0.4.191 of Intel Compiler XE for applications on IA-32 Build 20110427
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	48	1240	378	**1240**	**378**	1243	377	48	1030	455	**1038**	**452**	1039	451
401.bzip2	48	**1688**	**274**	1689	274	1683	275	48	**1582**	**293**	1581	293	1583	293
403.gcc	48	**965**	**400**	965	401	970	398	48	963	401	**964**	**401**	965	401
429.mcf	48	671	652	673	651	**672**	**652**	48	**681**	**643**	680	644	681	643
445.gobmk	48	1181	426	**1181**	**426**	1180	427	48	**1070**	**471**	1071	470	1070	471
456.hmmer	48	745	601	761	589	**746**	**600**	48	**571**	**785**	571	785	574	781
458.sjeng	48	1417	410	1424	408	**1420**	**409**	48	1323	439	1321	440	**1323**	**439**
462.libquantum	48	439	2270	**440**	**2260**	440	2260	48	439	2270	**440**	**2260**	440	2260
464.h264ref	48	**1826**	**582**	1840	577	1779	597	48	1830	580	**1794**	**592**	1781	596
471.omnetpp	48	**1016**	**295**	1017	295	1015	295	48	929	323	**930**	**322**	931	322
473.astar	48	1106	305	1103	306	**1104**	**305**	48	1106	305	1103	306	**1104**	**305**
483.xalancbmk	48	674	492	670	494	**672**	**493**	48	674	492	670	494	**672**	**493**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
echo 1 > /proc/sys/vm/zone_reclaim_mode
Huge pages were not configured for this run
```

Platform Notes

```
Patrol Scrubbing set to disabled in Maintenance Console
```

General Notes

```
The Express5800/A1080a-S and the Express5800/A1080a-D models are electronically equivalent.
The results have been measured on the Express5800/A1080a-S model.
Binaries were compiled on RHEL 5.6
```

Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/SmartHeap_9/lib -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/opt/SmartHeap_9/lib -lsmartheap

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Peak Optimization Flags (Continued)

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110705.html`
`http://www.spec.org/cpu2006/flags/NEC-platform-linux64-revB.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110705.xml`
`http://www.spec.org/cpu2006/flags/NEC-platform-linux64-revB.xml`

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