SPEC® CINT2006 Result

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Intel Corporation

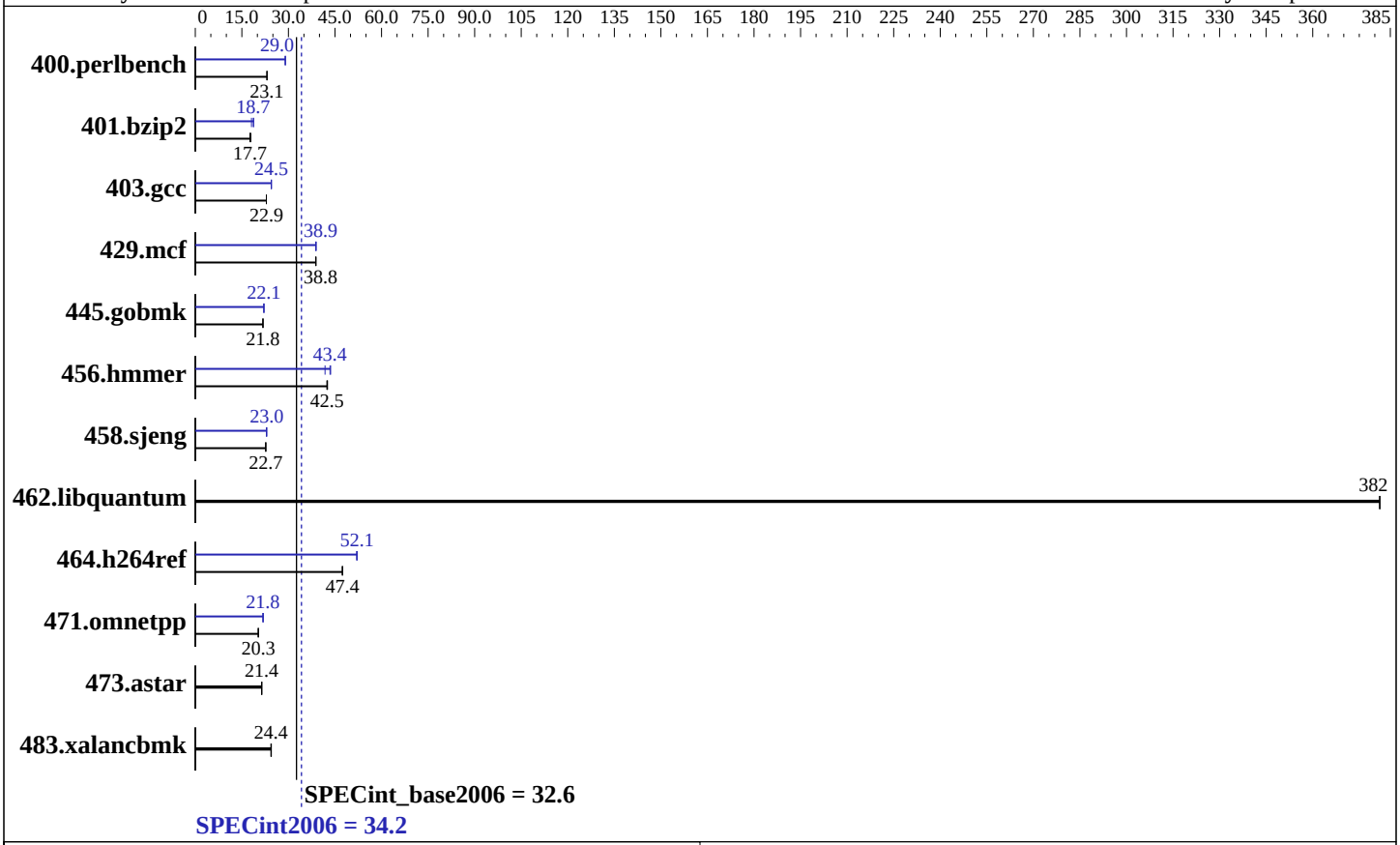
Intel DH61WW motherboard (Intel Pentium G850)

SPECint®2006 = 34.2

SPECint_base2006 = 32.6

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Oct-2011
Hardware Availability: May-2011
Software Availability: Apr-2011



Hardware

CPU Name:	Intel Pentium G850
CPU Characteristics:	
CPU MHz:	2900
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	3 MB I+D on chip per chip
Other Cache:	None
Memory:	4 GB (2 x 2 GB 2Rx8 PC3-10600U-9)
Disk Subsystem:	Seagate 250GB HDD, 7200 rpm
Other Hardware:	None

Software

Operating System:	Windows 7 Ultimate SP1 (64-bit)
Compiler:	C/C++: Version 12.0.3.176 of Intel C++ Studio XE for Windows; Libraries: Version 15.00.30729.01 of Microsoft Visual Studio 2008 Professional SP1
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap Library Version 9.01 from http://www.microquill.com/

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	423	23.1	**423**	**23.1**	424	23.1	337	29.0	337	29.0	**337**	**29.0**
401.bzip2	538	17.9	550	17.6	**546**	**17.7**	532	18.1	**515**	**18.7**	513	18.8
403.gcc	351	22.9	351	22.9	**351**	**22.9**	**329**	**24.5**	328	24.5	329	24.5
429.mcf	235	38.8	235	38.9	**235**	**38.8**	235	38.9	**235**	**38.9**	235	38.8
445.gobmk	**481**	**21.8**	481	21.8	481	21.8	474	22.1	474	22.1	**474**	**22.1**
456.hmmer	219	42.6	220	42.4	**219**	**42.5**	**215**	**43.4**	214	43.6	223	41.8
458.sjeng	533	22.7	533	22.7	**533**	**22.7**	527	22.9	527	23.0	**527**	**23.0**
462.libquantum	54.3	382	**54.3**	**382**	54.3	382	54.3	382	**54.3**	**382**	54.3	382
464.h264ref	467	47.4	**467**	**47.4**	467	47.4	425	52.0	425	52.1	**425**	**52.1**
471.omnetpp	308	20.3	309	20.3	**308**	**20.3**	286	21.9	**286**	**21.8**	286	21.8
473.astar	**328**	**21.4**	328	21.4	328	21.4	**328**	**21.4**	328	21.4	328	21.4
483.xalancbmk	282	24.5	**282**	**24.4**	283	24.4	282	24.5	**282**	**24.4**	283	24.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Component Notes

```
Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
```

General Notes

```
OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64 -DSPEC_CPU_NO_NEED_VA_COPY`
401.bzip2: `-DSPEC_CPU_P64`
403.gcc: `-DSPEC_CPU_P64 -DSPEC_CPU_WIN64`

Continued on next page

Base Portability Flags (Continued)

429.mcf: -DSPEC_CPU_P64
445.gobmk: -DSPEC_CPU_P64
456.hmmer: -DSPEC_CPU_P64
458.sjeng: -DSPEC_CPU_P64
462.libquantum: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qauto-ilp32 /F512000000

C++ benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
-Qauto-ilp32 /F512000000 shlW64M.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

400.perlbench: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch -Qauto-ilp32 /F512000000 shlW64M.lib -link /FORCE:MULTIPLE

401.bzip2: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias -Qauto-ilp32 /F512000000

403.gcc: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qauto-ilp32 /F512000000

429.mcf: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32 /F512000000

445.gobmk: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F512000000

456.hmmer: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qauto-ilp32 /F512000000

458.sjeng: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto-ilp32 /F512000000

462.libquantum: basepeak = yes

464.h264ref: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qauto-ilp32 /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block -Qauto-ilp32 /F512000000 shlW64M.lib -link /FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page

Peak Other Flags (Continued)

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.20111012.html`
`http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings-revC.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.20111012.xml`
`http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings-revC.xml`

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