SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability of the system.
SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability of the system.

### Results Table

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</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The config file option 'submit' was used. numactl was used to bind copies to the cores.

Non-Compliant
Hewlett-Packard Company

ProLiant DL380 G7
(2.26 GHz, Intel Xeon E5607)

SPECfp_rate2006 = NC
SPECfp_rate_base2006 = NC

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Oct-2011
Hardware Availability: Feb-2011
Software Availability: Sep-2011

SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability of the system.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages disabled with:
echo never > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Platform Notes

BIOS configuration:
HP Power Profile set to Maximum Performance
Thermal Configuration set to Increased Cooling
Data Reuse set to Disabled

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006/smartheap:/cpu2006/ic12.1-libs/ia32/:/cpu2006/ic12.1-libs/intel64"

Base Compiler Invocation

C benchmarks:

C++ benchmarks:
icc   -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc   -m64 ifort -m64
SPEC CFP2006 Result

Hewlett-Packard Company
ProLiant DL380 G7
(2.26 GHz, Intel Xeon E5607)

SPECfp_rate2006 = NC
SPECfp_rate_base2006 = NC

CPU2006 license: 3
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Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.lesie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
463.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

 Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Non-Compliant
SPEC CFP2006 Result

Hewlett-Packard Company

ProLiant DL380 G7
(2.26 GHz, Intel Xeon E5607)

SPECfp_rate2006 = NC
SPECfp_rate_base2006 = NC

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability of the system.

Peak Compiler Invocation

C benchmarks (except as noted below):
   icc -m64
   482.sphinx3: icc -m32

C++ benchmarks (except as noted below):
   icpc -m64
   450.soplex: icpc -m32

Fortran benchmarks:
   ifort -m64

Benchmarks using both Fortran and C:
   icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
415.games: -DSPEC_CPU_LP64
418.milc: -DSPEC_CPU_LP64
434.zeeomp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
433.milc: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability of the system.

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2) -O3 (pass 2)
-\-no-prec-div (pass 2) -opt-mem-layout-trans=3 (pass 2)
-\-prof-use (pass 2) -static -\-to-ill32

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -\-no-prec-div -\-opt-mem-layout-trans=3
-\-unroll12

C++ benchmarks:

444.namd: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2) -O3 (pass 2)
-\-no-prec-div (pass 2) -opt-mem-layout-trans=3 (pass 2)
-\-prof-use (pass 2) -\-fno-alias -\-auto-\-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2) -O3 (pass 2)
-\-no-prec-div (pass 2) -opt-mem-layout-trans=3 (pass 2)
-\-prof-use (pass 2) -\-opt-malloc-options=3

453.povray: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2) -O3 (pass 2)
-\-no-prec-div (pass 2) -opt-mem-layout-trans=3 (pass 2)
-\-prof-use (pass 2) -\-unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2) -O3 (pass 2)
-\-no-prec-div (pass 2) -\-prof-use (pass 2) -unroll12
-\-inline-level=0 -\-scalar-rep -\-static

434.zeusmp: basepeak = yes
SPEC CFP2006 Result

Hewlett-Packard Company
ProLiant DL380 G7
(2.26 GHz, Intel Xeon E5607)

SPECfp_rate2006 = NC
SPECfp_rate_base2006 = NC

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability of the system.

Peak Optimization Flags (Continued)

437.leslie3d: -xsse4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
459.GemsFDTD: basepeak = yes
465.tonto: -xsse4.2(pass 2) -ipo -gen(pass 1) -ipo(pass 2) -O3(pass 2)
   -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
   -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes
454.calculix: basepeak = yes
481.wrf: -xsse4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20111122.xml
http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.xml
SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability of the system.