## Acer Incorporated

### Acer AT110 F2(Xeon E3-1280)

**SPECint\_rate\_2006 = 177**  
**SPECint\_rate\_base\_2006 = 171**

<table>
<thead>
<tr>
<th>Test sponsor</th>
<th>Acer Incorporated</th>
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<tr>
<td>Tested by</td>
<td>Acer Incorporated</td>
</tr>
<tr>
<td>CPU2006 license</td>
<td>97</td>
</tr>
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<td>Test date</td>
<td>Dec-2011</td>
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</table>

### CPU Characteristics

- **CPU Name:** Intel Xeon E3-1280  
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.90 GHz  
- **CPU MHZ:** 3500  
- **FPU:** Integrated  
- **CPU(s) enabled:** 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
- **CPU(s) orderable:** 1 chip  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 256 KB I+D on chip per core  
- **L3 Cache:** 8 MB I+D on chip per chip  
- **Other Cache:** None  
- **Memory:** 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)  
- **Disk Subsystem:** 1 x 640 GB SATA, 7200 RPM  
- **Other Hardware:** None

### Operating System

- **Operating System:** Red Hat Enterprise Linux Server release 6.1 (Santiago)  
- **Compiler:** C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux  
- **Auto Parallel:** No  
- **File System:** ext4  
- **System State:** Run level 3 (add definition here)  
- **Base Pointers:** 32-bit  
- **Peak Pointers:** 32/64-bit  
- **Other Software:** Microquill SmartHeap V9.01
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SPEC CINT2006 Result

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Results Table

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<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
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<td>Base</td>
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<td>458.sjeng</td>
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<td>462.libquantum</td>
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<td>329</td>
<td>168</td>
<td>327</td>
<td>169</td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited" Hugepages were not configured on the system.

Platform Notes
BIOS Settings:
HW Prefetcher = Enabled
EIST = Enabled
Turbo Mode = Enabled
C6-State = Enabled
Sysinfo program /usr/cpu2006/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdfff5032aaa42e583f96b07f99d3
running on localhost.localdomain Wed Dec 14 12:14:41 2011

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E31280 @ 3.50GHz
1 "physical id"s (chips)
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Platform Notes (Continued)

8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 4
siblings : 8
physical 0: cores 0 1 2 3
cache size : 8192 KB

From /proc/meminfo
MemTotal: 8179252 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.1 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)

uname -a:
Linux localhost.localdomain 2.6.32-131.0.15.el6.x86_64 #1 SMP Tue May 10
15:42:40 EDT 2011 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Dec 14 12:13

SPEC is set to: /usr/cpu2006
Files system Type Size Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_root
ext4 769G 23G 708G 4% /

Additional information from dmidecode:
Memory:
2x Nanya NT4GC72B8PB0NF-CG 4 GB 1333 MHz 2 rank

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enable
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Base Compiler Invocation

C benchmarks:
   icc -m32

C++ benchmarks:
   icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
   -xAVX -ipo -O3 -no-prec-div -opt-prefetch

C++ benchmarks:
   -xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
   -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:
   403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
   icc -m32

   400.perlbench: icc -m64
   401.bzip2: icc -m64
   456.hmmer: icc -m64
   458.sjeng: icc -m64

C++ benchmarks:
   icpc -m32
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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll12 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes
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### Peak Other Flags

C benchmarks:

```bash
403.gcc -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at


You can also download the XML flags sources by saving the following links:


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