# SPEC® CINT2006 Result

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## Dell Inc.

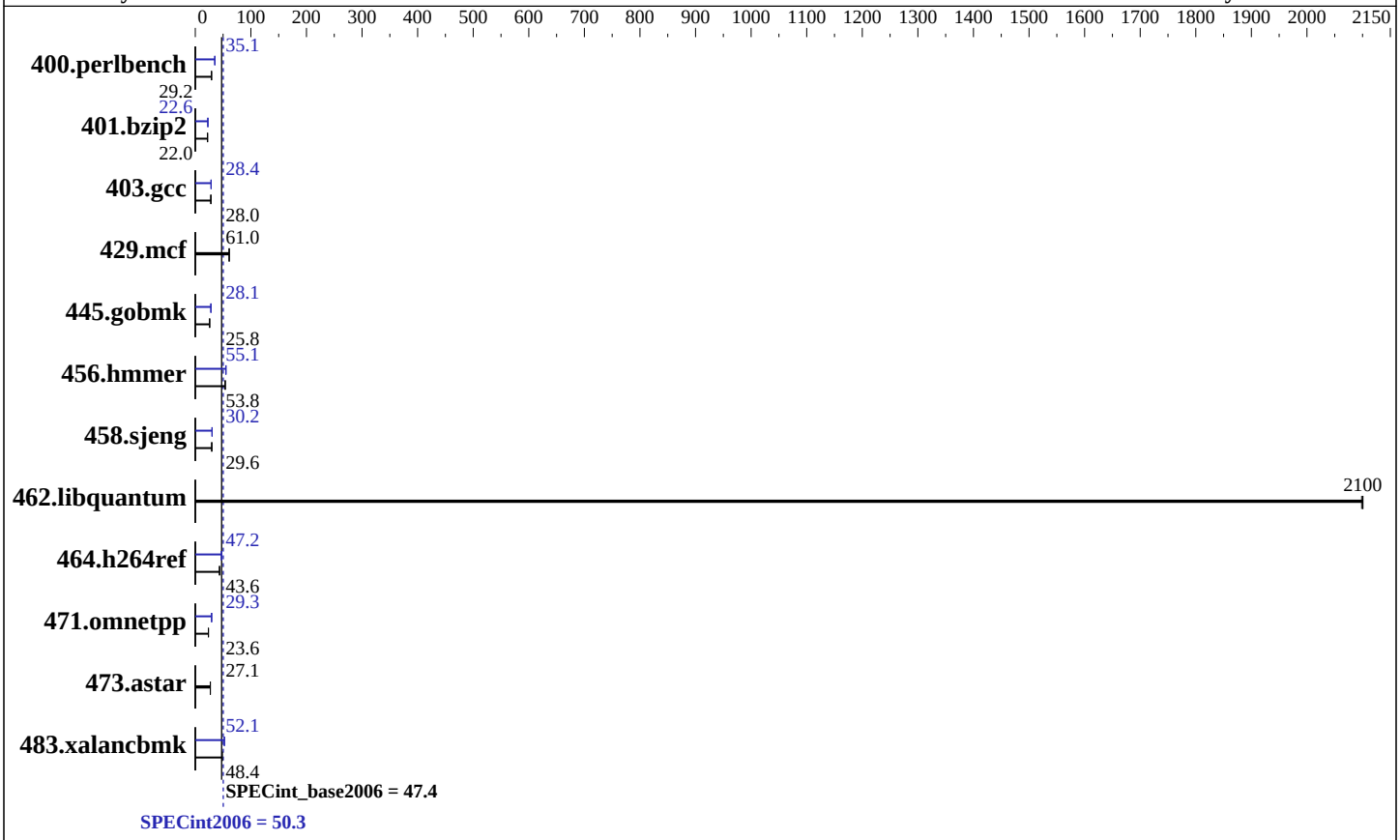
PowerEdge R610 (Intel Xeon X5690, 3.46 GHz)

SPECint®2006 = 50.3

SPECint_base2006 = 47.4

**CPU2006 license:** 55
**Test sponsor:** Dell Inc.
**Tested by:** Dell Inc.

**Test date:** Dec-2011
**Hardware Availability:** Feb-2011
**Software Availability:** Oct-2011




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5690 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.73 GHz |
| CPU MHz: | 3467 |
| FPU: | Integrated |
| CPU(s) enabled: | 12 cores, 2 chips, 6 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 12 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC) |
| Disk Subsystem: | 1 x 146 GB 15000 RPM SAS |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 6.1 (Santiago)<br>2.6.32-131.0.15.el6.x86_64 |
| Compiler: | C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux |
| Auto Parallel: | Yes |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32/64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V9.01 |

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## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 336 | 29.1 | 334 | 29.2 | **335** | **29.2** | 278 | 35.1 | 278 | 35.1 | **278** | **35.1** |
| 401.bzip2 | 439 | 22.0 | 438 | 22.0 | **439** | **22.0** | 426 | 22.6 | 427 | 22.6 | **427** | **22.6** |
| 403.gcc | **288** | **28.0** | 286 | 28.1 | 288 | 27.9 | **283** | **28.4** | 283 | 28.5 | 283 | 28.4 |
| 429.mcf | **150** | **61.0** | 150 | 60.9 | 149 | 61.1 | **150** | **61.0** | 150 | 60.9 | 149 | 61.1 |
| 445.gobmk | 407 | 25.8 | 407 | 25.8 | **407** | **25.8** | 374 | 28.1 | 373 | 28.1 | **373** | **28.1** |
| 456.hmmer | 173 | 53.8 | 173 | 53.8 | **173** | **53.8** | 169 | 55.1 | **169** | **55.1** | 169 | 55.1 |
| 458.sjeng | 409 | 29.6 | 408 | 29.6 | **408** | **29.6** | 402 | 30.1 | 401 | 30.2 | **401** | **30.2** |
| 462.libquantum | 9.87 | 2100 | **9.86** | **2100** | 9.86 | 2100 | 9.87 | 2100 | **9.86** | **2100** | 9.86 | 2100 |
| 464.h264ref | 507 | 43.7 | **507** | **43.6** | 508 | 43.6 | 469 | 47.2 | **469** | **47.2** | 468 | 47.3 |
| 471.omnetpp | **265** | **23.6** | 265 | 23.6 | 264 | 23.6 | 212 | 29.5 | **214** | **29.3** | 214 | 29.2 |
| 473.astar | 259 | 27.1 | **259** | **27.1** | 260 | 27.0 | 259 | 27.1 | **259** | **27.1** | 260 | 27.0 |
| 483.xalancbmk | 144 | 48.1 | **143** | **48.4** | 142 | 48.5 | 132 | 52.1 | 132 | 52.4 | **132** | **52.1** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
Sysinfo program /home/CPU2006v1.2/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on R610 Wed Dec  7 05:40:47 2011

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU X5690 @ 3.47GHz
      2 "physical id"s (chips)
      24 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 6
      siblings  : 12
      physical 0: cores 0 1 2 8 9 10
      physical 1: cores 0 1 2 8 9 10
   cache size : 12288 KB

From /proc/meminfo
   MemTotal:       99059436 kB
```

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## Platform Notes (Continued)

```
      HugePages_Total:       0
      Hugepagesize:       2048 kB

   /usr/bin/lsb_release -d
      Red Hat Enterprise Linux Server release 6.1 (Santiago)

   From /etc/*release* /etc/*version*
      redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
      system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
      system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

   uname -a:
      Linux R610 2.6.32-131.0.15.el6.x86_64 #1 SMP Tue May 10 15:42:40 EDT 2011
      x86_64 x86_64 x86_64 GNU/Linux

   run-level 3 Nov 29 09:20

   SPEC is set to: /home/CPU2006v1.2
      Filesystem     Type    Size  Used Avail Use% Mounted on
      /dev/mapper/vg_r610-lv_home
                     ext4     56G  3.7G   49G   7% /home

   Additional information from dmidecode:
     Memory:
      10x 00CE00B380CE M393B1K70BH1-CH9 8 GB 1333 MHz 2 rank
      2x 00CE04B380CE M393B1K70BH1-CH9 8 GB 1333 MHz 2 rank

   (End of data from sysinfo program)
```

## General Notes

```
 Environment variables set by runspec before the start of the run:
 KMP_AFFINITY = "granularity=fine,scatter"
 LD_LIBRARY_PATH = "/home/CPU2006v1.2/libs/32:/home/CPU2006v1.2/libs/64"
 OMP_NUM_THREADS = "12"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
  memory using RHEL5.5
  Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

# Base Compiler Invocation

C benchmarks:

```
icc  -m64
```

C++ benchmarks:

```
icpc -m64
```

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
445.gobmk: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
464.h264ref: `-DSPEC_CPU_LP64`
471.omnetpp: `-DSPEC_CPU_LP64`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

## Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32`
`-Wl,-z,muldefs -L/smartheap -lsmartheap64`

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m32`

473.astar: `icpc -m64`

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32 -ansi-alias

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs -L/smartheap -lsmartheap

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## Peak Optimization Flags (Continued)

473.astar: `basepeak = yes`

483.xalancbmk: `-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -Wl,-z,muldefs -L/smartheap -lsmartheap`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml`


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