Cisco Systems
Cisco UCS C200 M2 (Intel Xeon X5650, 2.67 GHz)

`SPECint_rate2006 = 388`
`SPECint_rate_base2006 = 371`

CPU2006 license: 9019
Test sponsor: Cisco Systems
Tested by: Cisco Systems

Test date: Jan-2012
Hardware Availability: Mar-2011
Software Availability: Dec-2011

400.perlbench
401.bzip2
403.gcc
429.mcf
445.gobmk
456.hmmer
458.sjeng
462.libquantum
464.h264ref
471.omnetpp
473.astar
483.xalancbmk

Hardware

CPU Name: Intel Xeon X5650
CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz
CPU MHz: 2667
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 96 GB (12 x 8 GB 2Rx4 PC3L-10600R-9, ECC)
Disk Subsystem: 600 GB SAS 10K RPM
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V9.01
SPEC CINT2006 Result

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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>24</td>
<td>773</td>
<td>303</td>
<td>774</td>
<td>303</td>
<td>772</td>
<td>304</td>
<td>24</td>
<td>656</td>
<td>358</td>
<td>658</td>
<td>356</td>
<td>657</td>
<td>357</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>24</td>
<td>1081</td>
<td>214</td>
<td>1078</td>
<td>215</td>
<td>1081</td>
<td>214</td>
<td>24</td>
<td>1036</td>
<td>223</td>
<td>1033</td>
<td>224</td>
<td>1034</td>
<td>224</td>
</tr>
<tr>
<td>403.mcc</td>
<td>24</td>
<td>820</td>
<td>236</td>
<td>822</td>
<td>235</td>
<td>822</td>
<td>235</td>
<td>24</td>
<td>825</td>
<td>234</td>
<td>832</td>
<td>232</td>
<td>841</td>
<td>230</td>
</tr>
<tr>
<td>429.mcf</td>
<td>24</td>
<td>488</td>
<td>448</td>
<td>487</td>
<td>449</td>
<td>488</td>
<td>449</td>
<td>24</td>
<td>488</td>
<td>448</td>
<td>487</td>
<td>449</td>
<td>488</td>
<td>449</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>24</td>
<td>498</td>
<td>450</td>
<td>497</td>
<td>450</td>
<td>498</td>
<td>450</td>
<td>24</td>
<td>420</td>
<td>533</td>
<td>421</td>
<td>532</td>
<td>420</td>
<td>533</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>24</td>
<td>889</td>
<td>327</td>
<td>889</td>
<td>327</td>
<td>890</td>
<td>326</td>
<td>24</td>
<td>831</td>
<td>349</td>
<td>832</td>
<td>349</td>
<td>831</td>
<td>349</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>24</td>
<td>211</td>
<td>2360</td>
<td>210</td>
<td>2360</td>
<td>210</td>
<td>2360</td>
<td>24</td>
<td>211</td>
<td>2360</td>
<td>210</td>
<td>2360</td>
<td>210</td>
<td>2360</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>24</td>
<td>455</td>
<td>110</td>
<td>454</td>
<td>110</td>
<td>455</td>
<td>110</td>
<td>24</td>
<td>416</td>
<td>345</td>
<td>416</td>
<td>345</td>
<td>416</td>
<td>345</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>24</td>
<td>654</td>
<td>229</td>
<td>651</td>
<td>230</td>
<td>653</td>
<td>230</td>
<td>24</td>
<td>635</td>
<td>236</td>
<td>632</td>
<td>237</td>
<td>633</td>
<td>237</td>
</tr>
<tr>
<td>473.astar</td>
<td>24</td>
<td>772</td>
<td>218</td>
<td>768</td>
<td>219</td>
<td>766</td>
<td>220</td>
<td>24</td>
<td>772</td>
<td>218</td>
<td>768</td>
<td>219</td>
<td>766</td>
<td>220</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>24</td>
<td>436</td>
<td>380</td>
<td>436</td>
<td>380</td>
<td>435</td>
<td>380</td>
<td>24</td>
<td>436</td>
<td>380</td>
<td>436</td>
<td>380</td>
<td>435</td>
<td>380</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Configuration: Data Reuse Optimization = Disabled
Sysinfo program /opt/cpu2006/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on localhost.localdomain Mon Jan 23 14:39:44 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU X5650 @ 2.67GHz
2 "physical id"s (chips)
24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6

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info@spec.org
http://www.spec.org/
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Platform Notes (Continued)

siblings : 12  
physical 0: cores 0 1 2 8 9 10  
physical 1: cores 0 1 2 8 9 10  

From /proc/meminfo  
MemTotal: 98997788 kB  
HugePages_Total: 0  
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d  
Red Hat Enterprise Linux Server release 6.2 (Santiago)

From /etc/*release* /etc/*version*  
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)  

uname -a:  
Linux localhost.localdomain 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011 x86_64 x86_64 x86_64 GNU/Linux  
run-level 3 Jan 23 14:07

SPEC is set to: /opt/cpu2006

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda1</td>
<td>ext4</td>
<td>917G</td>
<td>5.5G</td>
<td>865G</td>
<td>1%</td>
<td>/</td>
</tr>
</tbody>
</table>

Additional information from dmidecode:  
(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:  
LD_LIBRARY_PATH = "/opt/cpu2006/libs/32:/opt/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Filesystem page cache cleared with:  
echo 1 > /proc/sys/vm/drop_caches

runcspec command invoked through numactl i.e.:  
umactl --interleave=all runspec <etc>
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Base Compiler Invocation

C benchmarks:
  icc -m32

C++ benchmarks:
  icpc -m32

Base Portability Flags

  400.perlbench: -DSPEC_CPU_LINUX_IA32
  462.libquantum: -DSPEC_CPU_LINUX
  483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
  -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
  -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
  -Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:
  403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
  icc -m32

  400.perlbench: icc -m64
  401.bzip2: icc -m64
  456.hmmer: icc -m64
  458.sjeng: icc -m64

C++ benchmarks:
  icpc -m32
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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
429.mcf: basepeak = yes
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32
462.libquantum: basepeak = yes
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/smartheap -lsmartheap
473.astar: basepeak = yes

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2.xml

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For other inquiries, please contact webmaster@spec.org.

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