



SPEC[®] CFP2006 Result

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Fujitsu

SPECfp[®]2006 = **93.2**

PRIMERGY BX924 S3, Intel Xeon E5-2690, 2.90 GHz

SPECfp_base2006 = **88.3**

CPU2006 license: 19

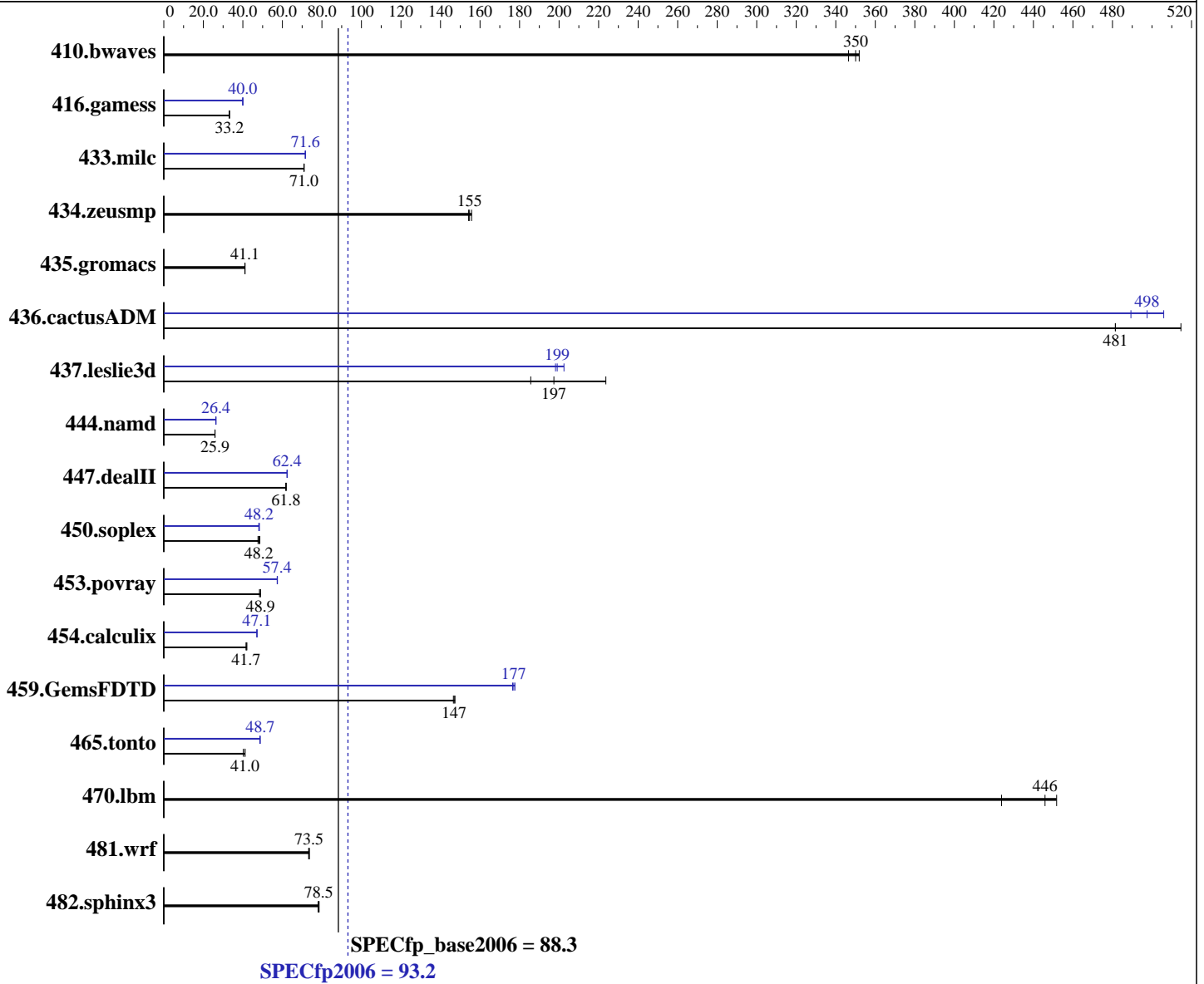
Test date: Feb-2012

Test sponsor: Fujitsu

Hardware Availability: Mar-2012

Tested by: Fujitsu

Software Availability: Feb-2012



Hardware

CPU Name: Intel Xeon E5-2690
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz
 CPU MHz: 2900
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.2.273 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.2.273 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

Continued on next page



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L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3L-12800R-11, ECC)
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	39.2	346	38.8	350	38.6	352	39.2	346	38.8	350	38.6	352
416.gamess	590	33.2	589	33.3	590	33.2	489	40.0	491	39.9	489	40.0
433.milc	129	71.0	129	70.9	129	71.0	128	71.6	128	71.6	128	71.7
434.zeusmp	58.8	155	59.0	154	58.4	156	58.8	155	59.0	154	58.4	156
435.gromacs	174	41.0	174	41.1	174	41.1	174	41.0	174	41.1	174	41.1
436.cactusADM	24.8	481	23.2	515	24.8	481	23.6	506	24.0	498	24.4	489
437.leslie3d	47.6	197	42.0	224	50.6	186	47.4	198	47.2	199	46.4	202
444.namd	309	25.9	310	25.9	310	25.9	304	26.4	304	26.4	304	26.4
447.dealII	185	61.8	185	61.8	185	62.0	184	62.3	183	62.4	183	62.5
450.soplex	173	48.2	175	47.6	172	48.6	172	48.4	173	48.2	174	48.1
453.povray	109	48.9	110	48.4	109	48.9	92.7	57.4	92.5	57.5	92.7	57.4
454.calculix	196	42.0	198	41.7	198	41.7	175	47.1	175	47.1	175	47.1
459.GemsFDTD	72.4	147	72.2	147	72.0	147	60.1	176	59.9	177	59.7	178
465.tonto	245	40.2	240	41.0	239	41.1	202	48.7	202	48.8	202	48.6
470.lbm	30.8	446	32.4	424	30.4	452	30.8	446	32.4	424	30.4	452
481.wrf	152	73.7	152	73.5	152	73.3	152	73.7	152	73.5	152	73.3
482.sphinx3	248	78.5	250	78.1	248	78.6	248	78.5	250	78.1	248	78.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Intel HT Technology = Disable
 Frequency Floor Override = Enable

General Notes

Environment variables set by runspec before the start of the run:
 KMP_AFFINITY = "granularity=fine,scatter"
 LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

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General Notes (Continued)

OMP_NUM_THREADS = "16"

Binaries compiled on a system with
Red Hat Enterprise Linux Server release 6.1 (Santiago)
Added glibc-static-2.12-1.25.el6.x86_64.rpm
to enable static linking
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias`

C++ benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias`

Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-static

450.soplex: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch
-ansi-alias

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -parallel
-opt-prefetch

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: -xAVX -ipo -O3 -no-prec-div -static -parallel
-opt-prefetch -ansi-alias

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.00.xml>

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For other inquiries, please contact webmaster@spec.org.

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