# SPEC® CFP2006 Result

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## Dell Inc.

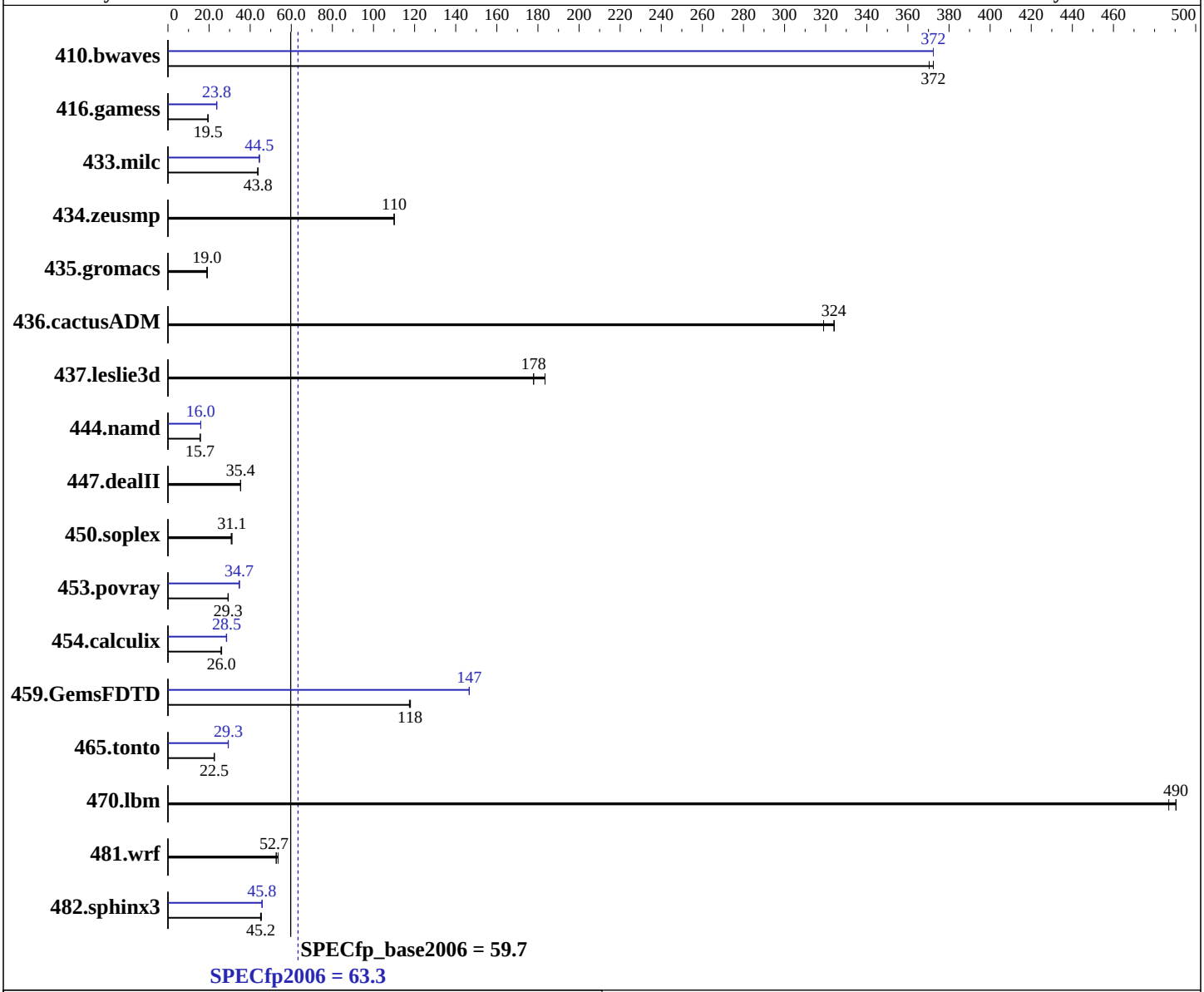
PowerEdge M620 (Intel Xeon E5-2650L, 1.80 GHz)

SPECfp®2006 = 63.3

SPECfp_base2006 = 59.7

**CPU2006 license:** 55
**Test sponsor:** Dell Inc.
**Tested by:** Dell Inc.

**Test date:** Feb-2012
**Hardware Availability:** Mar-2012
**Software Availability:** Feb-2012




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2650L |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.30 GHz |
| CPU MHz: | 1800 |
| FPU: | Integrated |
| CPU(s) enabled: | 16 cores, 2 chips, 8 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 SP2 (x86_64) 3.0.13-0.9-default |
| Compiler: | C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux |
| Auto Parallel: | Yes |
| File System: | ext3 |
| System State: | Run level 3 (add definition here) |

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L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 1 x 600 GB 10000 RPM SAS
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | **36.5** | **372** | 36.5 | 372 | 36.7 | 370 | 36.5 | 372 | 36.5 | 372 | **36.5** | **372** |
| 416.gamess | 1004 | 19.5 | 1007 | 19.4 | **1004** | **19.5** | 824 | 23.8 | 828 | 23.7 | **824** | **23.8** |
| 433.milc | 210 | 43.7 | **210** | **43.8** | 210 | 43.8 | **206** | **44.5** | 206 | 44.5 | 207 | 44.5 |
| 434.zeusmp | 82.7 | 110 | 82.7 | 110 | **82.7** | **110** | 82.7 | 110 | 82.7 | 110 | **82.7** | **110** |
| 435.gromacs | 372 | 19.2 | **376** | **19.0** | 377 | 19.0 | 372 | 19.2 | **376** | **19.0** | 377 | 19.0 |
| 436.cactusADM | 36.9 | 324 | **36.9** | **324** | 37.5 | 319 | 36.9 | 324 | **36.9** | **324** | 37.5 | 319 |
| 437.leslie3d | 51.2 | 183 | 52.8 | 178 | **52.8** | **178** | 51.2 | 183 | 52.8 | 178 | **52.8** | **178** |
| 444.namd | **511** | **15.7** | 511 | 15.7 | 511 | 15.7 | **503** | **16.0** | 503 | 16.0 | 502 | 16.0 |
| 447.dealII | 324 | 35.3 | 323 | 35.4 | **324** | **35.4** | 324 | 35.3 | 323 | 35.4 | **324** | **35.4** |
| 450.soplex | 268 | 31.1 | **268** | **31.1** | 271 | 30.8 | 268 | 31.1 | **268** | **31.1** | 271 | 30.8 |
| 453.povray | 181 | 29.4 | **181** | **29.3** | 182 | 29.2 | 154 | 34.6 | 153 | 34.8 | **153** | **34.7** |
| 454.calculix | 316 | 26.1 | 319 | 25.8 | **318** | **26.0** | 289 | 28.6 | **289** | **28.5** | 291 | 28.4 |
| 459.GemsFDTD | 90.3 | 117 | 89.9 | 118 | **90.1** | **118** | 72.4 | 147 | 72.4 | 147 | **72.4** | **147** |
| 465.tonto | **437** | **22.5** | 437 | 22.5 | 436 | 22.6 | **336** | **29.3** | 336 | 29.3 | 335 | 29.4 |
| 470.lbm | 28.2 | 487 | 28.0 | 490 | **28.0** | **490** | 28.2 | 487 | 28.0 | 490 | **28.0** | **490** |
| 481.wrf | 212 | 52.7 | **212** | **52.7** | 209 | 53.6 | 212 | 52.7 | **212** | **52.7** | 209 | 53.6 |
| 482.sphinx3 | **431** | **45.2** | 433 | 45.1 | 429 | 45.5 | 426 | 45.7 | 425 | 45.9 | **426** | **45.8** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
System Profile set to Custom
CPU Power Management set to Maximum Performance
Memory Frequency set to Maximum Performance
Turbo Boost set to Enabled
C States/C1E set to Enabled
Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on Brutus-4P Fri Feb 24 02:31:33 2012

This section contains SUT (System Under Test) info as seen by
```

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## Platform Notes (Continued)

```
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2650L 0 @ 1.80GHz
      2 "physical id"s (chips)
      32 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 16
      physical 0: cores 0 1 2 3 4 5 6 7
      physical 1: cores 0 1 2 3 4 5 6 7
   cache size : 20480 KB

From /proc/meminfo
   MemTotal:       132122688 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 11 (x86_64)
      VERSION = 11
      PATCHLEVEL = 2

uname -a:
   Linux Brutus-4P 3.0.13-0.9-default #1 SMP Mon Jan 16 17:33:03 UTC 2012
   (54ddfaf) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 23 17:46 last=S

SPEC is set to: /root/CPU2006-1.2
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sdi1      ext3  546G   72G  447G  14% /

Additional information from dmidecode:

(End of data from sysinfo program)
```

## General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"
OMP_NUM_THREADS = "16"
```

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## General Notes (Continued)

```
Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages disabled with:
echo never  > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>       /proc/sys/vm/drop_caches
```

## Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

## Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

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## Base Optimization Flags (Continued)

C++ benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

## Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

## Peak Portability Flags

`Same as Base Portability Flags`

## Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-static -auto-ilp32 -ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel`

C++ benchmarks:

Continued on next page

# Peak Optimization Flags (Continued)

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120328.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120328.xml

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For other inquiries, please contact webmaster@spec.org.

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