Dell Inc.

PowerEdge R720 (Intel Xeon E5-2609, 2.40 GHz)

**SPECint\_rate2006 = 224**

**SPECint\_rate\_base2006 = 215**

<table>
<thead>
<tr>
<th>SPECint_rate2006</th>
<th>SPECint_rate_base2006</th>
</tr>
</thead>
<tbody>
<tr>
<td>224</td>
<td>215</td>
</tr>
</tbody>
</table>

**CPU2006 license:** 55

**Test sponsor:** Dell Inc.

**Tested by:** Dell Inc.

**Test date:** Feb-2012

**Hardware Availability:** Mar-2012

**Software Availability:** Feb-2012

**Operating System:** SUSE Linux Enterprise Server 11 SP2 (x86_64) 3.0.13-0.9-default

**Compiler:** C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux

**Auto Parallel:** No

**File System:** ext3

**System State:** Run level 3 (add definition here)

**Base Pointers:** 32-bit

**Peak Pointers:** 32/64-bit

**Other Software:** Microquill SmartHeap V9.01

**CPU Name:** Intel Xeon E5-2609

**CPU Characteristics:**

- Integrated
- 8 cores, 2 chips, 4 cores/chip
- 1.2 chip

**CPU MHz:** 2400

**FPU:** Integrated

**Primary Cache:** 32 KB I + 32 KB D on chip per core

**Secondary Cache:** 256 KB I+D on chip per core

**L3 Cache:** 10 MB I+D on chip per chip

**Other Cache:** None

**Memory:** 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1066 MHz)

**Disk Subsystem:** 1 x 146 GB 10000 RPM SAS

**Other Hardware:** None
Dell Inc. 

PowerEdge R720 (Intel Xeon E5-2609, 2.40 GHz)

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R720 (Intel Xeon E5-2609, 2.40 GHz)

SPECint_rate2006 = 224

SPECint_rate_base2006 = 215

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Feb-2012
Hardware Availability: Mar-2012
Software Availability: Feb-2012

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>8</td>
<td>495</td>
<td>158</td>
<td>496</td>
<td>158</td>
<td>496</td>
<td>158</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>401.bzip2</td>
<td>8</td>
<td>704</td>
<td>110</td>
<td>703</td>
<td>110</td>
<td>703</td>
<td>110</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>403.gcc</td>
<td>8</td>
<td>366</td>
<td>176</td>
<td>365</td>
<td>176</td>
<td>365</td>
<td>176</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>429.mcf</td>
<td>8</td>
<td>202</td>
<td>360</td>
<td>202</td>
<td>360</td>
<td>202</td>
<td>360</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>445.gobmk</td>
<td>8</td>
<td>598</td>
<td>140</td>
<td>597</td>
<td>141</td>
<td>596</td>
<td>141</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>456.hmmer</td>
<td>8</td>
<td>267</td>
<td>279</td>
<td>268</td>
<td>278</td>
<td>268</td>
<td>278</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>458.sjeng</td>
<td>8</td>
<td>647</td>
<td>150</td>
<td>647</td>
<td>150</td>
<td>646</td>
<td>150</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>462.libquantum</td>
<td>8</td>
<td>120</td>
<td>1380</td>
<td>121</td>
<td>1370</td>
<td>121</td>
<td>1370</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>464.h264ref</td>
<td>8</td>
<td>613</td>
<td>289</td>
<td>610</td>
<td>290</td>
<td>614</td>
<td>289</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>8</td>
<td>412</td>
<td>121</td>
<td>413</td>
<td>121</td>
<td>411</td>
<td>122</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>473.astar</td>
<td>8</td>
<td>449</td>
<td>125</td>
<td>450</td>
<td>125</td>
<td>452</td>
<td>124</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>8</td>
<td>218</td>
<td>253</td>
<td>218</td>
<td>253</td>
<td>219</td>
<td>252</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

System Profile set to Custom
CPU Power Management set to Maximum Performance
Memory Frequency set to Maximum Performance
C States/C1E set to Enabled
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on linux-twrm Tue Feb 21 11:26:31 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2609 0 @ 2.40GHz
  2 "physical id"s (chips)
  8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
Continued on next page
SPEC CINT2006 Result

Dell Inc.

PowerEdge R720 (Intel Xeon E5-2609, 2.40 GHz)

SPECint_rate2006 = 224
SPECint_rate_base2006 = 215

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Feb-2012
Hardware Availability: Mar-2012
Software Availability: Feb-2012

Platform Notes (Continued)

following excerpts from /proc/cpuinfo might not be reliable. Use with caution.

- cpu cores : 4
- siblings : 4
- physical 0: cores 0 1 2 3
- physical 1: cores 0 1 2 3
- cache size : 10240 KB

From /proc/meminfo

- MemTotal: 132122688 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*
SuSE-release:
- SUSE Linux Enterprise Server 11 (x86_64)
- VERSION = 11
- PATCHLEVEL = 2

uname -a:
Linux linux-twrm 3.0.13-0.9-default #1 SMP Mon Jan 16 17:33:03 UTC 2012
(54ddfaf) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 21 11:17 last=S

SPEC is set to: /root/cpu2006-1.2
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 ext3 131G 6.2G 118G 6% /

Additional information from dmidecode:

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5
The Dell PowerEdge R720 and
the Bull NovaScale R460 F3 models are electronically equivalent.
The results have been measured on a Dell PowerEdge R720 model
Filesystem page cache cleared with:
- echo 1 > /proc/sys/vm/drop_caches
- runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
SPEC CINT2006 Result

Dell Inc.
PowerEdge R720 (Intel Xeon E5-2609, 2.40 GHz)

SPECint_rate2006 = 224
SPECint_rate_base2006 = 215

CPU2006 license: 55
Test date: Feb-2012
Test sponsor: Dell Inc.
Hardware Availability: Mar-2012
Tested by: Dell Inc.
Software Availability: Feb-2012

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:
403gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32
SPEC CINT2006 Result

Dell Inc.
PowerEdge R720 (Intel Xeon E5-2609, 2.40 GHz)

SPECint_rate2006 = 224
SPECint_rate_base2006 = 215

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.
Test date: Feb-2012
Hardware Availability: Mar-2012
Software Availability: Feb-2012

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xsse4.2 -ipo -03 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xsse4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xsse4.2 -ipo -03 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-03(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-1/smartheap -1/smartheap

473.astar: basepeak = yes

Continued on next page
## SPEC CINT2006 Result

**Dell Inc.**

**PowerEdge R720 (Intel Xeon E5-2609, 2.40 GHz)**

<table>
<thead>
<tr>
<th>SPECint_rate2006 = 224</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECint_rate_base2006 = 215</td>
</tr>
</tbody>
</table>

### Test Details

- **CPU2006 license:** 55
- **Test sponsor:** Dell Inc.
- **Tested by:** Dell Inc.
- **Test date:** Feb-2012
- **Hardware Availability:** Mar-2012
- **Software Availability:** Feb-2012

### Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

### Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at


You can also download the XML flags sources by saving the following links:


---

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Originally published on 27 March 2012.