Acer Incorporated
Acer AR320 F2 (Intel Core i3-2120)

SPECfp®2006 = 50.9
SPECfp_base2006 = 50.0

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

CPU Name: Intel Core i3-2120
CPU Characteristics:
CPU MHz: 3300
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Hardware

Software

Operating System: Red Hat Enterprise Linux Server release 6.1 (Santiago)
Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: ext4

Standard Performance Evaluation Corporation
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http://www.spec.org/
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System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Software Availability: None

Operating System Notes

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Sysinfo program /usr/cpu2006/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdfff5032aaa42e583f96b07f99d3
running on localhost.localdomain Sat Dec 31 00:53:27 2011

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Core(TM) i3-2120 CPU @ 3.30GHz
Continued on next page
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Software Availability: Dec-2011

Platform Notes (Continued)

1 "physical id"s (chips)
4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 2
siblings : 4
physical 0: cores 0 1

cache size : 3072 KB

From /proc/meminfo
MemTotal: 8046960 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.1 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)

uname -a:
Linux localhost.localdomain 2.6.32-131.0.15.el6.x86_64 #1 SMP Tue May 10 15:42:40 EDT 2011 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 31 00:52

SPEC is set to: /usr/cpu2006
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_root
ext4 50G 23G 25G 49% /

Additional information from dmidecode:

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"
OMP_NUM_THREADS = "2"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL6.1_x64
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
umactl --interleave=all runspec <etc>
SPEC CFP2006 Result
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Base Compiler Invocation

C benchmarks:
  icc  -m64

C++ benchmarks:
  icpc  -m64

Fortran benchmarks:
  ifort  -m64

Benchmarks using both Fortran and C:
  icc  -m64 ifort  -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.tbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
  -xAVX  -ipo  -O3  -no-prec-div  -static  -parallel  -opt-prefetch
            -ansi-alias

C++ benchmarks:
  -xAVX  -ipo  -O3  -no-prec-div  -static  -opt-prefetch  -ansi-alias

Fortran benchmarks:
  -xAVX  -ipo  -O3  -no-prec-div  -static  -parallel  -opt-prefetch

Benchmarks using both Fortran and C:
  -xAVX  -ipo  -O3  -no-prec-div  -static  -parallel  -opt-prefetch
            -ansi-alias
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**Test date:** Dec-2011
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**Peak Compiler Invocation**

C benchmarks:
- icc  -m64

C++ benchmarks:
- icpc  -m64

Fortran benchmarks:
- ifort  -m64

Benchmarks using both Fortran and C:
- icc  -m64  ifort  -m64

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**Peak Portability Flags**

Same as Base Portability Flags

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**Peak Optimization Flags**

C benchmarks:
- 433.milc: -xAVX(pass 2)  -prof-gen(pass 1)  -ipo(pass 2)  -O3(pass 2)
  -no-prec-div(pass 2)  -prof-use(pass 2)  -static  -auto-ilp32
  -ansi-alias

- 470.lbm: basepeak = yes

- 482.sphinx3: -xAVX  -ipo  -O3  -no-prec-div  -unroll2  -ansi-alias
  -parallel

C++ benchmarks:
- 444.namd: -xAVX(pass 2)  -prof-gen(pass 1)  -ipo(pass 2)  -O3(pass 2)
  -no-prec-div(pass 2)  -prof-use(pass 2)  -fno-alias
  -auto-ilp32

- 447.dealII: basepeak = yes

- 450.soplex: basepeak = yes

- 453.povray: -xAVX(pass 2)  -prof-gen(pass 1)  -ipo(pass 2)  -O3(pass 2)
  -no-prec-div(pass 2)  -prof-use(pass 2)  -unroll4  -ansi-alias

Fortran benchmarks:
- 410.bwaves: -xAVX  -ipo  -O3  -no-prec-div  -opt-prefetch  -parallel
  -static

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Peak Optimization Flags (Continued)

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
            -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
            -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml

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For other inquiries, please contact webmaster@spec.org.

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