



SPEC[®] CFP2006 Result

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Acer Incorporated

SPECfp[®]_rate2006 = 75.8

Acer AT310 F2 (Intel Core i3-2100)

SPECfp_rate_base2006 = 74.2

CPU2006 license: 97

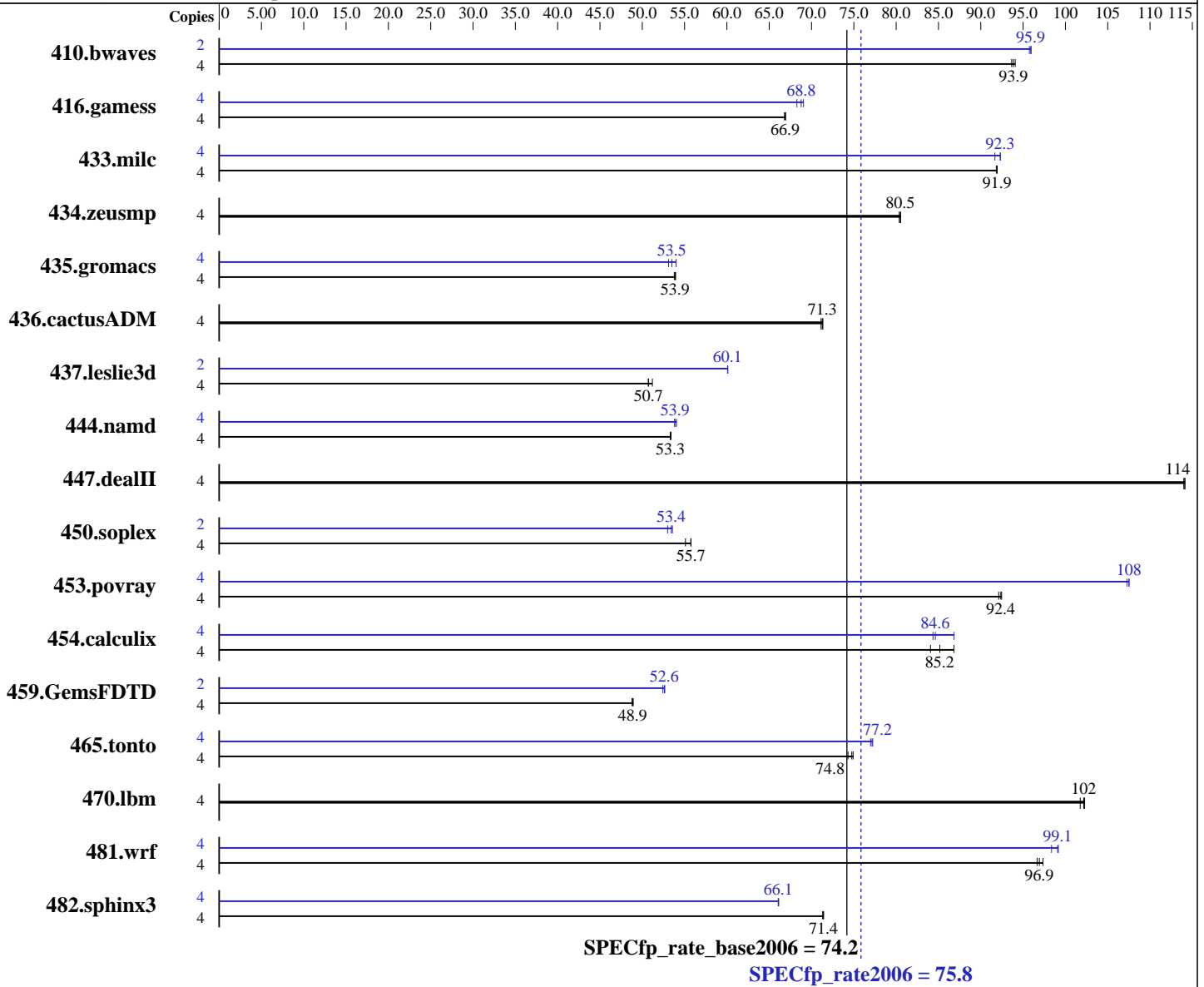
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Jan-2012

Hardware Availability: Jan-2012

Software Availability: Dec-2011



Hardware

CPU Name: Intel Core i3-2100
 CPU Characteristics:
 CPU MHz: 3100
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.1
 Kernel 2.6.32-131.0.15.el6.x86_64-default
 Compiler: C/C++/Fortran: Version 12.1.0.225 of Intel Compiler XE
 Build 20110803
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit

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L3 Cache: 3 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9,ECC)
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	4	578	94.1	580	93.6	<u>579</u>	<u>93.9</u>	2	284	95.7	283	95.9	<u>283</u>	<u>95.9</u>		
416.gamess	4	<u>1171</u>	<u>66.9</u>	1172	66.8	1170	66.9	4	1147	68.3	1134	69.1	<u>1139</u>	<u>68.8</u>		
433.milc	4	399	91.9	<u>399</u>	<u>91.9</u>	400	91.8	4	398	92.3	401	91.7	<u>398</u>	<u>92.3</u>		
434.zeusmp	4	452	80.5	453	80.4	<u>452</u>	<u>80.5</u>	4	452	80.5	453	80.4	<u>452</u>	<u>80.5</u>		
435.gromacs	4	531	53.8	<u>530</u>	<u>53.9</u>	529	53.9	4	538	53.1	529	54.0	<u>534</u>	<u>53.5</u>		
436.cactusADM	4	670	71.3	<u>671</u>	<u>71.3</u>	672	71.1	4	670	71.3	<u>671</u>	<u>71.3</u>	672	71.1		
437.leslie3d	4	<u>741</u>	<u>50.7</u>	734	51.2	742	50.7	2	<u>313</u>	<u>60.1</u>	313	60.1	313	60.1		
444.namd	4	602	53.3	<u>602</u>	<u>53.3</u>	601	53.4	4	<u>596</u>	<u>53.9</u>	594	54.0	596	53.8		
447.dealII	4	401	114	401	114	<u>401</u>	<u>114</u>	4	401	114	401	114	<u>401</u>	<u>114</u>		
450.soplex	4	605	55.1	598	55.8	<u>599</u>	<u>55.7</u>	2	315	53.0	<u>312</u>	<u>53.4</u>	311	53.6		
453.povray	4	<u>230</u>	<u>92.4</u>	231	92.1	230	92.5	4	198	107	<u>198</u>	<u>108</u>	198	108		
454.calculix	4	380	86.8	<u>388</u>	<u>85.2</u>	393	84.1	4	380	86.8	<u>390</u>	<u>84.6</u>	391	84.3		
459.GemsFDTD	4	867	48.9	870	48.8	<u>869</u>	<u>48.9</u>	2	<u>403</u>	<u>52.6</u>	403	52.7	405	52.4		
465.tonto	4	<u>526</u>	<u>74.8</u>	525	74.9	530	74.3	4	<u>510</u>	<u>77.2</u>	511	77.0	510	77.2		
470.lbm	4	540	102	<u>538</u>	<u>102</u>	537	102	4	540	102	<u>538</u>	<u>102</u>	537	102		
481.wrf	4	<u>461</u>	<u>96.9</u>	459	97.3	462	96.7	4	454	98.4	451	99.2	<u>451</u>	<u>99.1</u>		
482.sphinx3	4	1093	71.3	<u>1092</u>	<u>71.4</u>	1092	71.4	4	<u>1180</u>	<u>66.1</u>	1180	66.1	1179	66.1		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Sysinfo program /usr/cpu2006/config/sysinfo.rev6800
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3
running on localhost.localdomain Tue Jan 3 05:58:04 2012

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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Core(TM) i3-2100 CPU @ 3.10GHz
 1 "physical id"s (chips)
 4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores    : 2
  siblings     : 4
  physical 0   : cores 0 1
cache size     : 3072 KB

```

```

From /proc/meminfo
MemTotal:      8046960 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.1 (Santiago)

```

```

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
Linux localhost.localdomain 2.6.32-131.0.15.el6.x86_64 #1 SMP Tue May 10
15:42:40 EDT 2011 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jan 2 18:45

```

SPEC is set to: /usr/cpu2006
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_root
ext4            50G     27G   21G   57% /

```

Additional information from dmidecode:

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB

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General Notes (Continued)

memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias

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Base Optimization Flags (Continued)

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-static -auto-ilp32

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Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 07:56:23 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 April 2012.