<table>
<thead>
<tr>
<th>SPECfp®2006</th>
<th>70.4</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECfp_base2006</td>
<td>67.1</td>
</tr>
</tbody>
</table>

**CPU Name:** Intel Xeon E5-2630  
**CPU Characteristics:** Intel Turbo Boost Technology up to 2.80 GHz  
**CPU MHz:** 2300  
**FPU:** Integrated  
**CPU(s) enabled:** 12 cores, 2 chips, 6 cores/chip  
**CPU(s) orderable:** 1.2 chips  
**Primary Cache:** 32 KB I + 32 KB D on chip per core  
**Secondary Cache:** 256 KB I+D on chip per core

**Operating System:** Red Hat Enterprise Linux Server release 6.2 (Santiago)  
**Compiler:** C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
**Auto Parallel:** Yes  
**File System:** ext4
## SPEC CFP2006 Result

### Fujitsu

**PRIMERGY RX200 S7, Intel Xeon E5-2630, 2.30 GHz**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>SPECfp2006</th>
<th>SPECfp_base2006</th>
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</thead>
<tbody>
<tr>
<td></td>
<td>70.4</td>
<td>67.1</td>
</tr>
</tbody>
</table>

**CPU2006 license:** 19  
**Test sponsor:** Fujitsu  
**Tested by:** Fujitsu  
**Test date:** Jan-2012  
**Hardware Availability:** Mar-2012  
**Software Availability:** Dec-2011

- **L3 Cache:** 15 MB I+D on chip per chip  
- **Other Cache:** None  
- **Memory:** 128 GB (16 x 8 GB 2Rx4 PC3L-12800R-11, ECC, running at 1333 MHz and CL9)  
- **Disk Subsystem:** 1 x SATA, 500 GB, 7200 RPM  
- **Other Hardware:** None  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 32/64-bit  
- **Other Software:** None

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
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<th>Base Ratio</th>
<th>Base Seconds</th>
<th>Base Ratio</th>
<th>Base Seconds</th>
<th>Base Ratio</th>
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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

- **BIOS configuration:**
  - Intel HT Technology = Disable
  - Frequency Floor Override = Enable

---

Standard Performance Evaluation Corporation  
info@spec.org  
http://www.spec.org/
SPEC CFP2006 Result

Fujitsu

PRIMERGY RX200 S7, Intel Xeon E5-2630, 2.30 GHz

SPECfp2006 = 70.4
SPECfp_base2006 = 67.1

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jan-2012
Hardware Availability: Mar-2012
Software Availability: Dec-2011

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"
OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
  icc -m64

C++ benchmarks:
  icpc -m64

Fortran benchmarks:
  ifort -m64

Benchmarks using both Fortran and C:
  icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.cactusADM: -DSPEC_CPU_LP64 -nofor_main
459.GemsFD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64
SPEC CFP2006 Result

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CPU2006 license: 19
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Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
icc   -m64

C++ benchmarks:
icpc  -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc   -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3:  -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

Continued on next page
Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
- no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
- auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
- no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
- static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
- no-prec-div(pass 2) -prof-use(pass 2) -unroll2
- inline-level=0 -scalar-rep -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
- no-prec-div(pass 2) -prof-use(pass 2) -unroll2
- inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
- no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
- opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
# SPEC CFP2006 Result

## Fujitsu

| SPECfp2006 = | 70.4 |
| SPECfp_base2006 = | 67.1 |

**PRIMERGY RX200 S7, Intel Xeon E5-2630, 2.30 GHz**

| CPU2006 license: | 19 |
| Test sponsor: | Fujitsu |
| Tested by: | Fujitsu |
| Test date: | Jan-2012 |
| Hardware Availability: | Mar-2012 |
| Software Availability: | Dec-2011 |

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml](http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml)

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
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