**Itautec Servidor Itautec LX105 (Intel Xeon E3-1220)**

**SPECint®_rate2006 = 143**

**SPECint_rate_base2006 = 138**

**CPU2006 license:** 9001  
**Test date:** Mar-2012  
**Test sponsor:** Itautec  
**Hardware Availability:** Jun-2012  
**Tested by:** Itautec  
**Software Availability:** Dec-2011

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<td>483.xalancbmk</td>
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</table>

**Hardware**

- **CPU Name:** Intel Xeon E3-1220  
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.40 GHz  
- **CPU MHz:** 3100  
- **FPU:** Integrated  
- **CPU(s) enabled:** 4 cores, 1 chip, 4 cores/chip  
- **CPU(s) orderable:** 1 chip  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 256 KB I+D on chip per core  
- **L3 Cache:** 8 MB I+D on chip per chip  
- **Other Cache:** None  
- **Memory:** 8 GB (4 x 2 GB 2Rx8 PC3-10600U-9, ECC)  
- **Disk Subsystem:** 500 GB, SATA-2, 7200 RPM  
- **Other Hardware:** None

**Software**

- **Operating System:** Red Hat Enterprise Linux Server Release 6.2, 2.6.32-220.el6.x86_64  
- **Compiler:** C/C++: Version 12.1.0 of Intel Compiler XE Build 20111011  
- **Auto Parallel:** No  
- **File System:** ext4  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 32-bit  
- **Peak Pointers:** 32/64-bit  
- **Other Software:** Microquill SmartHeap V8.1
## Itautec

Servidor Itautec LX105 (Intel Xeon E3-1220)

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<td>154</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

### Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
Large pages were not enabled for this run

### Base Compiler Invocation

C benchmarks:
icc  -m32

C++ benchmarks:
icpc -m32

### Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
SPEC CINT2006 Result

Itautec
Servidor Itautec LX105 (Intel Xeon E3-1220)

SPECint_rate2006 = 143
SPECint_rate_base2006 = 138

CPU2006 license: 9001
Test sponsor: Itautec
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Test date: Mar-2012
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Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
  icc  -m32
  400.perlbench: icc  -m64
  401.bzip2: icc  -m64
  456.hmmer: icc  -m64
  458.sjeng: icc  -m64
C++ benchmarks:
  icpc  -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page
### SPEC CINT2006 Result

**Itautec**  
Servidor Itautec LX105 (Intel Xeon E3-1220)  

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#### Peak Optimization Flags (Continued)

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<td><code>-ipo</code></td>
<td><code>-O3</code></td>
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<td></td>
<td><code>-no-prec-div</code></td>
<td><code>-prof-use</code></td>
<td><code>auto-ilp32</code></td>
<td><code>-auto-ilp32</code></td>
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<td>bzip2</td>
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<td><code>-prof-gen</code></td>
<td><code>-ipo</code></td>
<td><code>-O3</code></td>
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<td><code>-no-prec-div</code></td>
<td><code>-prof-use</code></td>
<td><code>-opt-prefetch</code></td>
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<td><code>-O3</code></td>
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<td>mcf</td>
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<td>sjeng</td>
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<td><code>-W1,-z,muldefs</code></td>
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<td><code>-O3</code></td>
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</table>

C++ benchmarks:

- `omnetpp`  
- `astar`  
- `xalancbmk`

### Peak Other Flags

**C benchmarks:**

- `gcc`: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at:

# SPEC CINT2006 Result

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