



SPEC® CINT2006 Result

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Fujitsu

SPECint®2006 = 47.2

CELSIUS R920 (Intel Xeon E5-2637)

SPECint_base2006 = 44.6

CPU2006 license: 19

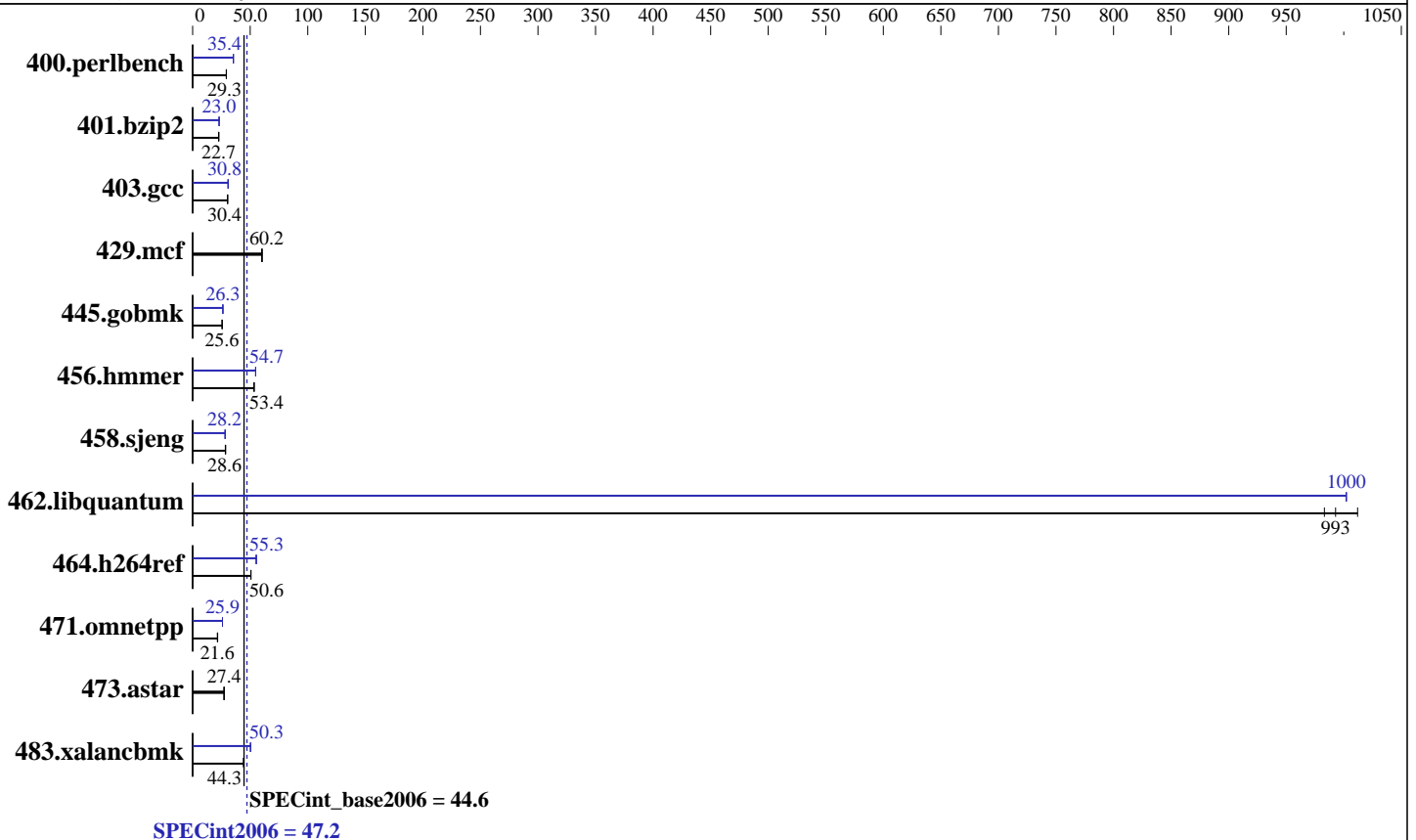
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Mar-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-2637
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 5 MB I+D on chip per chip
 Other Cache: None
 Memory: 64 GB (16 x 4 GB 2Rx8 PC3-12800R-11, ECC)
 Disk Subsystem: 1 x SATA III, 500 GB, 7200 rpm
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.1, 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++: Version 12.1.2.273 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi - user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap 10 (Multi-Core)



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	333	29.3	<u>334</u>	<u>29.3</u>	335	29.2	<u>277</u>	<u>35.3</u>	276	35.5	<u>276</u>	<u>35.4</u>
401.bzip2	425	22.7	<u>426</u>	<u>22.7</u>	427	22.6	<u>420</u>	<u>23.0</u>	419	23.0	420	23.0
403.gcc	<u>265</u>	<u>30.4</u>	265	30.3	265	30.4	<u>261</u>	<u>30.8</u>	261	30.8	262	30.8
429.mcf	<u>151</u>	<u>60.2</u>	151	60.5	152	60.0	<u>151</u>	<u>60.2</u>	151	60.5	152	60.0
445.gobmk	409	25.6	<u>409</u>	<u>25.6</u>	409	25.6	399	26.3	400	26.3	<u>399</u>	<u>26.3</u>
456.hammer	175	53.3	<u>175</u>	<u>53.4</u>	175	53.4	<u>170</u>	<u>54.7</u>	170	54.7	170	54.7
458.sjeng	<u>423</u>	<u>28.6</u>	423	28.6	426	28.4	428	28.3	429	28.2	<u>429</u>	<u>28.2</u>
462.libquantum	<u>20.9</u>	<u>993</u>	20.5	1010	21.1	983	<u>20.7</u>	<u>1000</u>	20.7	1000	20.7	1000
464.h264ref	437	50.7	<u>437</u>	<u>50.6</u>	438	50.5	400	55.4	401	55.1	<u>400</u>	<u>55.3</u>
471.omnetpp	<u>289</u>	<u>21.6</u>	287	21.7	291	21.5	241	25.9	<u>241</u>	<u>25.9</u>	241	25.9
473.astar	<u>256</u>	<u>27.4</u>	255	27.5	259	27.1	<u>256</u>	<u>27.4</u>	255	27.5	259	27.1
483.xalancbmk	155	44.5	156	44.3	<u>156</u>	<u>44.3</u>	137	50.3	<u>137</u>	<u>50.3</u>	137	50.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
Hyper-Threading Technology = Disabled
Frequency Floor Override = Enabled

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/work/cpu2006/libs/32:/work/cpu2006/libs/64"
OMP_NUM_THREADS = "4"

Binaries compiled on a system with
2x Xeon E5-2690 CPU + 64 GB memory using
Red Hat Enterprise Linux Server release 6.1 (Santiago)

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled



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Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/opt/SmartHeap/lib64 -lsmartheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

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Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias`

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Peak Optimization Flags (Continued)

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch
-auto-p32

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.xml>



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For other inquiries, please contact webmaster@spec.org.

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