Supermicro

SuperServer 1017C-TF (X9SCL-F, Intel i3-2100)

SPECint\_rate2006 = 86.4
SPECint\_rate\_base2006 = 82.5

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

CPU Name: Intel Core i3-2100
CPU Characteristics:
CPU MHz: 3100
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 3 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)
Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
Other Hardware: None

Operating System: Red Hat Enterprise Linux Server release 6.1 (Santiago)
Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V9.01
## SPEC CINT2006 Result

**Supermicro**

SuperServer 1017C-TF (X9SCL-F, Intel i3-2100)

<table>
<thead>
<tr>
<th>CPU2006 license: 001176</th>
<th>Test date: Mar-2012</th>
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<tr>
<td>Test sponsor: Supermicro</td>
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<td>Tested by: Supermicro</td>
<td>Software Availability: Oct-2011</td>
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### SPECint_rate2006 = 86.4

### SPECint_rate_base2006 = 82.5

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**Results Table**

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<tr>
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<th>Seconds</th>
<th>Ratio</th>
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**Peak**

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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

Sysinfo program /home/cpu2006/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdfff5032aaa42e583f96b07f99d3
running on localhost.localdomain Fri Mar 30 19:34:20 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Core(TM) i3-2100 CPU @ 3.10GHz
  1 "physical id"s (chips)
  4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 2
siblings : 4

Continued on next page
Supermicro
SuperServer 1017C-TF (X9SCL-F, Intel i3-2100)

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Platform Notes (Continued)

    physical 0: cores 0 1
    cache size : 3072 KB

From /proc/meminfo
    MemTotal: 8040476 kB
    HugePages_Total: 0
    Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
    Red Hat Enterprise Linux Server release 6.1 (Santiago)

From /etc/*release* /etc/*version*
    redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
    system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)

uname -a:
    Linux localhost.localdomain 2.6.32-131.0.15.el6.x86_64 #1 SMP Tue May 10
        15:42:40 EDT 2011 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 30 19:28

SPEC is set to: /home/cpu2006
    Filesystem Type Size Used Avail Use% Mounted on
    /dev/mapper/VolGroup-lv_home
        ext4 392G 66G 306G 18% /home

Additional information from dmidecode:
    Memory:
        2x Hynix Semiconduc HMT351U7BFR8C-H9 4 GB 1333 MHz 2 rank

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
    LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
    Transparent Huge Pages enabled with:
        echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
    icc -m32

Continued on next page
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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32
400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32
Supermicro
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### Peak Portability Flags

- 400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
- 401.bzip2: -DSPEC_CPU_LP64
- 456.hmmer: -DSPEC_CPU_LP64
- 458.sjeng: -DSPEC_CPU_LP64
- 462.libquantum: -DSPEC_CPU_LINUX
- 483.xalancbmk: -DSPEC_CPU_LINUX

### Peak Optimization Flags

**C** benchmarks:

- 400.perlbench: -xAVX (pass 2)  -prof-gen (pass 1) -ipo (pass 2)  -O3 (pass 2)
  -no-prec-div (pass 2)  -prof-use (pass 2)  -auto-ilp32
- 401.bzip2: -xAVX (pass 2)  -prof-gen (pass 1) -ipo (pass 2)  -O3 (pass 2)
  -no-prec-div (pass 2)  -prof-use (pass 2)  -opt-prefetch
  -auto-ilp32 -ansi-alias
- 403.gcc: -xAVX -ipo -O3 -no-prec-div
- 429.mcf: basepeak = yes
- 445.gobmk: -xAVX (pass 2)  -prof-gen (pass 1) -prof-use (pass 2)
  -ansi-alias -opt-mem-layout-trans=3
- 456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
- 458.sjeng: -xAVX (pass 2)  -prof-gen (pass 1) -ipo (pass 2)  -O3 (pass 2)
  -no-prec-div (pass 2)  -prof-use (pass 2)  -unroll4
  -auto-ilp32
- 462.libquantum: basepeak = yes
- 464.h264ref: -xAVX (pass 2)  -prof-gen (pass 1) -ipo (pass 2)  -O3 (pass 2)
  -no-prec-div (pass 2)  -prof-use (pass 2)  -unroll2
  -ansi-alias

**C++** benchmarks:

- 471.omnetpp: -xAVX (pass 2)  -prof-gen (pass 1) -ipo (pass 2)  -O3 (pass 2)
  -no-prec-div (pass 2)  -prof-use (pass 2)  -ansi-alias
  -opt-ra-region-strategy=block -Wl,-z,muldefs -L/smartheap -lsmartheap
- 473.astar: basepeak = yes
- 483.xalancbmk: basepeak = yes
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Peak Other Flags

C benchmarks:

403.gcc -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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