Supermicro
SuperServer 1017C-TF (X9SCL-F, Intel E3-1220)

SPECint®_rate2006 = 143
SPECint_rate_base2006 = 138

CPU2006 license: 001176
Test date: Mar-2012
Test sponsor: Supermicro
Hardware Availability: Apr-2011
Tested by: Supermicro
Software Availability: Oct-2011

<table>
<thead>
<tr>
<th>SPECint rates</th>
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</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>4</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>4</td>
</tr>
<tr>
<td>403.gcc</td>
<td>4</td>
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<td>4</td>
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<tr>
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<td>4</td>
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<td>4</td>
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</table>

Software
Operating System: Red Hat Enterprise Linux Server Release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V9.01

Hardware
CPU Name: Intel Xeon E3-1220
CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz
CPU MHz: 3100
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (4 x 4 GB 2Rx8 PC3-10600E-9, ECC)
Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
Other Hardware: None
## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>4</td>
<td>367</td>
<td>107</td>
<td>367</td>
<td>107</td>
<td>366</td>
<td>107</td>
<td>366</td>
<td>107</td>
<td>310</td>
<td>126</td>
<td></td>
<td></td>
</tr>
<tr>
<td>401.bzip2</td>
<td>4</td>
<td>530</td>
<td>72.8</td>
<td>531</td>
<td>72.6</td>
<td>530</td>
<td>72.8</td>
<td>495</td>
<td>78.0</td>
<td>498</td>
<td>77.6</td>
<td>497</td>
<td>77.7</td>
</tr>
<tr>
<td>403.gcc</td>
<td>4</td>
<td>302</td>
<td>107</td>
<td>301</td>
<td>107</td>
<td>300</td>
<td>107</td>
<td>302</td>
<td>107</td>
<td>305</td>
<td>106</td>
<td>302</td>
<td>107</td>
</tr>
<tr>
<td>429.mcf</td>
<td>4</td>
<td>172</td>
<td>212</td>
<td>171</td>
<td>213</td>
<td>171</td>
<td>213</td>
<td>172</td>
<td>212</td>
<td>171</td>
<td>213</td>
<td></td>
<td></td>
</tr>
<tr>
<td>445.gobmk</td>
<td>4</td>
<td>453</td>
<td>92.5</td>
<td>453</td>
<td>92.7</td>
<td>452</td>
<td>92.8</td>
<td>440</td>
<td>95.4</td>
<td>441</td>
<td>95.1</td>
<td>440</td>
<td>95.3</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>4</td>
<td>207</td>
<td>180</td>
<td>209</td>
<td>178</td>
<td>206</td>
<td>181</td>
<td>195</td>
<td>191</td>
<td>194</td>
<td>192</td>
<td>195</td>
<td>192</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>4</td>
<td>479</td>
<td>101</td>
<td>479</td>
<td>101</td>
<td>478</td>
<td>101</td>
<td>460</td>
<td>105</td>
<td>460</td>
<td>105</td>
<td></td>
<td></td>
</tr>
<tr>
<td>462.libquantum</td>
<td>4</td>
<td>95.8</td>
<td>865</td>
<td>95.2</td>
<td>871</td>
<td>95.1</td>
<td>872</td>
<td>95.8</td>
<td>865</td>
<td>95.2</td>
<td>871</td>
<td></td>
<td></td>
</tr>
<tr>
<td>464.h264ref</td>
<td>4</td>
<td>457</td>
<td>194</td>
<td>456</td>
<td>194</td>
<td>458</td>
<td>193</td>
<td>446</td>
<td>198</td>
<td>454</td>
<td>195</td>
<td>452</td>
<td>196</td>
</tr>
<tr>
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<td>4</td>
<td>318</td>
<td>78.6</td>
<td>318</td>
<td>78.5</td>
<td>318</td>
<td>78.6</td>
<td>302</td>
<td>82.7</td>
<td>303</td>
<td>82.5</td>
<td></td>
<td></td>
</tr>
<tr>
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<td>4</td>
<td>360</td>
<td>78.1</td>
<td>362</td>
<td>77.7</td>
<td>361</td>
<td>77.7</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
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<td>4</td>
<td>182</td>
<td>152</td>
<td>183</td>
<td>151</td>
<td>183</td>
<td>151</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage-enabled

## Base Compiler Invocation

C benchmarks:
  icc -m32

C++ benchmarks:
  icpc -m32
### Supermicro

**SuperServer 1017C-TF (X9SCL-F, Intel E3-1220)**

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- **Test sponsor:** Supermicro
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### Base Portability Flags

- 400.perlbench: -DSPEC_CPU_LINUX_IA32
- 462.libquantum: -DSPEC_CPU_LINUX
- 483.xalancbmk: -DSPEC_CPU_LINUX

### Base Optimization Flags

**C benchmarks:**
- -xAVX
- -ipo
- -O3
- -no-prec-div
- -opt-prefetch
- -opt-mem-layout-trans=3

**C++ benchmarks:**
- -xAVX
- -ipo
- -O3
- -no-prec-div
- -opt-prefetch
- -opt-mem-layout-trans=3
- -Wl,-z,muldefs -L/smartheap -lsmartheap

### Base Other Flags

**C benchmarks:**
- 403.gcc: -Dalloca=_alloca

### Peak Compiler Invocation

**C benchmarks (except as noted below):**
- icc -m32
  - 400.perlbench: icc -m64
  - 401.bzip2: icc -m64
  - 456.hmmer: icc -m64
  - 458.sjeng: icc -m64

**C++ benchmarks:**
- icpc -m32

### Peak Portability Flags

- 400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
- 401.bzip2: -DSPEC_CPU_LP64
- 456.hmmer: -DSPEC_CPU_LP64
- 458.sjeng: -DSPEC_CPU_LP64
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### Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX  
483.xalancbmk: -DSPEC_CPU_LINUX

### Peak Optimization Flags

**C benchmarks:**

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll12 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12 -ansi-alias

**C++ benchmarks:**

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes
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Peak Other Flags

C benchmarks:
403.gcc -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml

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For other inquiries, please contact webmaster@spec.org.

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