Acer Incorporated

Acer AR320 F2 (Intel Core i3-2100)

**SPECint\_rate2006** = 84.8
**SPECint\_rate\_base2006** = 81.0

**Hardware**
- **CPU Name:** Intel Core i3-2100
- **CPU Characteristics:**
  - **CPU MHZ:** 3100
  - **FPU:** Integrated
  - **CPU(s) enabled:** 2 cores, 1 chip, 2 cores/chip, 2 threads/core
  - **CPU(s) orderable:** 1 chip
  - **Primary Cache:** 32 KB I + 32 KB D on chip per core
  - **Secondary Cache:** 256 KB I+D on chip per core
  - **L3 Cache:** 3 MB I+D on chip per chip
  - **Other Cache:** None
  - **Memory:** 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)
  - **Disk Subsystem:** 1 x 500 GB SATA, 7200RPM
  - **Other Hardware:** None

**Software**
- **Operating System:** Red Hat Enterprise Linux Server release 6.1 (Santiago)
  - 2.6.32-131.0.15.el6.x86_64
- **Compiler:** C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
- **Auto Parallel:** No
- **File System:** ext4
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 32-bit
- **Peak Pointers:** 32/64-bit
- **Other Software:** Microquill SmartHeap V9.01
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CPU2006 license: 97
Test date: Mar-2012
Test sponsor: Acer Incorporated
Hardware Availability: Jan-2012
Tested by: Acer Incorporated
Software Availability: Dec-2011

Results Table

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<td>297</td>
<td>93.0</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes
Sysinfo program /usr/cpu2006/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 $6f2ebdf5f032aaa4e583f96b07f99d3
running on localhost.localdomain Thu Mar 1 09:11:28 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Core(TM) i3-2100 CPU @ 3.10GHz
  1 "physical id"s (chips)
  4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 2
siblings : 4

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SPEC CINT2006 Result

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Platform Notes (Continued)

physical 0: cores 0 1
cache size : 3072 KB

From /proc/meminfo
MemTotal: 8046960 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.1 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)

uname -a:
Linux localhost.localdomain 2.6.32-131.0.15.el6.x86_64 #1 SMP Tue May 10
15:42:40 EDT 2011 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 1 09:07

SPEC is set to: /usr/cpu2006

Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_root ext4 50G 8.1G 39G 18% /

Additional information from dmidecode:
Memory:
2x Nanya NT4GC72B8PG0NF-DI 4 GB 1333 MHz 2 rank

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
icc -m32

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### Base Compiler Invocation (Continued)

**C++ benchmarks:**
- icpc -m32

### Base Portability Flags

- 400.perlbench: `-DSPEC_CPU_LINUX_IA32`
- 462.libquantum: `-DSPEC_CPU_LINUX`
- 483.xalancbmk: `-DSPEC_CPU_LINUX`

### Base Optimization Flags

**C benchmarks:**
- `-xAVX -ipo -O3 -no-prec-div -opt-prefetch`

**C++ benchmarks:**
- `-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs -L/smartheap -lsmartheap`

### Base Other Flags

**C benchmarks:**
- 403.gcc: `-Dalloca=_alloca`

### Peak Compiler Invocation

**C benchmarks (except as noted below):**
- icc -m32
- 400.perlbench: icc -m64
- 401.bzip2: icc -m64
- 456.hmmer: icc -m64
- 458.sjeng: icc -m64

**C++ benchmarks:**
- icpc -m32
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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-No-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-No-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias
403.gcc: -xAVX -ipo -O3 -no-prec-div
429.mcf: basepeak = yes
445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias
456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll12 -auto-ilp32
458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14
-auto-ilp32
462.libquantum: basepeak = yes
464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

473.astar: basepeak = yes
483.xalancbmk: basepeak = yes
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## Peak Other Flags

**C benchmarks:**

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Originally published on 22 May 2012.