SPEC® CINT2006 Result

Fujitsu

PRIMERGY TX140 S1p, Intel Celeron G550, 2.60 GHz

SPEClnt®2006 = NC
SPEClnt_base2006 = NC

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result does not meet the 3 month availability requirement in the SPEC CPU2006 run rules, because the tested processor was not supported on this platform within three months of publication.

Non-Compliant

Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Celeron G550</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Characteristics:</td>
<td></td>
</tr>
<tr>
<td>CPU MHz:</td>
<td>2600</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>2 cores, 1 chip, 2 cores/chip</td>
</tr>
</tbody>
</table>

Software

<table>
<thead>
<tr>
<th>Operating System:</th>
<th>Red Hat Enterprise Linux Server release 6.2 (Santiago)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 12.1.0.293 of Intel C++ Studio XE for Linux</td>
</tr>
</tbody>
</table>

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SPEC CINT2006 Result

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SPECint2006 = NC
SPECint_base2006 = NC

Test date: May-2012
Hardware Availability: Jun-2012
Software Availability: Feb-2012

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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
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Platform Notes

BIOS configuration:
Intel HT Technology = Disable

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"
OMP_NUM_THREADS = "2"

Binaries compiled on a system with 1x E3-1270v2 CPU + 32 GB memory using RHEL6.2
For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-rec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-rec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/opt/SmartHeap/lib64 -lsmartheap64

Base Other Flags

C benchmarks:
-Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
  icc -m64

400.perlbench: icc -m32

Continued on next page
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Peak Compiler Invocation (Continued)

445.gobmk: icc -m32
464.h264ref: icc -m32
C++ benchmarks (except as noted below):
icpc -m32
473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias
Continued on next page
Fujitsu

PRIMERGY TX140 S1p, Intel Celeron G550, 2.60 GHz

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

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Peak Optimization Flags (Continued)

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32
429.mcf: basepeak = y
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32 -ansi-alias
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3 -unroll2 -prof-use(pass 2)
462.libquantum: basepeak = yes
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3 -unroll2 -ansi-alias
471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
473.astar: basepeak = yes
483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result does not meet the 3 month availability requirement in the SPEC CPU2006 run rules, because the tested processor was not supported on this platform within three months of publication.

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
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