IBM Corporation

IBM System x3630 M4 (Intel Xeon E5-2403)

SPECfp®2006 = 44.3
SPECfp_base2006 = 42.5

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jul-2012
Hardware Availability: May-2012
Software Availability: Dec-2011

Hardware

<table>
<thead>
<tr>
<th>SPECfp Base2006 = 42.5</th>
</tr>
</thead>
</table>

| SPECfp2006 = 44.3 |

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: ext4
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- **L3 Cache:** 10 MB I+D on chip per chip
- **Other Cache:** None
- **Memory:** 96 GB (12 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1066 MHz)
- **Disk Subsystem:** 1 x 500 GB SATA, 7200 RPM
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 32/64-bit
- **Other Software:** None

**Operating System Notes**
Stack size set to unlimited using "ulimit -s unlimited"
Zone reclaim mode enabled with:
```
echo 1 > /proc/sys/vm/zone_reclaim_mode
```

**Platform Notes**

**BIOS setting:**
Operating Mode set to Maximum Performance
Sysinfo program /home/SPECcpu-v1.2/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on x3630m4-rhel62 Sat Jul 21 15:09:52 2012

This section contains SUT (System Under Test) info as seen by
Continued on next page
Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2403 0 @ 1.80GHz
   2 "physical id"s (chips)
   8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
cautions.)
   cpu cores : 4
   siblings : 4
   physical 0: cores 0 1 2 3
   physical 1: cores 0 1 2 3
   cache size : 10240 KB

From /proc/meminfo
MemTotal:       99044920 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.2 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)

uname -a:
   Linux x3630m4-rhel62 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
      x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Jul 20 12:26

SPEC is set to: /home/SPECcpu-v1.2
   Filesystem    Type    Size  Used Avail Use% Mounted on
   /dev/mapper/vg_x3630m4rhel62-lv_home  ext4    383G  21G  343G   6% /home

Additional information from dmidecode:
   Memory:
      12x Samsung M393B1K70DH0-CK0 8 GB 1066 MHz 2 rank

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/SPECcpu-v1.2/libs/32:/home/SPECcpu-v1.2/libs/64"
SPECCFP2006 Result

IBM Corporation
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</table>

**General Notes (Continued)**

OMP_NUM_THREADS = "8"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

**Base Compiler Invocation**

C benchmarks:
```
icc  -m64
```

C++ benchmarks:
```
icpc -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc  -m64 ifort -m64
```

**Base Portability Flags**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Portability Flags</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>416.game5s</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>433.mlirc</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>-DSPEC_CPU_LP64 -nofor_main</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>-DSPEC_CPU_LP64 -nofor_main</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>444.namd</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>447.dealII</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>450.soplex</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>453.povray</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>454.calculix</td>
<td>-DSPEC_CPU_LP64 -nofor_main</td>
</tr>
<tr>
<td>459.GemsFD3D</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>465.tonto</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>470.lbm</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>481.wrf</td>
<td>-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
</tbody>
</table>

**Base Optimization Flags**

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias
```

Continued on next page
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Base Optimization Flags (Continued)

C++ benchmarks:
- xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:
- xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
- xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
  -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
  -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
  -ansi-alias
470.lbm: basepeak = yes
482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
  -parallel

C++ benchmarks:

Continued on next page
The flags files that were used to format this result can be browsed at:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html
You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml
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For other inquiries, please contact webmaster@spec.org.

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