# SPEC® CINT2006 Result

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## Fujitsu

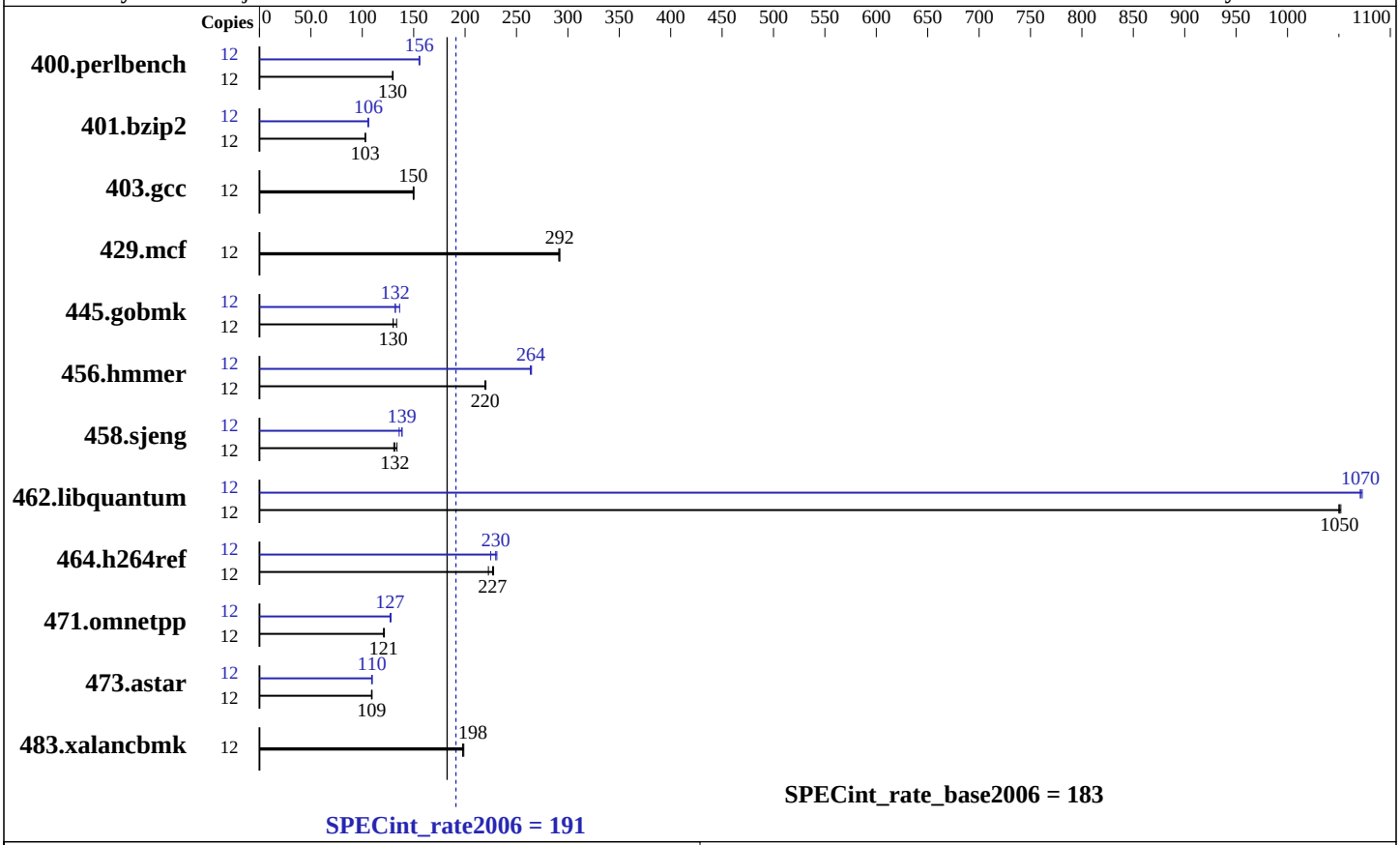
PRIMERGY TX150 S8, Intel Xeon E5-2420, 1.90 GHz

**SPECint®_rate2006 = 191**

**SPECint_rate_base2006 = 183**

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Jun-2012
**Hardware Availability:** Jul-2012
**Software Availability:** Feb-2012




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2420 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.40 GHz |
| CPU MHz: | 1900 |
| FPU: | Integrated |
| CPU(s) enabled: | 6 cores, 1 chip, 6 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 15 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 48 GB (6 x 8 GB 2Rx4 PC3L-12800R-11, ECC, running at 1333 MHz and CL9) |
| Disk Subsystem: | 1 x SATA, 500 GB, 7200 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 6.2 (Santiago)<br>2.6.32-220.el6.x86_64 |
| Compiler: | C/C++: Version 12.1.0.293 of Intel C++ Studio XE for Linux |
| Auto Parallel: | No |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V10.0 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 12 | 902 | 130 | **905** | **130** | 905 | 129 | 12 | 752 | 156 | **753** | **156** | 755 | 155 |
| 401.bzip2 | 12 | **1125** | **103** | 1122 | 103 | 1127 | 103 | 12 | 1098 | 105 | **1092** | **106** | 1092 | 106 |
| 403.gcc | 12 | 643 | 150 | 646 | 150 | **645** | **150** | 12 | 643 | 150 | 646 | 150 | **645** | **150** |
| 429.mcf | 12 | 376 | 291 | **375** | **292** | 375 | 292 | 12 | 376 | 291 | **375** | **292** | 375 | 292 |
| 445.gobmk | 12 | 969 | 130 | 944 | 133 | **968** | **130** | 12 | **952** | **132** | 954 | 132 | 923 | 136 |
| 456.hmmer | 12 | 510 | 219 | 508 | 220 | **510** | **220** | 12 | 423 | 265 | **424** | **264** | 425 | 264 |
| 458.sjeng | 12 | **1103** | **132** | 1086 | 134 | 1111 | 131 | 12 | 1069 | 136 | 1047 | 139 | **1048** | **139** |
| 462.libquantum | 12 | 237 | 1050 | **237** | **1050** | 236 | 1050 | 12 | 232 | 1070 | **232** | **1070** | 232 | 1070 |
| 464.h264ref | 12 | 1166 | 228 | **1171** | **227** | 1193 | 223 | 12 | 1150 | 231 | 1181 | 225 | **1156** | **230** |
| 471.omnetpp | 12 | 617 | 122 | **620** | **121** | 621 | 121 | 12 | 590 | 127 | **590** | **127** | 586 | 128 |
| 473.astar | 12 | **771** | **109** | 773 | 109 | 770 | 109 | 12 | **769** | **110** | 772 | 109 | 769 | 110 |
| 483.xalancbmk | 12 | 419 | 198 | 417 | 199 | **418** | **198** | 12 | 419 | 198 | 417 | 199 | **418** | **198** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

 Binaries compiled on a system with 2x E5-2650 CPU + 96 GB
 memory using RHEL6.2
 For information about Fujitsu please visit: http://www.fujitsu.com
```

## Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

Continued on next page

## Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

473.astar: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3 -Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

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## Peak Optimization Flags (Continued)

483.xalancbmk: `basepeak = yes`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html`
`http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml`
`http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml`


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For other inquiries, please contact webmaster@spec.org.

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