SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

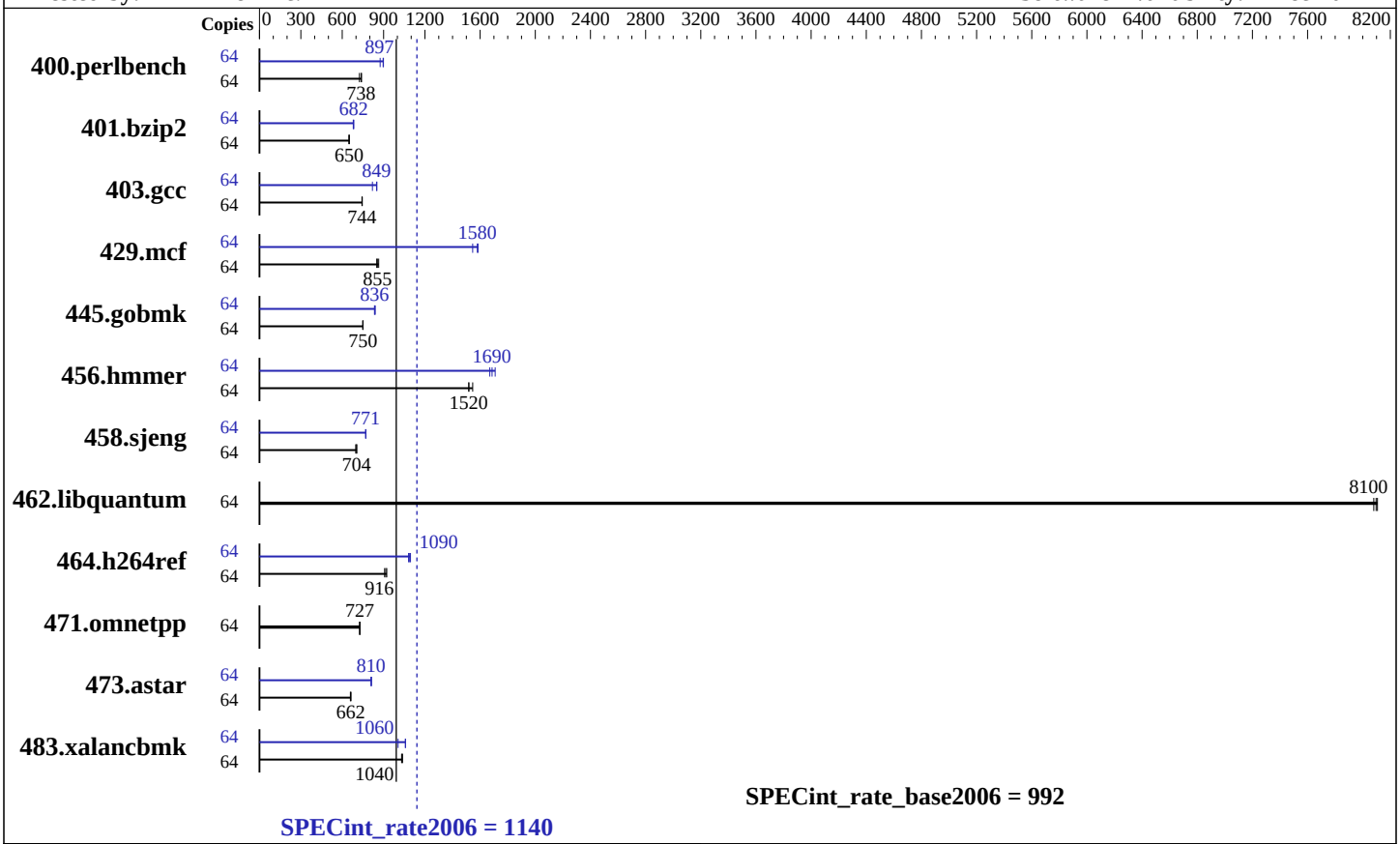
PowerEdge M915 (AMD Opteron 6284 SE, 2.70 GHz)

SPECint®_rate2006 = 1140

SPECint_rate_base2006 = 992

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jul-2012
Hardware Availability: Jul-2012
Software Availability: Dec-2011



Hardware

CPU Name:	AMD Opteron 6284 SE
CPU Characteristics:	AMD Turbo CORE technology up to 3.40 GHz
CPU MHz:	2700
FPU:	Integrated
CPU(s) enabled:	64 cores, 4 chips, 16 cores/chip
CPU(s) orderable:	2,4 chips
Primary Cache:	512 KB I on chip per chip, 64 KB I shared / 2 cores; 16 KB D on chip per core
Secondary Cache:	16 MB I+D on chip per chip, 2 MB shared / 2 cores
L3 Cache:	16 MB I+D on chip per chip, 8 MB shared / 8 cores
Other Cache:	None
Memory:	256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem:	1 x 300 GB SAS, 15000 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.2 Kernel 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 4.5.1 of x86 Open64 Compiler Suite (from AMD)
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (Full multiuser with network)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap 10.0 32-bit Library for Linux

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge M915 (AMD Opteron 6284 SE, 2.70 GHz)

SPECint_rate2006 = 1140

SPECint_rate_base2006 = 992

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jul-2012
Hardware Availability: Jul-2012
Software Availability: Dec-2011

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	64	863	725	**847**	**738**	845	740	64	715	875	**697**	**897**	696	899
401.bzip2	64	951	649	949	651	**950**	**650**	64	904	684	**905**	**682**	908	680
403.gcc	64	693	743	691	745	**692**	**744**	64	605	851	629	819	**607**	**849**
429.mcf	64	687	850	675	864	**683**	**855**	64	368	1580	378	1550	**370**	**1580**
445.gobmk	64	896	750	895	750	**895**	**750**	64	800	839	**803**	**836**	805	834
456.hmmer	64	386	1550	**393**	**1520**	393	1520	64	349	1710	**354**	**1690**	358	1670
458.sjeng	64	**1100**	**704**	1098	705	1110	698	64	1006	770	**1005**	**771**	1004	772
462.libquantum	64	164	8110	**164**	**8100**	164	8080	64	164	8110	**164**	**8100**	164	8080
464.h264ref	64	**1546**	**916**	1535	923	1561	907	64	**1301**	**1090**	1293	1090	1310	1080
471.omnetpp	64	550	727	550	728	**550**	**727**	64	550	727	550	728	**550**	**727**
473.astar	64	682	659	678	663	**678**	**662**	64	553	812	555	809	**554**	**810**
483.xalancbmk	64	**426**	**1040**	429	1030	426	1040	64	417	1060	439	1000	**418**	**1060**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152'  was used to set environment locked pages in memory limit

Set transparent_hugepage=never in run script.

Set vm/nr_hugepages=57344 in run script.
mount -t hugetlbfs nodev /mnt/hugepages
```

General Notes

```
Environment variables set by runspec before the start of the run:
HUGETLB_LIMIT = "896"
LD_LIBRARY_PATH = "/root/cpu2006/amd1104-rate-libs-revC/32:/root/cpu2006/amd1104-rate-libs-revC/64"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at
http://developer.amd.com/cpu/open64

Binaries were compiled on a system with 2x AMD Opteron 6274 chips + 64GB Memory using RHEL 6.1
```

Base Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-march=bdver1 -Ofast -CG:local_sched_alg=1 -INLINE:aggressive=ON
-IPA:plimit=8000 -IPA:small_pu=100 -HP:bd=2m:heap=2m -mso
-LNO:prefetch=2

C++ benchmarks:
-march=bdver1 -Ofast -m32 -INLINE:aggressive=on -CG:cmp_peep=on
-D__OPEN64_FAST_SET -L/root/work/libraries/SmartHeap-10/lib -lsmartheap

Peak Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

Continued on next page

Peak Portability Flags (Continued)

401.bzip2: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:prefetch=2 -LNO:opt=0
-IPA:plimit=20000 -OPT:unroll_times_max=8
-OPT:unroll_size=256 -OPT:unroll_level=2 -OPT:keep_ext=on
-WOPT:if_conv=0 -WOPT:sib=on -CG:local_sched_alg=1
-CG:unroll_fb_req=on -CG:movext_icmp=off -HP:bd=2m:heap=2m

401.bzip2: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -LNO:prefetch=2 -LNO:pf2=0
-OPT:alias=disjoint -OPT:goto=off -CG:local_sched_alg=1
-HP:bdt=2m:heap=2m

403.gcc: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:trip_count=256
-CG:cmp_peep=on -CG:pre_minreg_level=2 -m32
-HP:bdt=2m:heap=2m -GRA:unspill=on -IPA:small_pu=200
-WOPT:sib=on

429.mcf: -march=bdver1 -O3 -OPT:unroll_times_max=5 -ipa
-INLINE:aggressive=on -CG:gcm=off -CG:dsched=on
-GRA:prioritize_by_density=on -m32 -HP:bdt=2m:heap=2m -mso

445.gobmk: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:unroll_size=256
-OPT:unroll_times_max=8 -OPT:keep_ext=on -IPA:plimit=750
-IPA:min_hotness=300 -IPA:pu_reorder=1
-LNO:ignore_feedback=off -WOPT:if_conv=2 -HP:bd=2m:heap=2m

456.hmmer: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:prefetch=2
-OPT:alias=disjoint -OPT:unroll_times_max=16
-OPT:unroll_size=512 -OPT:unroll_level=2 -OPT:keep_ext=on
-CG:cflow=0 -CG:cmp_peep=on -CG:pre_local_sched=off
-HP:bdt=2m:heap=2m

Continued on next page

Peak Optimization Flags (Continued)

458.sjeng: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -CG:ptr_load_use=0
-CG:divrem_opt=on -CG:movext_icmp=off -CG:locs_best=on
-LNO:full_unroll=10 -IPA:pu_reorder=2 -HP:heap=2m:bd=2m
-WOPT:sib=on

462.libquantum: basepeak = yes

464.h264ref: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -OPT:unroll_size=256
-OPT:unroll_times_max=2 -IPA:plimit=20000
-OPT:alias=disjoint -CG:ptr_load_use=0
-CG:local_sched_alg=1 -HP:bdt=2m:heap=2m

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -TENV:frame_pointer=off
-WOPT:if_conv=0 -WOPT:sib=on -CG:divrem_opt=on
-CG:p2align=1 -CG:dsched=on -GRA:optimize_boundary=on
-OPT:alias=disjoint -INLINE:aggressive=on
-IPA:small_pu=3000 -IPA:plimit=3000 -m32
-HP:bdt=2m:heap=2m

483.xalancbmk: -march=bdver1 -Ofast -LNO:prefetch=2 -OPT:unroll_size=512
-OPT:unroll_times_max=8 -D__OPEN64_FAST_SET
-INLINE:aggressive=on -m32 -CG:cmp_peep=on
-CG:local_sched=off -CG:p2align=1 -GRA:unspill=on
-TENV:frame_pointer=off -fno-emit-exceptions
-L/root/work/libraries/SmartHeap-10/lib -lsmartheap

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/x86-open64-451-flags-rate-revC.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/x86-open64-451-flags-rate-revC.xml

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge M915 (AMD Opteron 6284 SE, 2.70 GHz)

SPECint_rate2006 = 1140

SPECint_rate_base2006 = 992

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jul-2012
Hardware Availability: Jul-2012
Software Availability: Dec-2011

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 11:00:15 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 29 August 2012.