SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

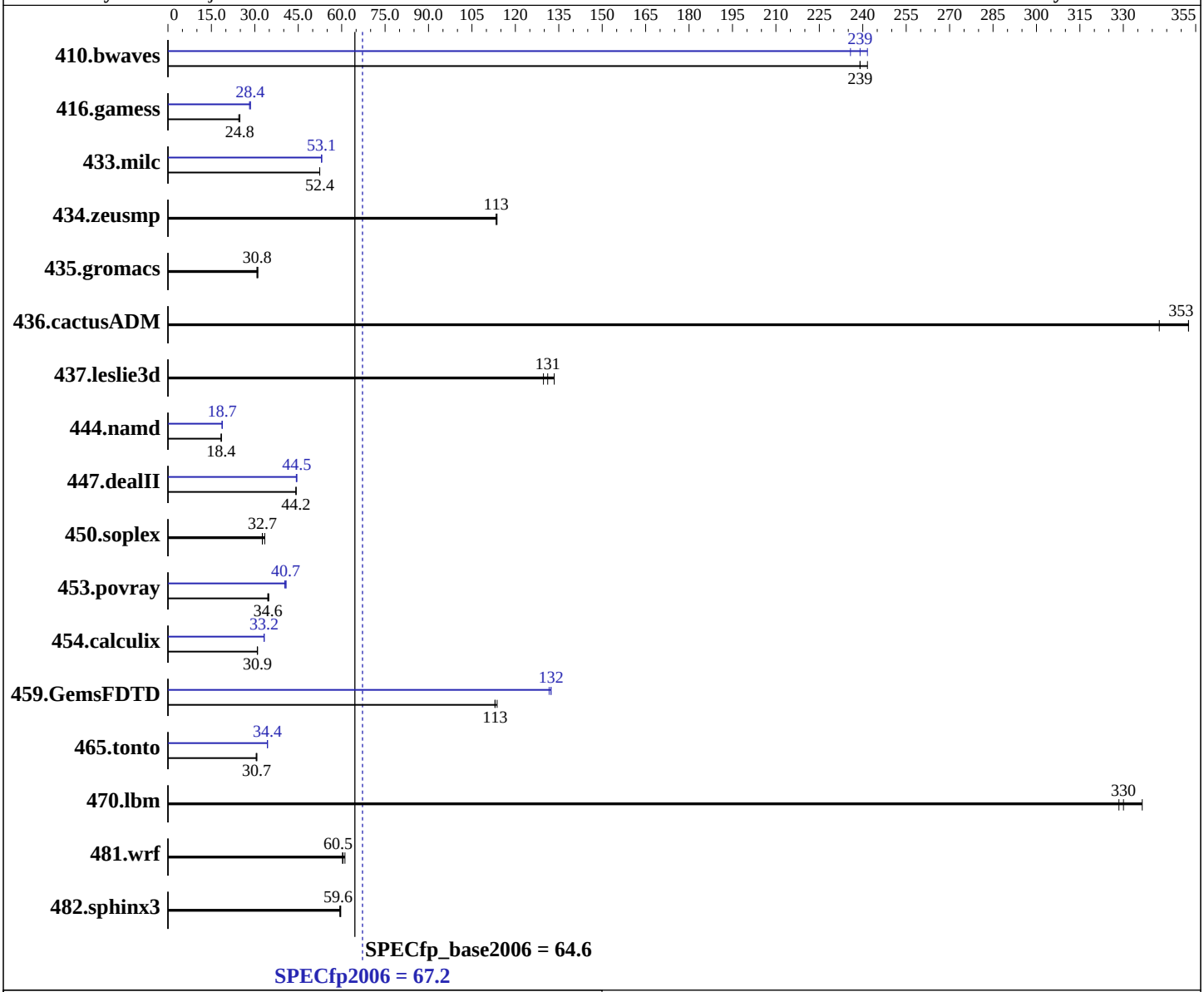
PRIMERGY TX200 S7, Intel Xeon E5-2430, 2.20 GHz

SPECfp®2006 = 67.2

SPECfp_base2006 = 64.6

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Aug-2012
Hardware Availability: Jul-2012
Software Availability: Feb-2012



Hardware

CPU Name:	Intel Xeon E5-2430
CPU Characteristics:	Intel Turbo Boost Technology up to 2.70 GHz
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.2 (Santiago) 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.0.293 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.293 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY TX200 S7, Intel Xeon E5-2430, 2.20 GHz

SPECfp2006 = 67.2

SPECfp_base2006 = 64.6

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Aug-2012
Hardware Availability: Jul-2012
Software Availability: Feb-2012

L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 96 GB (12 x 8 GB 2Rx4 PC3L-12800R-11, ECC, running at 1333 MHz and CL9)
Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	56.2	242	**56.8**	**239**	56.8	239	**56.8**	**239**	56.2	242	57.6	236
416.gamess	801	24.5	789	24.8	**790**	**24.8**	695	28.2	688	28.5	**689**	**28.4**
433.milc	**175**	**52.4**	175	52.4	175	52.4	173	53.0	**173**	**53.1**	173	53.1
434.zeusmp	80.2	113	80.0	114	**80.2**	**113**	80.2	113	80.0	114	**80.2**	**113**
435.gromacs	230	31.1	**232**	**30.8**	232	30.8	230	31.1	**232**	**30.8**	232	30.8
436.cactusADM	33.9	353	34.9	342	**33.9**	**353**	33.9	353	34.9	342	**33.9**	**353**
437.leslie3d	70.5	133	**71.7**	**131**	72.5	130	70.5	133	**71.7**	**131**	72.5	130
444.namd	436	18.4	**436**	**18.4**	436	18.4	428	18.7	428	18.7	**428**	**18.7**
447.dealII	**259**	**44.2**	259	44.2	259	44.2	258	44.3	257	44.5	**257**	**44.5**
450.soplex	249	33.5	256	32.6	**255**	**32.7**	249	33.5	256	32.6	**255**	**32.7**
453.povray	**154**	**34.6**	154	34.5	153	34.8	**131**	**40.7**	130	40.8	132	40.3
454.calculix	267	30.9	266	31.0	**267**	**30.9**	**248**	**33.2**	248	33.2	248	33.2
459.GemsFDTD	93.3	114	**93.9**	**113**	93.9	113	**80.2**	**132**	80.2	132	80.6	132
465.tonto	321	30.7	323	30.5	**321**	**30.7**	**286**	**34.4**	286	34.4	287	34.3
470.lbm	41.8	328	40.8	337	**41.6**	**330**	41.8	328	40.8	337	**41.6**	**330**
481.wrf	**185**	**60.5**	185	60.3	183	61.1	**185**	**60.5**	185	60.3	183	61.1
482.sphinx3	328	59.4	327	59.7	**327**	**59.6**	328	59.4	327	59.7	**327**	**59.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Platform Notes

```
BIOS configuration:
Frequency Floor Override = Enable
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"
OMP_NUM_THREADS = "12"

 Binaries compiled on a system with 1x E3-1270v2 CPU + 32 GB
 memory using RHEL6.2
 For information about Fujitsu please visit: http://www.fujitsu.com
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

Fortran benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html

You can also download the XML flags sources by saving the following links:

`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml`
`http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml`

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 10:24:13 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 11 September 2012.