



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY TX120 S3p, Intel Core i3-3220, 3.30 GHz

SPECint®2006 = 44.3

SPECint_base2006 = 42.0

CPU2006 license: 19

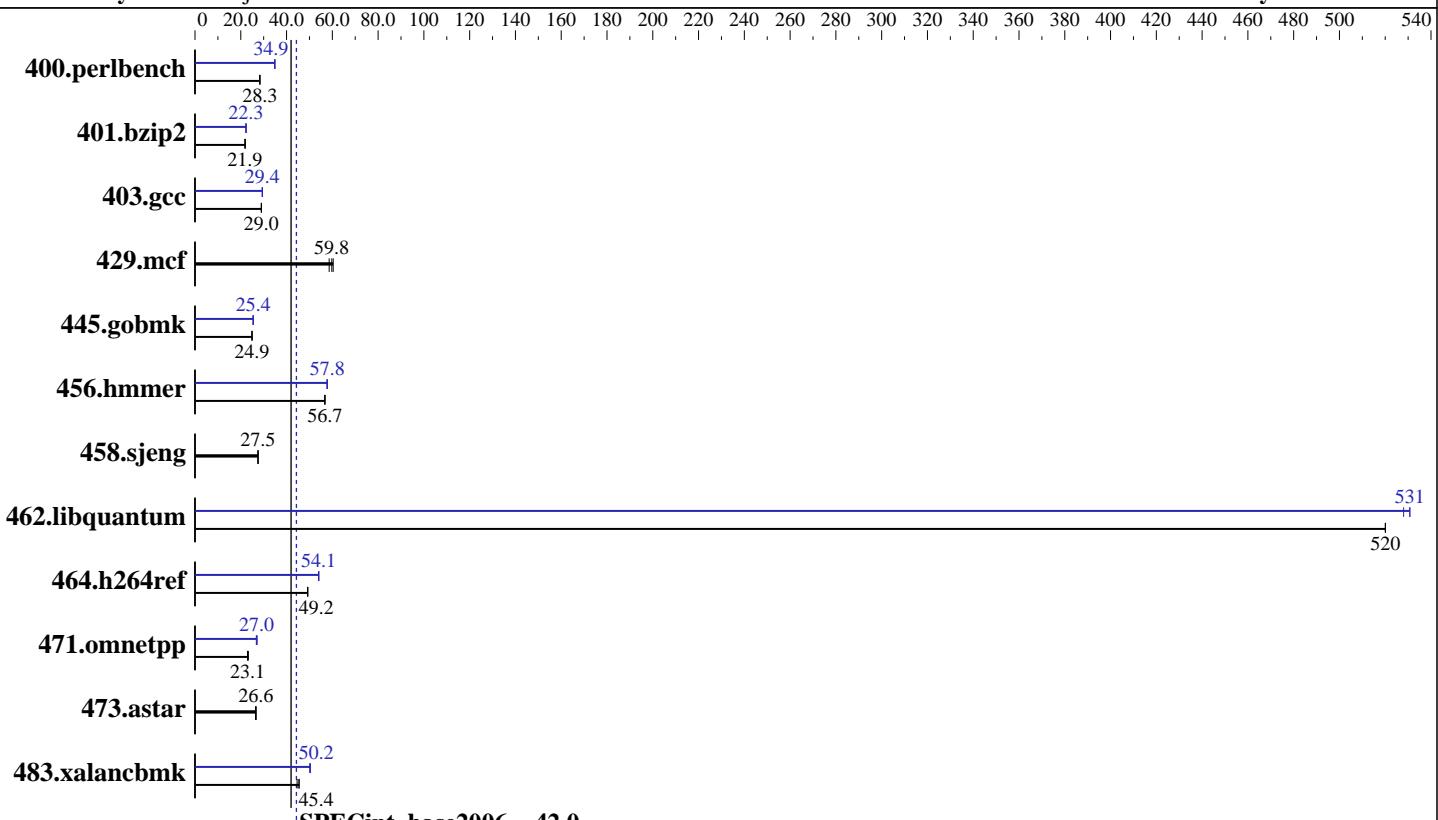
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jul-2012

Hardware Availability: Sep-2012

Software Availability: Feb-2012



Hardware

CPU Name: Intel Core i3-3220
CPU Characteristics:
CPU MHz: 3300
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 3 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)
Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
Compiler: 2.6.32-220.el6.x86_64
C/C++: Version 12.1.0.293 of Intel C++ Studio XE for Linux
Auto Parallel: Yes
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY TX120 S3p, Intel Core i3-3220, 3.30 GHz

SPECint2006 = 44.3

SPECint_base2006 = 42.0

CPU2006 license: 19

Test date: Jul-2012

Test sponsor: Fujitsu

Hardware Availability: Sep-2012

Tested by: Fujitsu

Software Availability: Feb-2012

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	345	28.4	345	28.3	345	28.3	280	34.9	280	34.9	280	34.9
401.bzip2	443	21.8	440	21.9	440	21.9	432	22.3	433	22.3	434	22.2
403.gcc	277	29.0	277	29.0	278	29.0	274	29.4	274	29.4	274	29.4
429.mcf	153	59.8	151	60.4	156	58.6	153	59.8	151	60.4	156	58.6
445.gobmk	421	24.9	421	24.9	421	24.9	413	25.4	413	25.4	413	25.4
456.hammer	164	56.8	164	56.7	165	56.7	162	57.8	162	57.8	162	57.7
458.sjeng	439	27.5	440	27.5	439	27.5	439	27.5	440	27.5	439	27.5
462.libquantum	39.8	520	39.8	520	39.8	520	39.0	531	39.2	528	39.0	531
464.h264ref	449	49.3	450	49.2	450	49.2	409	54.1	409	54.1	410	54.0
471.omnetpp	268	23.3	271	23.1	272	22.9	231	27.1	231	27.0	233	26.8
473.astar	264	26.6	265	26.5	263	26.7	264	26.6	265	26.5	263	26.7
483.xalancbmk	152	45.4	155	44.6	152	45.5	137	50.2	137	50.2	137	50.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enable

Platform Notes

BIOS configuration:

Intel HT Technology = Disable

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

OMP_NUM_THREADS = "2"

Binaries compiled on a system with 1x E3-1270v2 CPU + 32 GB memory using RHEL6.2

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:

icc -m64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY TX120 S3p, Intel Core i3-3220, 3.30 GHz

SPECint2006 = 44.3

SPECint_base2006 = 42.0

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jul-2012

Hardware Availability: Sep-2012

Software Availability: Feb-2012

Base Compiler Invocation (Continued)

C++ benchmarks:

`icpc -m64`

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64  
401.bzip2: -DSPEC_CPU_LP64  
403.gcc: -DSPEC_CPU_LP64  
429.mcf: -DSPEC_CPU_LP64  
445.gobmk: -DSPEC_CPU_LP64  
456.hammer: -DSPEC_CPU_LP64  
458.sjeng: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX  
464.h264ref: -DSPEC_CPU_LP64  
471.omnetpp: -DSPEC_CPU_LP64  
473.astar: -DSPEC_CPU_LP64  
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32`

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/opt/SmartHeap/lib64 -lsmartheap64`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY TX120 S3p, Intel Core i3-3220, 3.30 GHz

SPECint2006 = 44.3

SPECint_base2006 = 42.0

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jul-2012

Hardware Availability: Sep-2012

Software Availability: Feb-2012

Peak Compiler Invocation (Continued)

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
456.hmmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-opt-prefetch -ansi-alias`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32`
`-opt-prefetch -ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div -inline-calloc`
`-opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
`-ansi-alias`

456.hmmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32`
`-ansi-alias`

458.sjeng: `basepeak = yes`

462.libquantum: `-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch`
`-auto-p32`

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY TX120 S3p, Intel Core i3-3220, 3.30 GHz

SPECint2006 = 44.3

SPECint_base2006 = 42.0

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jul-2012

Hardware Availability: Sep-2012

Software Availability: Feb-2012

Peak Optimization Flags (Continued)

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Thu Jul 24 10:30:48 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 11 September 2012.