# SPEC® CINT2006 Result

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## Itautec

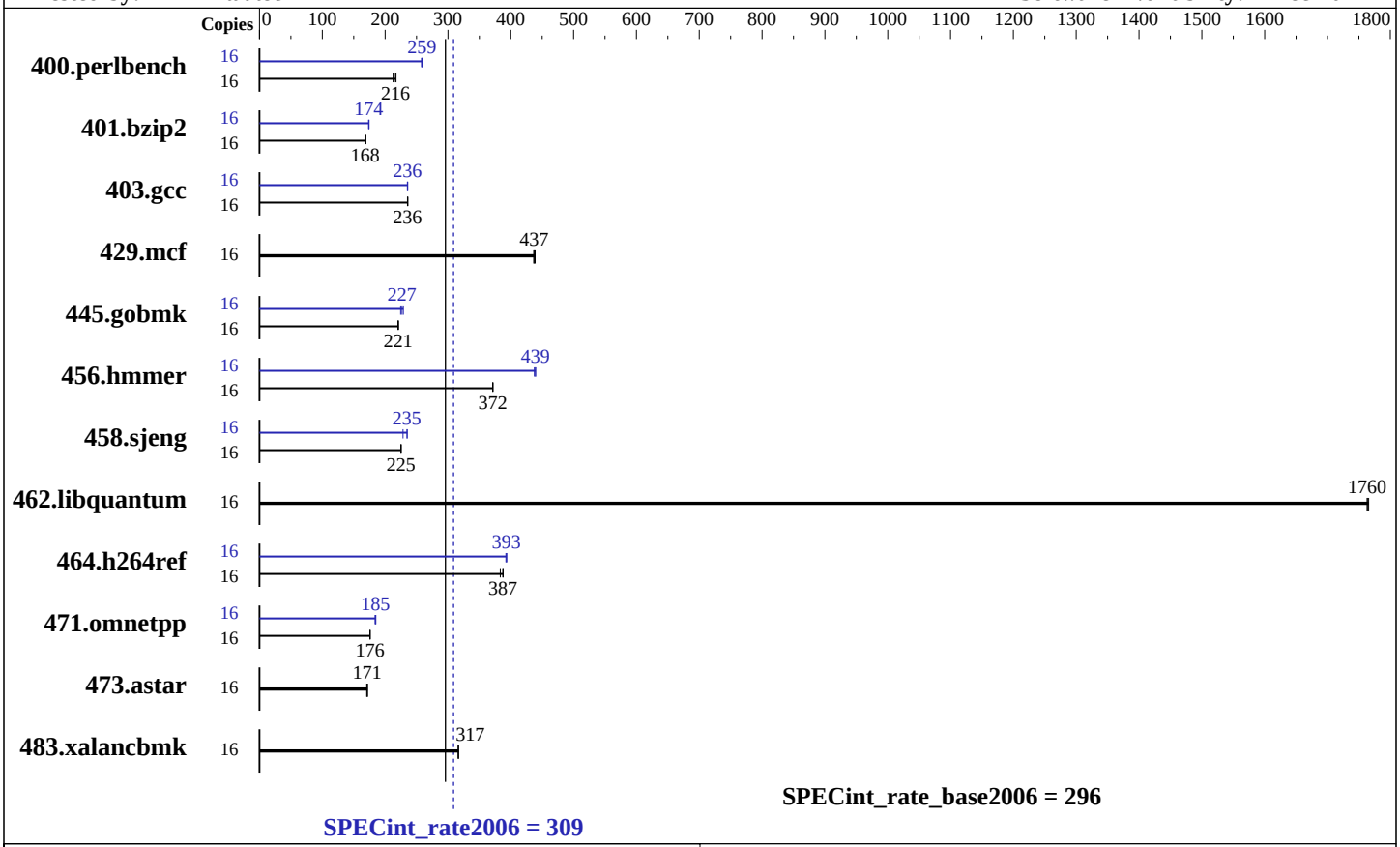
Servidor Itautec MX225+ (Intel Xeon E5-2665)

SPECint®_rate2006 = 309

SPECint_rate_base2006 = 296

**CPU2006 license:** 9001
**Test sponsor:** Itautec
**Tested by:** Itautec

**Test date:** Dec-2011
**Hardware Availability:** Jun-2012
**Software Availability:** Dec-2011




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2665 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.10 GHz |
| CPU MHz: | 2400 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 1 chip, 8 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 20 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 32 GB (8 x 4 GB 1Rx4 PC3-12800R-11, ECC) |
| Disk Subsystem: | 500 GB, SATA-2, 7200 RPM |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server Release 6.2, 2.6.32-220.el6.x86_64 |
| Compiler: | C/C++: Version 12.1.0 of Intel Compiler XE Build 20111011 |
| Auto Parallel: | No |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 16 | 719 | 217 | 735 | 213 | **722** | **216** | 16 | 606 | 258 | **605** | **259** | 604 | 259 |
| 401.bzip2 | 16 | 918 | 168 | **917** | **168** | 911 | 169 | 16 | **887** | **174** | 885 | 174 | 889 | 174 |
| 403.gcc | 16 | 545 | 236 | 547 | 236 | **546** | **236** | 16 | **546** | **236** | 546 | 236 | 547 | 235 |
| 429.mcf | 16 | 334 | 436 | **334** | **437** | 333 | 439 | 16 | 334 | 436 | **334** | **437** | 333 | 439 |
| 445.gobmk | 16 | **761** | **221** | 759 | 221 | 761 | 221 | 16 | **740** | **227** | 747 | 225 | 733 | 229 |
| 456.hmmer | 16 | 402 | 371 | 402 | 372 | **402** | **372** | 16 | 340 | 439 | 341 | 438 | **340** | **439** |
| 458.sjeng | 16 | 859 | 225 | **859** | **225** | 860 | 225 | 16 | 823 | 235 | 847 | 228 | **825** | **235** |
| 462.libquantum | 16 | 188 | 1760 | **188** | **1760** | 188 | 1770 | 16 | 188 | 1760 | **188** | **1760** | 188 | 1770 |
| 464.h264ref | 16 | 913 | 388 | 924 | 383 | **914** | **387** | 16 | **900** | **393** | 903 | 392 | 900 | 393 |
| 471.omnetpp | 16 | 569 | 176 | **568** | **176** | 568 | 176 | 16 | **541** | **185** | 544 | 184 | 540 | 185 |
| 473.astar | 16 | **655** | **171** | 658 | 171 | 653 | 172 | 16 | **655** | **171** | 658 | 171 | 653 | 172 |
| 483.xalancbmk | 16 | **349** | **317** | 349 | 316 | 349 | 317 | 16 | **349** | **317** | 349 | 316 | 349 | 317 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
Large pages were not enabled for this run
```

## Platform Notes

```
Sysinfo program /home/rcaneca/cpu2006/Docs/sysinfo
$Rev: 6775 $ $Date:: 2011-08-16 #$ 8787f7622badcf24e01c368b1db4377c
running on itautecfam5 Sun Dec  4 17:23:56 2011

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Genuine Intel(R) CPU @ 2.40GHz
      1 "physical id"s (chips)
      16 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 8
      siblings  : 16
```

Continued on next page

## Platform Notes (Continued)

```
        physical 0: cores 0 1 2 3 4 5 6 7
     cache size : 20480 KB

  From /proc/meminfo
     MemTotal:       32851432 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  /usr/bin/lsb_release -d
     Red Hat Enterprise Linux Server release 6.2 (Santiago)

  From /etc/*release* /etc/*version*
     redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
     system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
     system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

  uname -a:
     Linux itautecfam5 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
     x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Dec 4 17:21

  SPEC is set to: /home/rcaneca/cpu2006
     Filesystem     Type    Size  Used Avail Use% Mounted on
     /dev/mapper/vg_itautecfam5-lv_home
                    ext4    280G  4.5G  262G   2% /home

  (End of data from sysinfo program)
```

## General Notes

```
This result was measured on the Servidor Itautec MX205.
The Servidor Itautec MX205, the Servidor Itautec MX225+ and the Servidor Itautec LX205
are electronically equivalent.
```

## Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

Continued on next page

## Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

## Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.html`
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.xml`
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml`


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